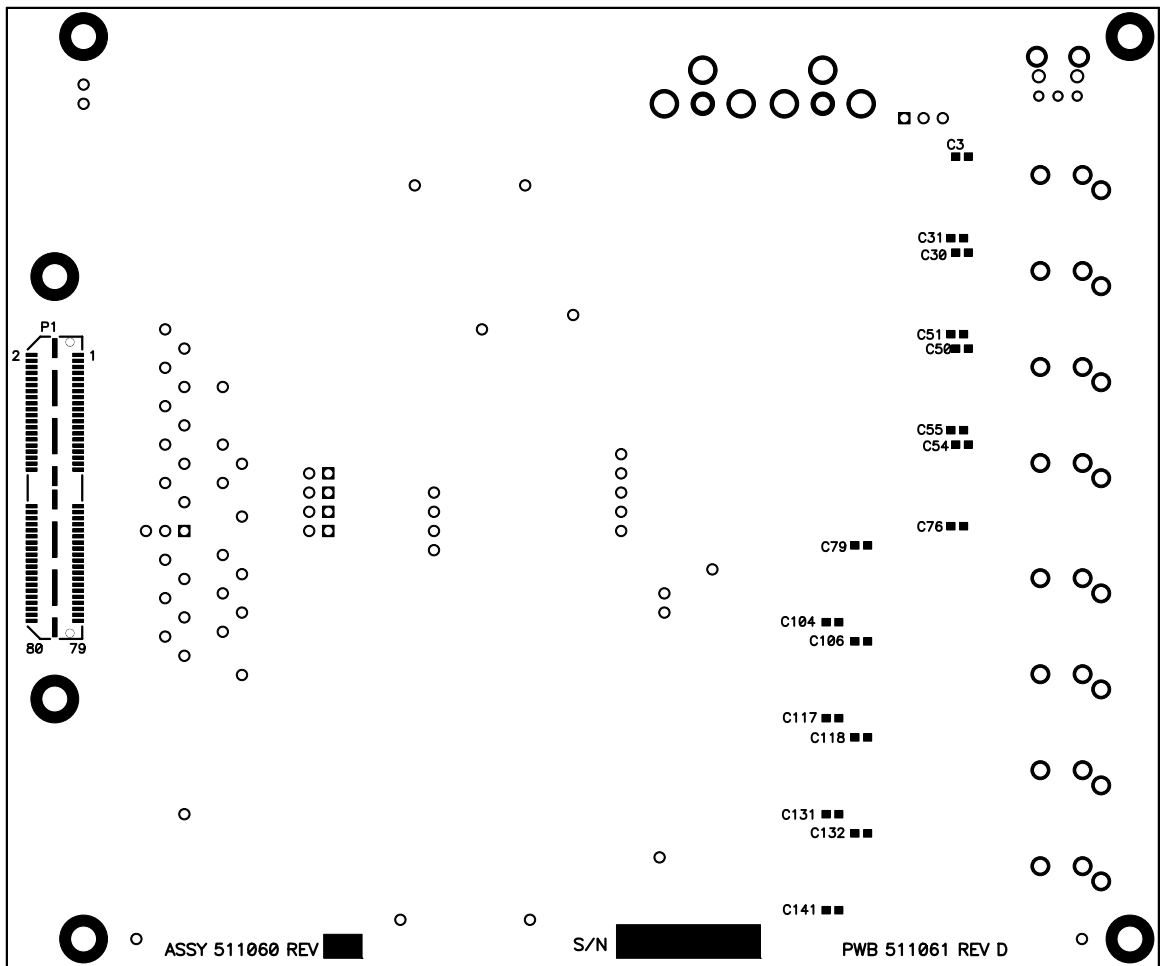
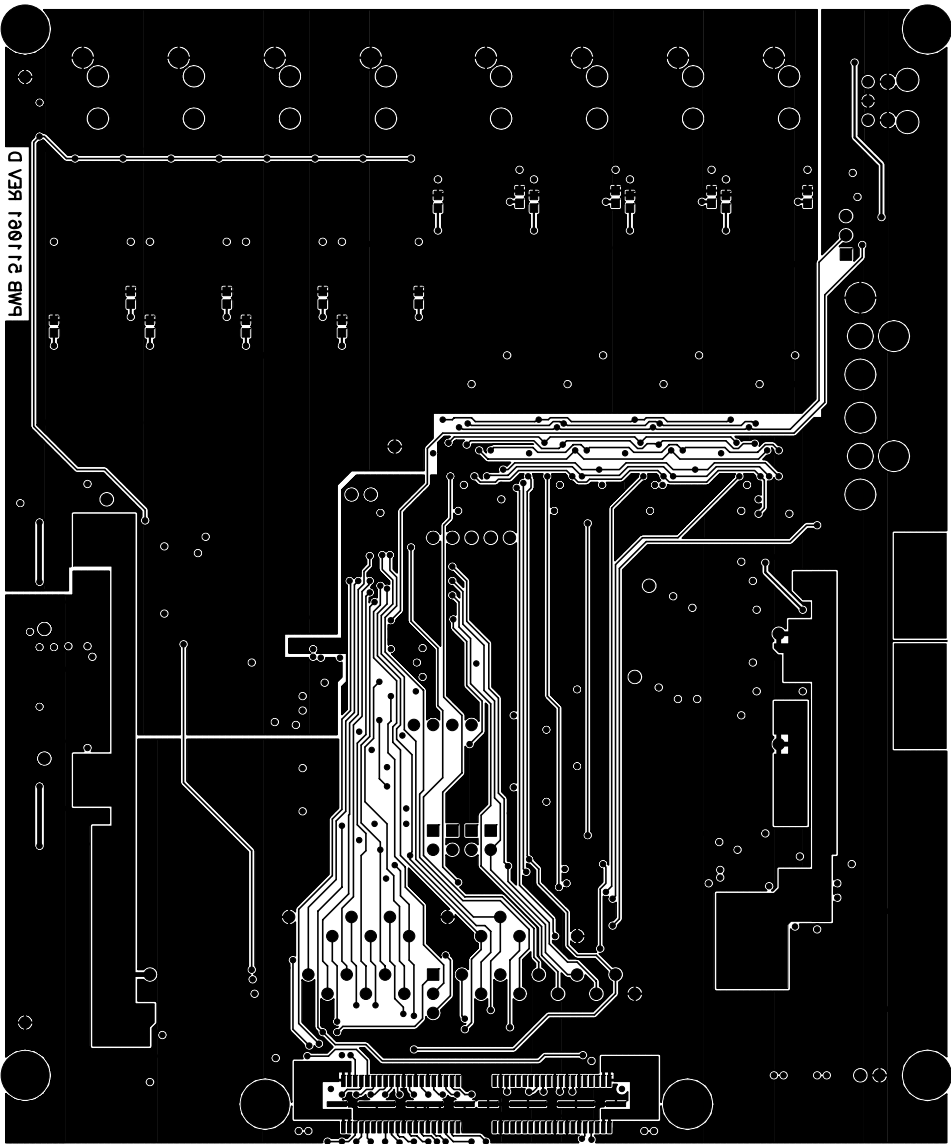


SPECTRUM
DIGITAL
INCORPORATED





PWB 511061 REV D
8-23-10
A REYAJ - 3DI2 MOTTOR

PWB 511061 REV D
8-23-10
MOTOROLA

2/2

VER 020112 Y22A

C11C

C11S
C11J

C11B
C11A

C10E
C10F



C13



C18

C24
C22

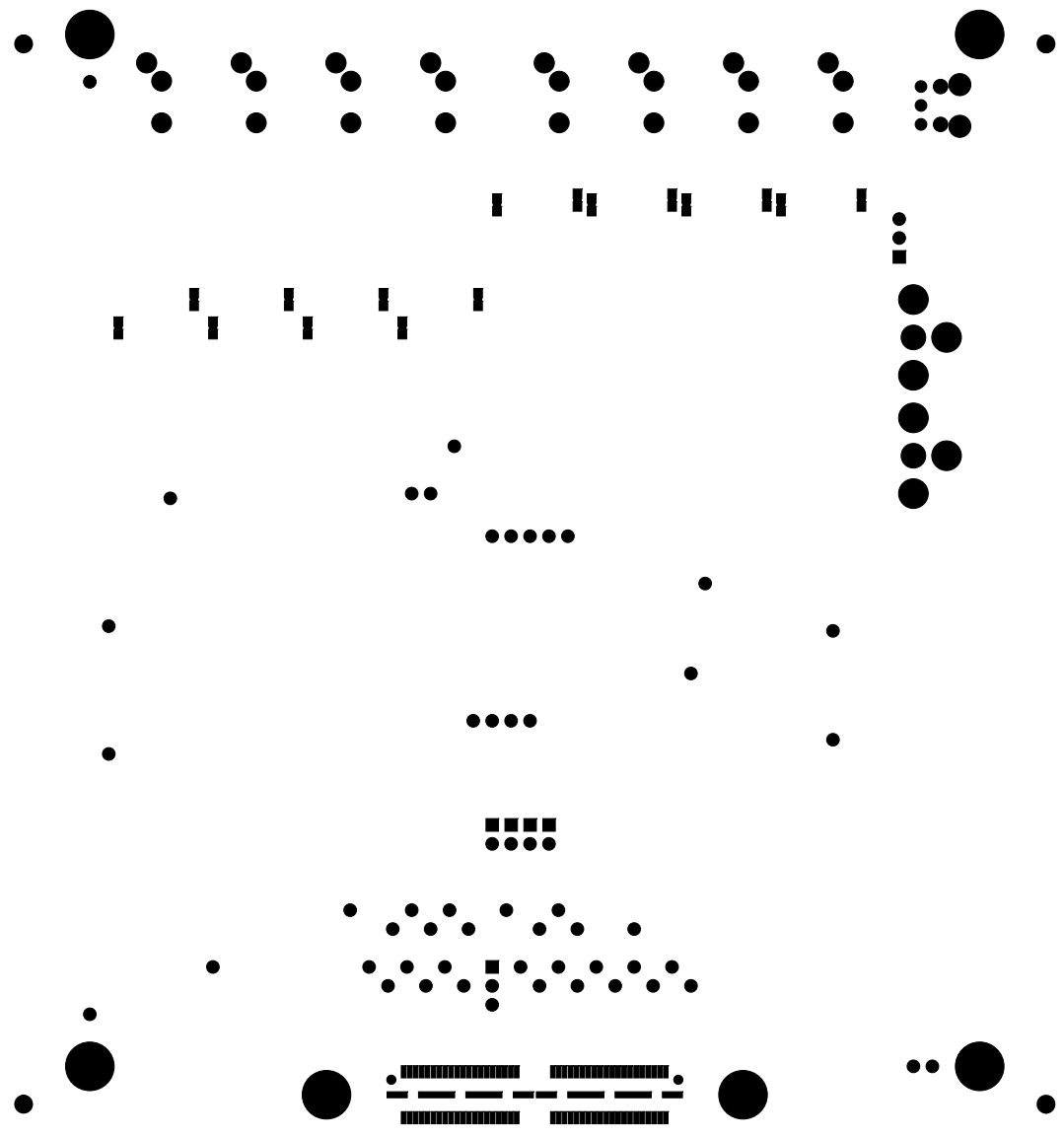


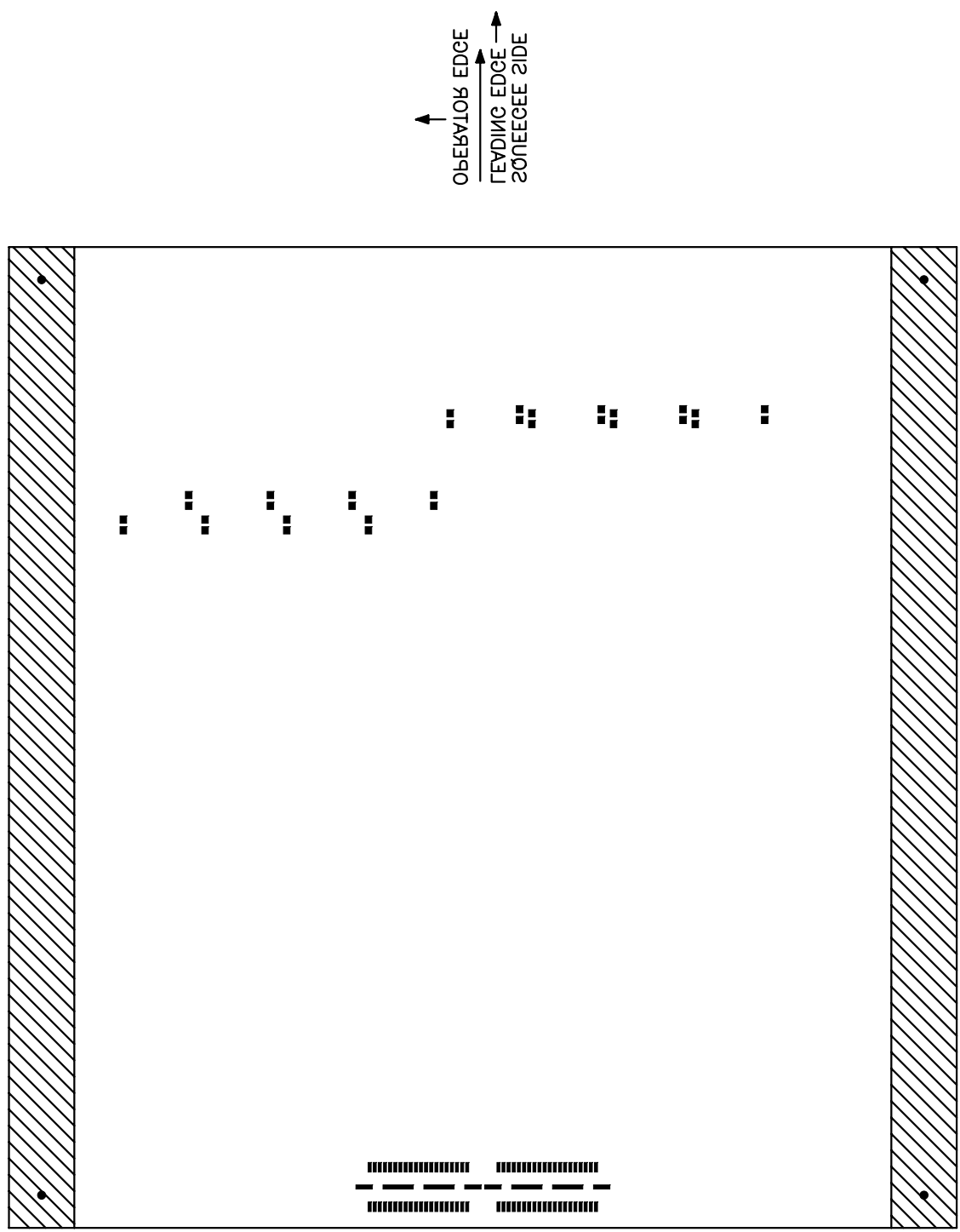
C20
C21

C30
C31

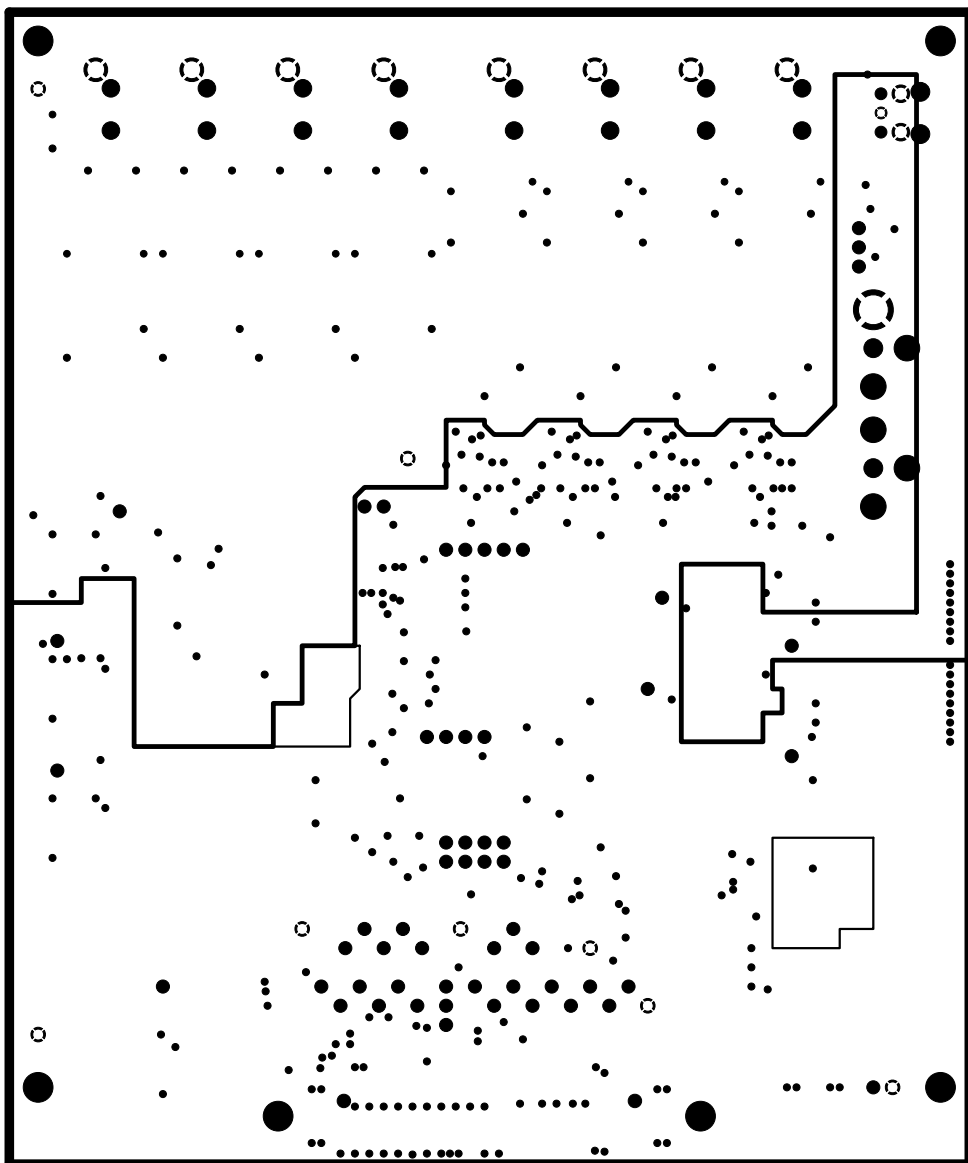
C3

PWB 511061 REV D
8-23-10
K2AMP9EQDJ02 2D12 MOTT0B

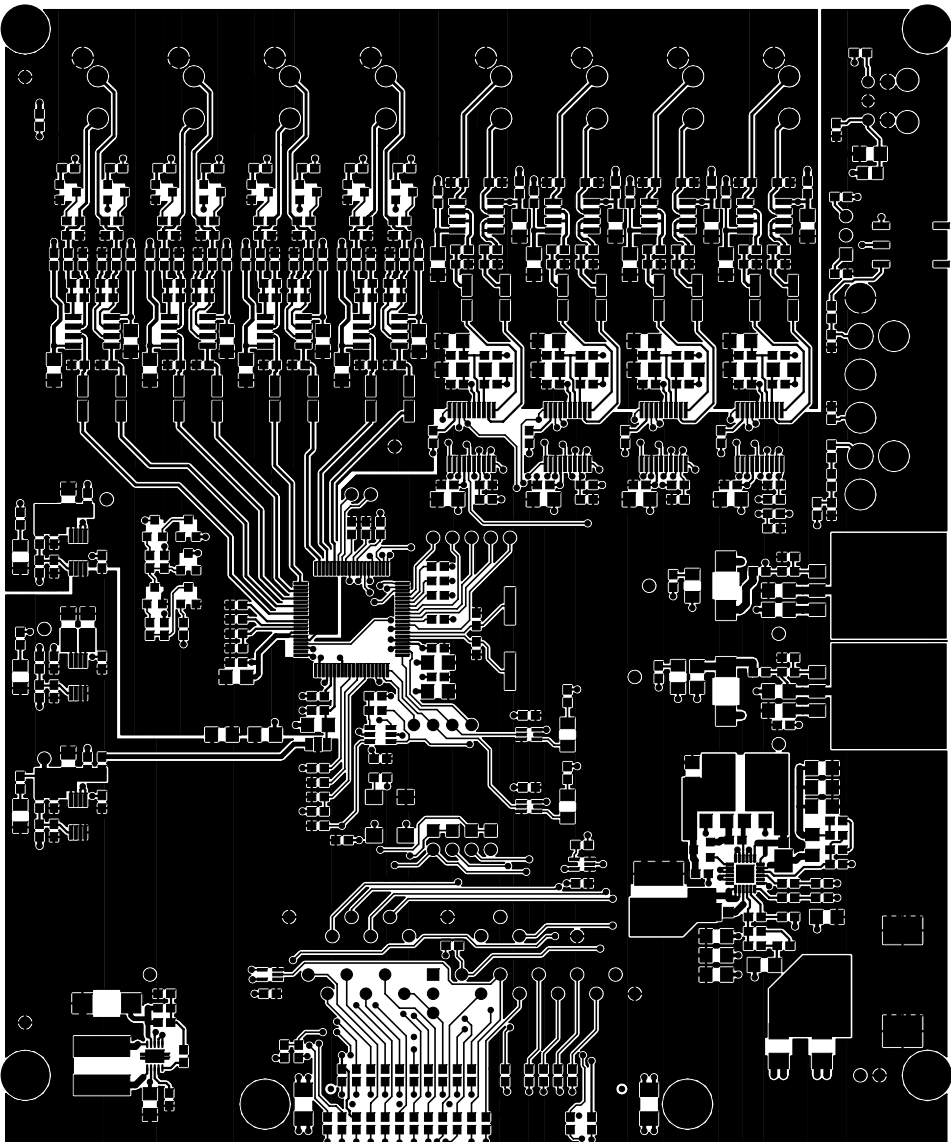




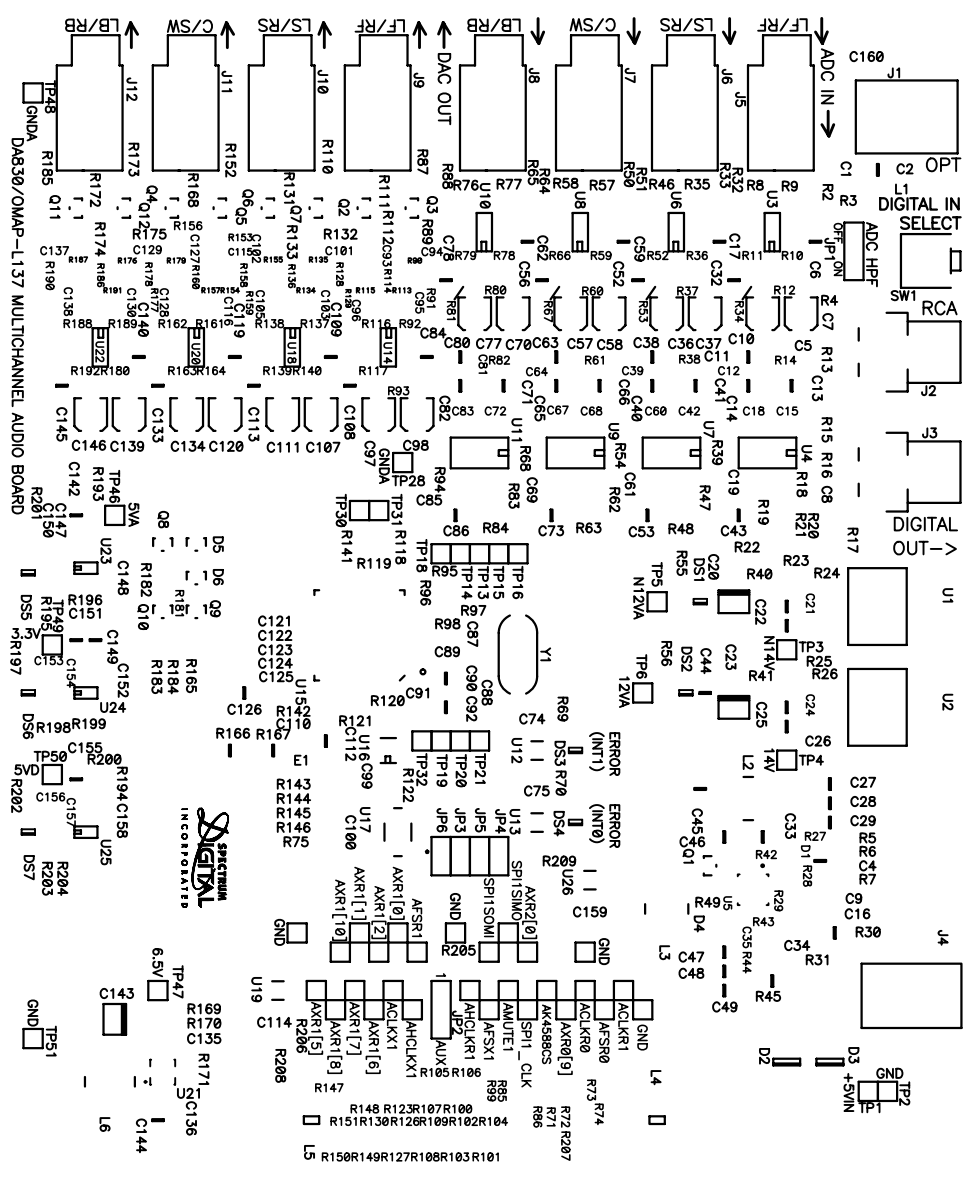
PWB 511061 REV D
8-23-10
ИТАРРАРДЛОС ЭДИР МОТТОВ



PWB 511061 REV D
8-23-10
GND PLANE - LAYER 2



PWB 511061 REV D
8-23-10
TOP SIDE - LAYER 1

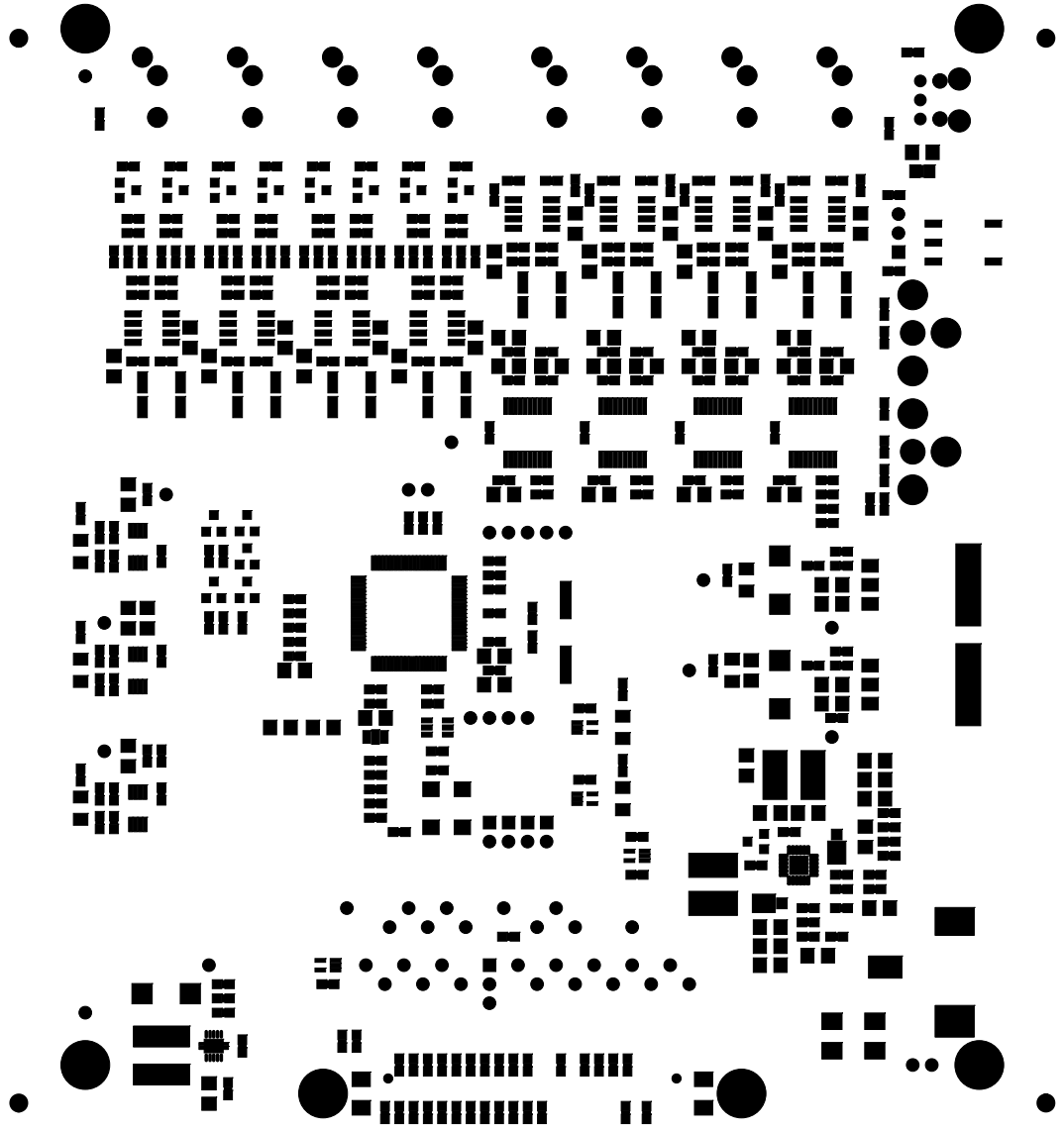


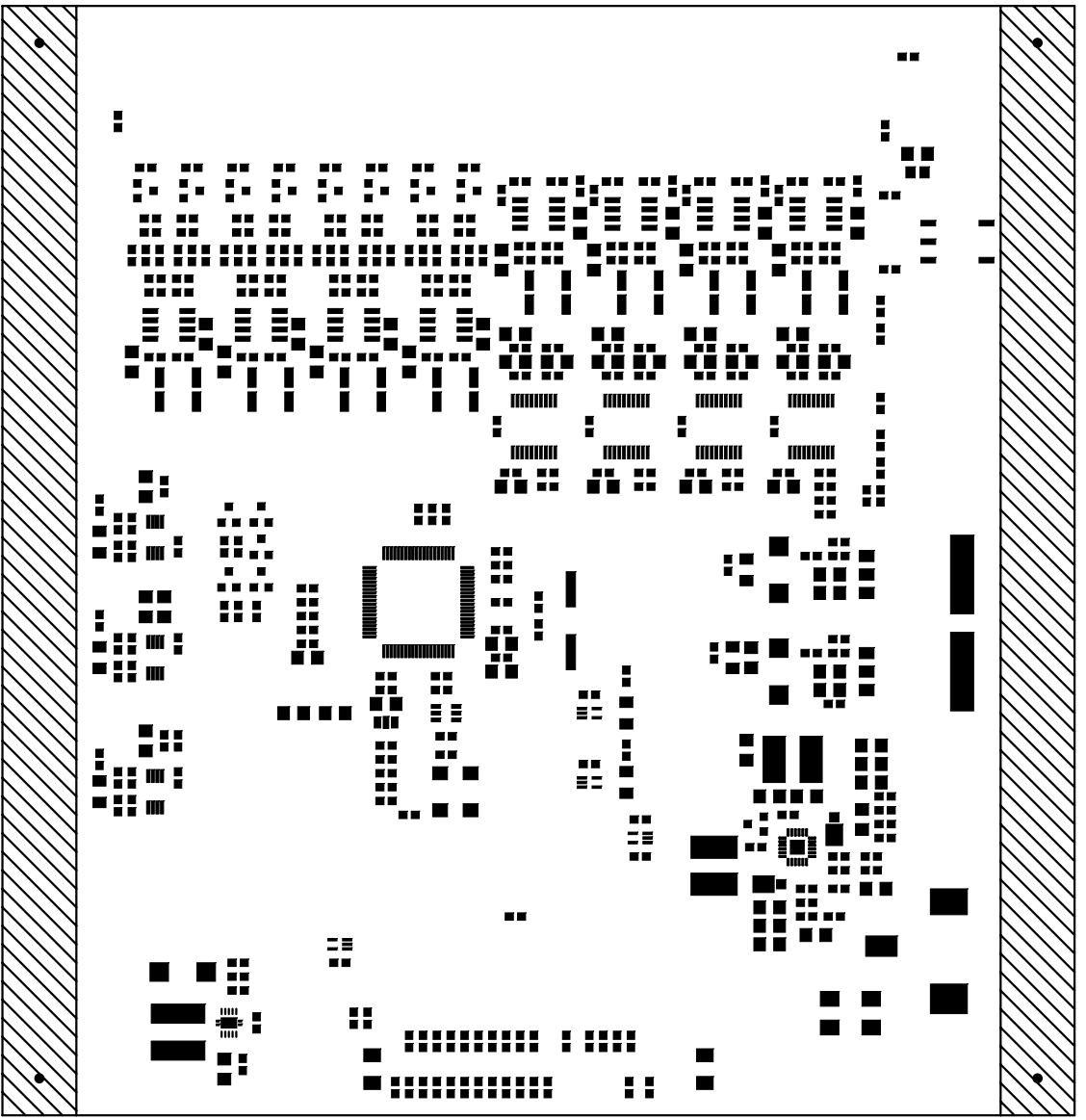
PWB 511061 REV D
 8-23-10
 TOP SIDE SILKSCREEN



TP47 6.5V
 TP48 GND
 TP49 5V
 TP50 5V
 TP51 GND

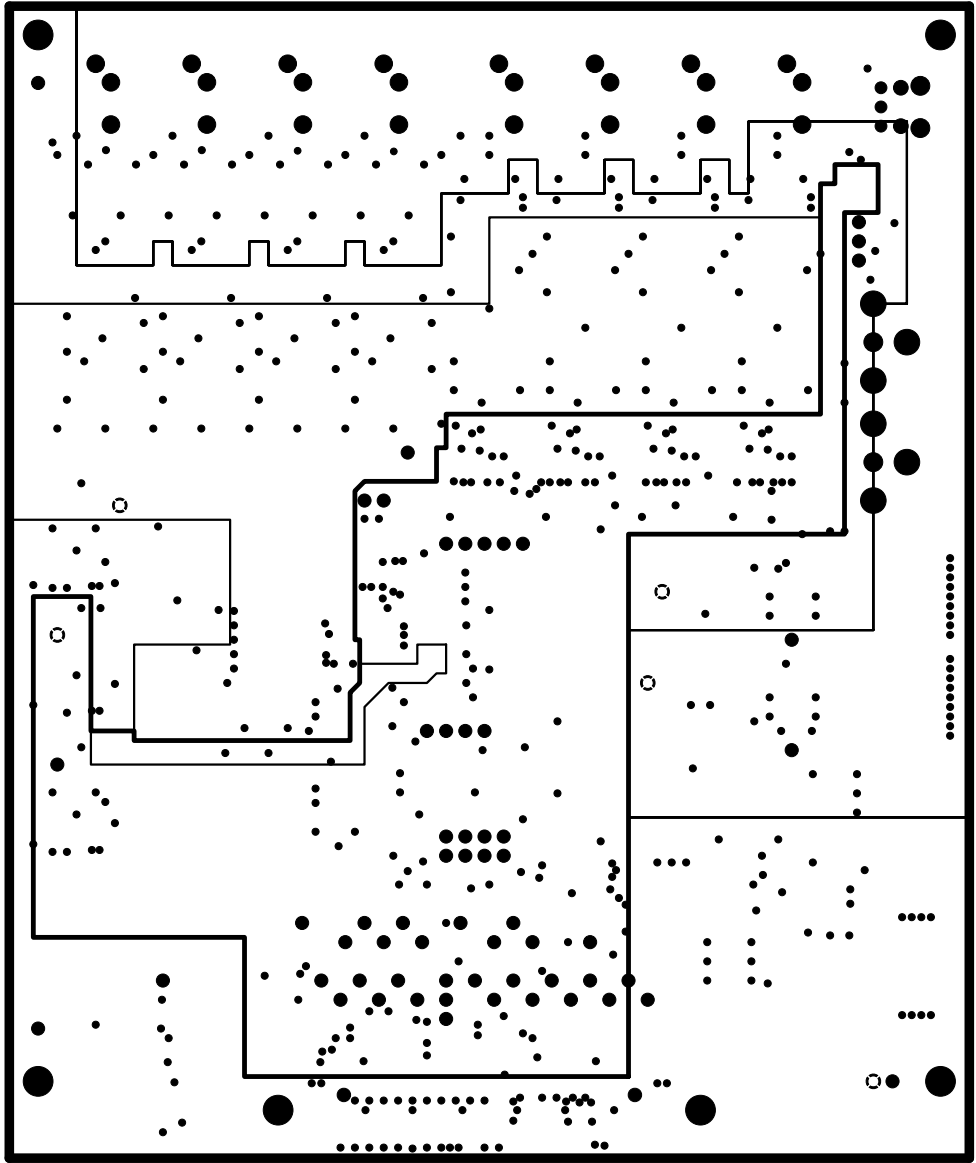
PWB 511061 REV D
8-23-10
TOP SIDE SOLDERMASK





PWB 511061 REV D
8-23-10
TOP SIDE SOLDERPASTE

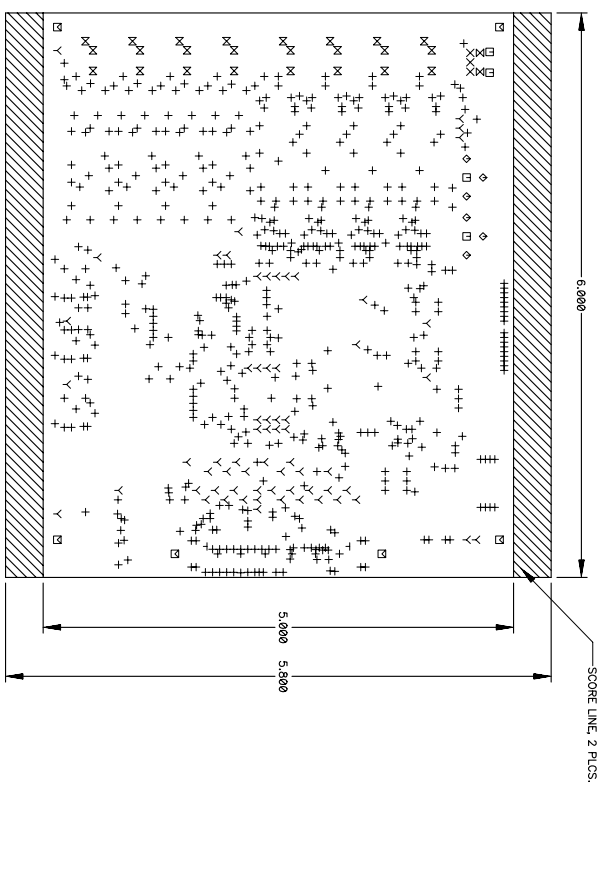
SQUEEGEE SIDE
LEADING EDGE →
↑
OPERATOR EDGE



PWB 511061 REV D
8-23-10
VCC PLANE - LAYER 3

- NOTES: UNLESS OTHERWISE SPECIFIED**
- 1 MATERIAL: HIGH TEMP FR4 (G>=170). THICKNESS: .062 ;
 - 2 DIMENSIONS APPLY TO THE CENTER OF HOLES AND PADS.
 - 3 MINIMUM COPPER WALL THICKNESS SHALL BE .001 FOR ALL PLATED THRU HOLES.
 - 4 ALL artwork VIEWED FROM COMPONENT SIDE ARE AFTER PLATING.
 - 5 MAXIMUM ALLOWABLE LINE REDUCTIONS DUE TO PIN HOLES, NICKS AND LINE SHRINKAGE IS TO BE 20%.
 - 7 INTERNAL AND EXTERNAL CORNERS TO BE .508 RADIUS MAX AT SHOP OPTION.
 - 8 EDGE CONNECTOR TABS ARE TO BE ELECTROPLATED TO A MINIMUM THICKNESS OF .00005 GOLD OVER A MINIMUM THICKNESS OF .0001 NICKEL.
 - 9 SOLDER MASK SHALL BE LIQUID PHOTOIMAGEABLE MATTE FINISH WITH A THICKNESS NOT TO EXCEED 1.5 MILS AND SHALL BE APPLIED TO BOTH SIDES OF BOARD OVER BARE COPPER PER IPC-SM-840, CLASS 2.
 - 10 SOLDER MASK COLOR: GREEN.
 - 11 EXPOSED CONDUCTOR AREAS TO BE FINISHED WITH ELECTROLESS NICKEL THICKNESS .000250 TO MINIMUM PLUS IMMERSION GOLD THICKNESS .000205 MINIMUM.

- 12 SILKSCREEN WITH WHITE EPOXY BASED INK PER SILKSCREEN ARTWORK.
- 13 BOARD IS TO BE 100% ELECTRICALLY TESTED.
- 14 CONTROLLED IMPEDANCE OF 55 OHMS +/- 10 OHMS TO BE MATCHED ON ALL SIGNAL LAYERS ON 8 MIL TRACES.



Drill Table

Hole Dia (Inch)	Symbol	Quantity	Plated
0.012	+	599	Yes
0.031	X	3	Yes
0.038	Y	65	Yes
0.041	T	2	No
0.046	X	2	Yes
0.062	M	24	Yes
0.068	□	4	Yes
0.104	◇	6	Yes
0.125	⊠	6	Yes

TABLE OF LAYERS

NO.	DESCRIPTION	COPPER WT.
1.1	TOP SIDE	1/2 OZ
1.2	GND PLANE	1.0Z
1.3	VCC PLANE	1.0Z
1.4	BOTTOM SIDE	1/2 OZ

PWB 511061 REV D
8-23-10
FAB DRAWING

- UNLESS OTHERWISE SPECIFIED**
- DIMENSIONS ARE IN INCHES
 - ANGLES +/- .1°
 - TOLERANCES 2 PLACE DECIMALS +/- .02
 - REMOVE ALL BURRS AND SHARP EDGES
 - CONCENTRIC MACHINED DIAMETERS 9/10 RIM
 - DIMENSIONAL LIMITS APPLY AFTER PROCESSES
- HOLE DIAMETER TOLERANCE UNLESS OTHERWISE NOTED IN DRILL TABLE
ALL NON-PLATED HOLES +/- .004
ALL PLATED HOLES +/- .003

DATE	8-23-10
DESIGNED BY	RON PETERSON
ENGINEER	ENG-MGR
QA	
MFG	
RISE	

SPECTRUM DIGITAL
INCORPORATED

PWB DA830 MULTI-CHANNEL
AUDIO BOARD

SIZE DRAWING NO 511061
SCALE NONE

REV D
SHEET 1 OF 1

REV	ECN NO / CLASS	DRAFTER / DATE	DATE	APPROVED

DWG NO	511061	SH 1	1
REVISIONS			