

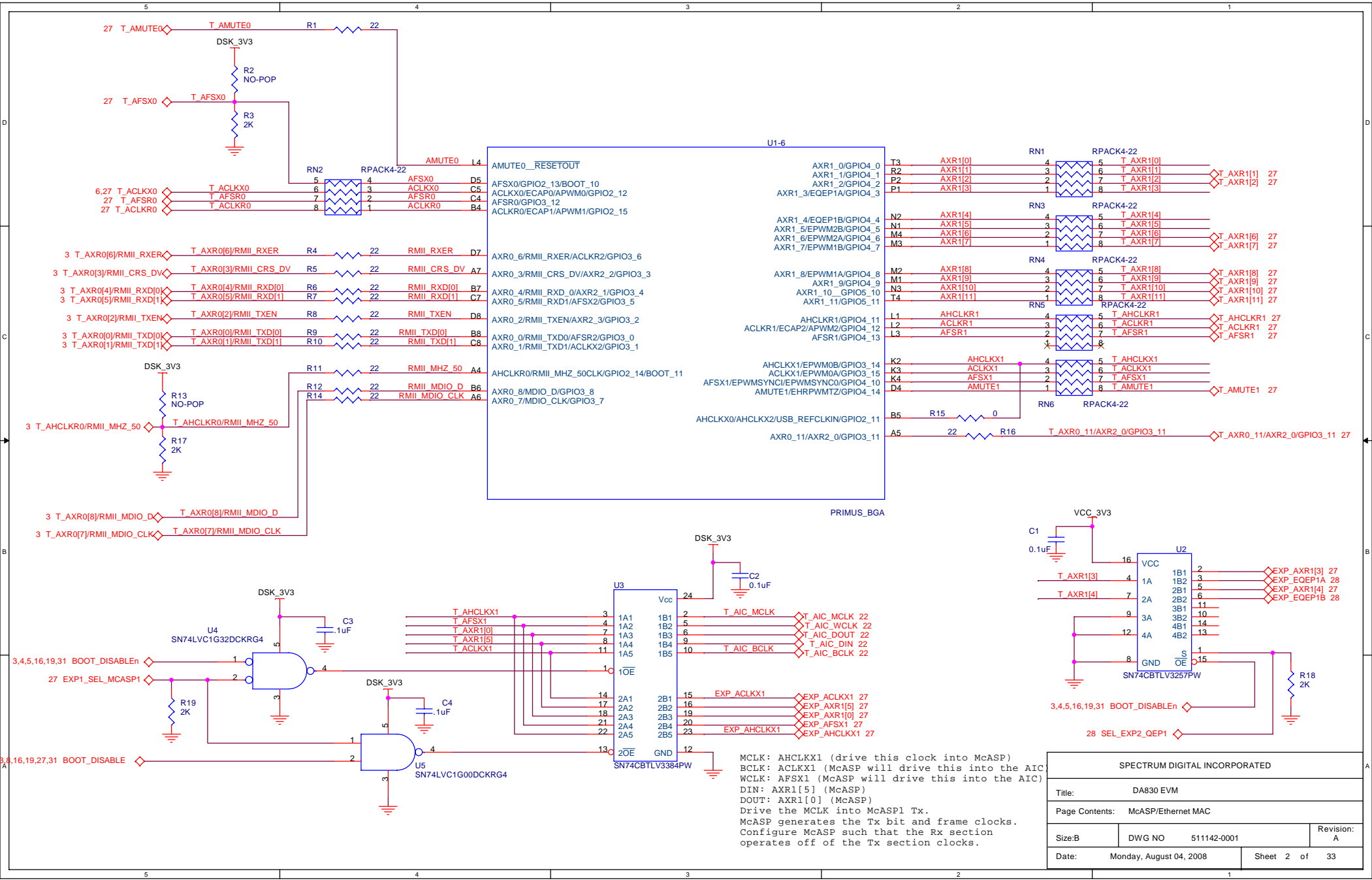
SCHMATIC CONTENTS

- SHEET01 - TITLE PAGE
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- SHEET03 - ETHERNET/McASP MUX
- SHEET04 - DSP SERIAL I/O
- SHEET05 - SPI MUXING
- SHEET06 - DSP USB 2.0 PORT
- SHEET07 - DSP USB 1.0 PORT
- SHEET08 - DSP EMIF A
- SHEET09 - DSP EMIF B
- SHEET10 - DSP JTAG/CLOCKS
- SHEET11 - DSP POWER PINS
- SHEET12 - DSP GROUND PINS
- SHEET13 - DSP DECOUPLING CAPACITORS
- SHEET14 - BOOT MODE SWITCHES
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- SHEET29 - POWER SUPPLIES
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REVISION STATUS OF SHEETS

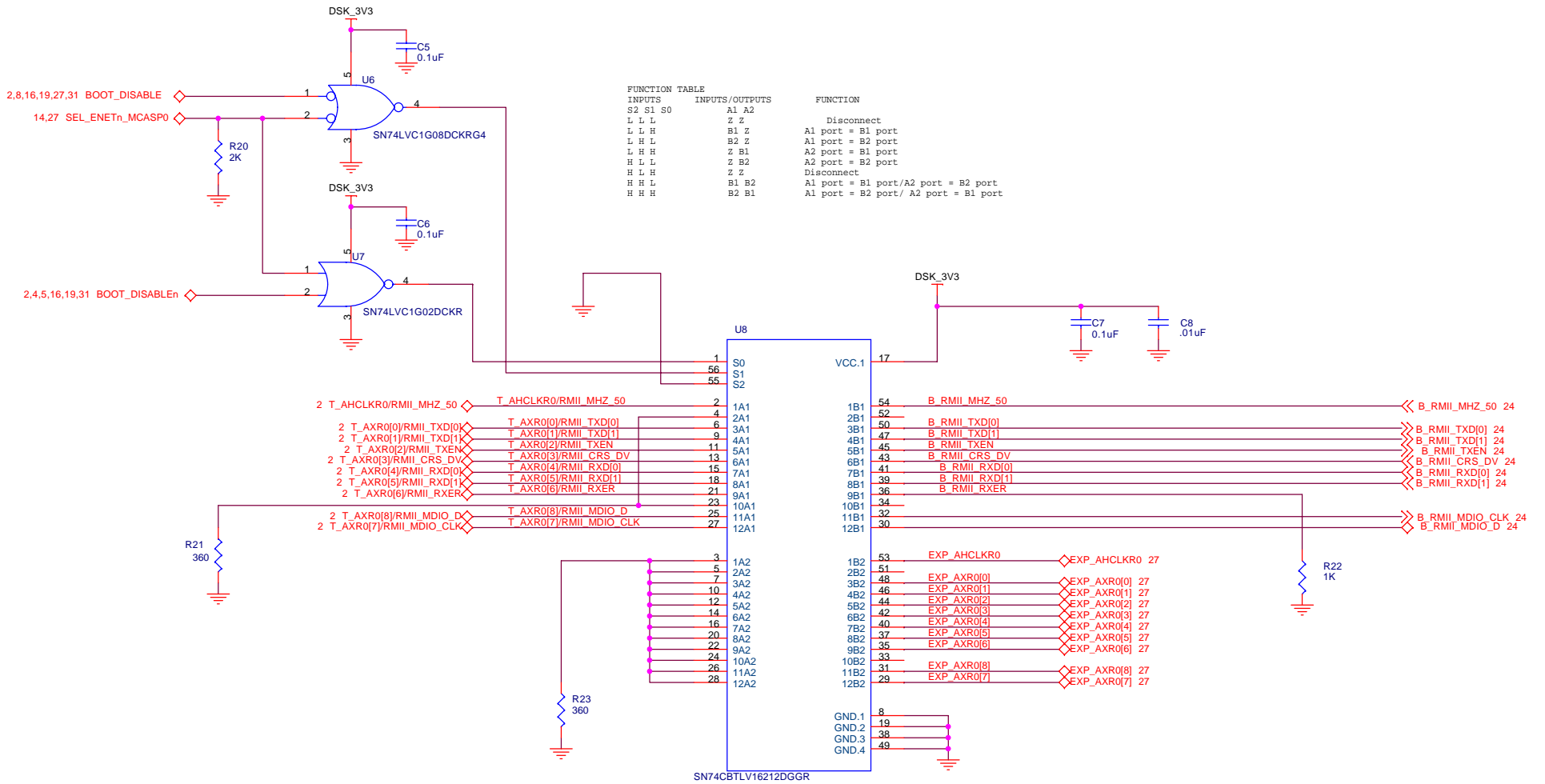
REV	1	2	3	4	5	6	7	8	9	10	APPLICATION	USED ON	DATE	BY		
														DWN	R.R.P.	06/01/2008
														CHK	T.W.K.	06/01/2008
SH														ENGR	R.R.P.	12/01/2006
REV	A	A	A	A	A	A								ENGR-MGR	R.R.P.	06/01/2008
SH	11	12	13	14	15	16								QA	C.M.D.	06/01/2008
REV	B	B	B	B	B	A	A	A	A	A	NEXT ASSY	USED ON		MFG	R.R.P.	06/01/2008
SH	1	2	3	4	5	6	7	8	9	10	APPLICATION			RLSE	R.R.P.	06/01/2008

SPECTRUM DIGITAL INCORPORATED			
Title: DA830 EVM			
Page Contents: TITLE PAGE			
Size: B	DWG NO	511142-0001	Revision: A
Date: Monday, August 04, 2008		Sheet 1 of 33	

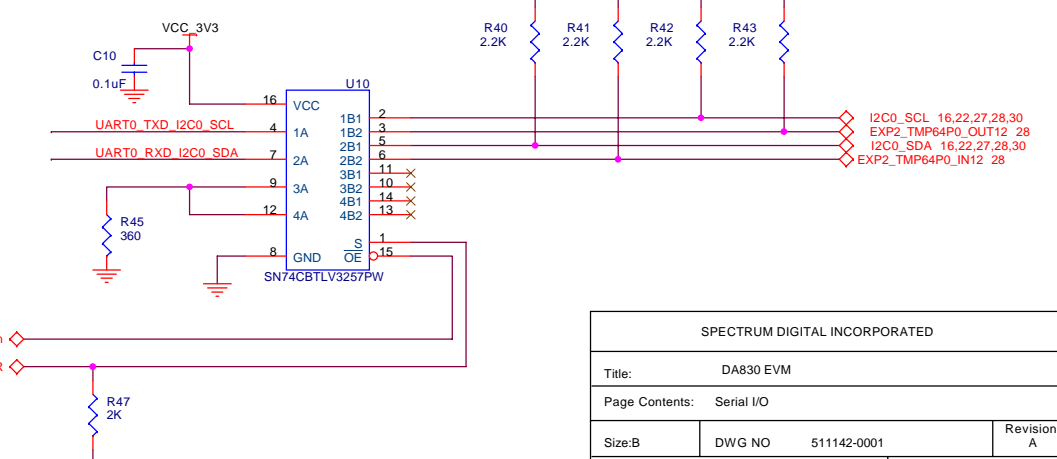
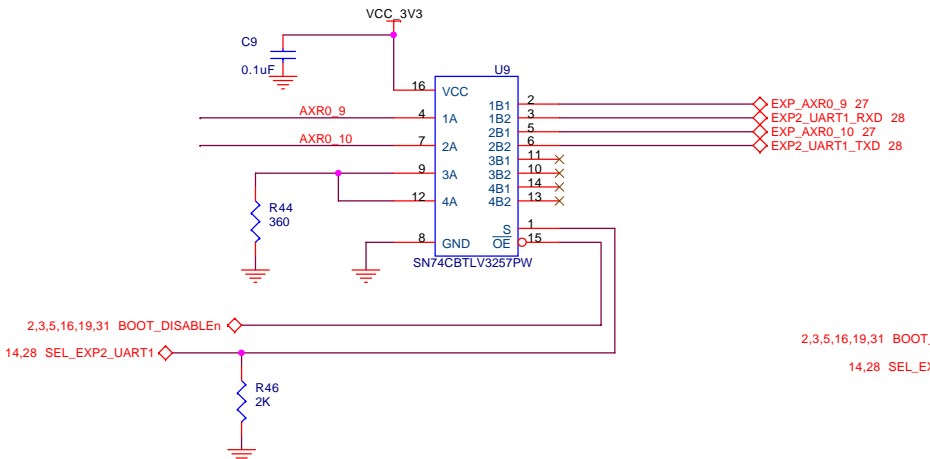
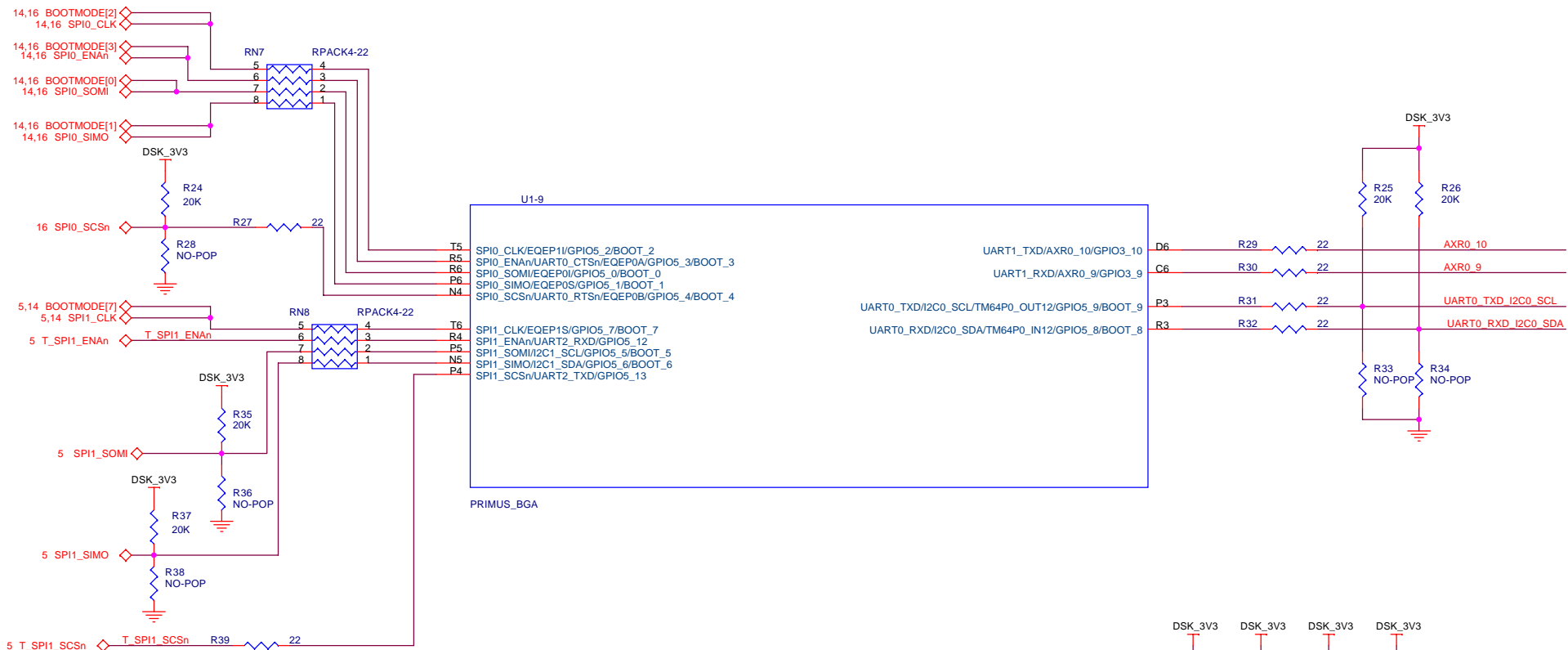


MCLK: AHCLKX1 (drive this clock into McASP)
 BCLK: ACLKX1 (McASP will drive this into the AIC)
 WCLK: AFSX1 (McASP will drive this into the AIC)
 DIN: AXR1[5] (McASP)
 DOUT: AXR1[0] (McASP)
 McASP generates the Tx bit and frame clocks.
 Configure McASP such that the Rx section operates off of the Tx section clocks.

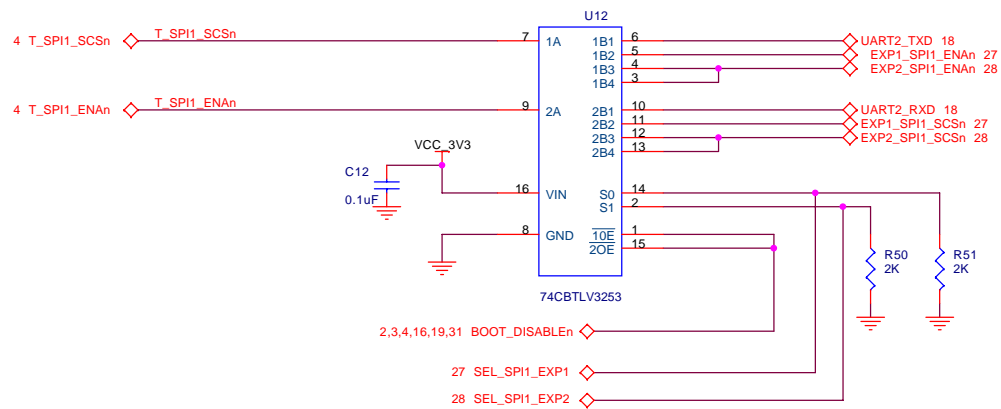
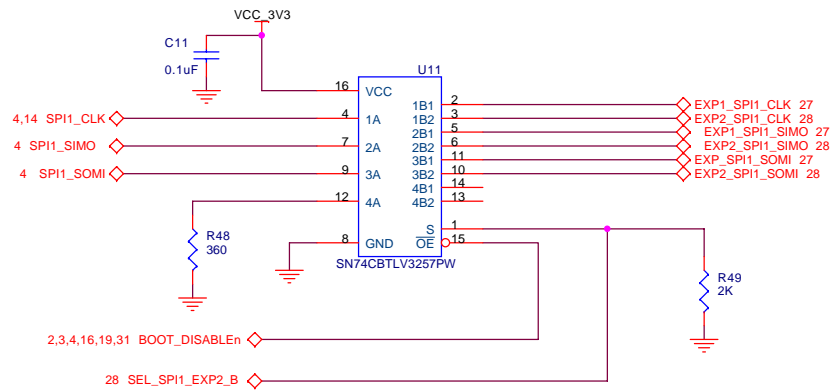
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Page Contents: McASP/Ethernet MAC			
Size: B	DWG NO	511142-0001	Revision: A
Date:	Monday, August 04, 2008	Sheet 2 of	33



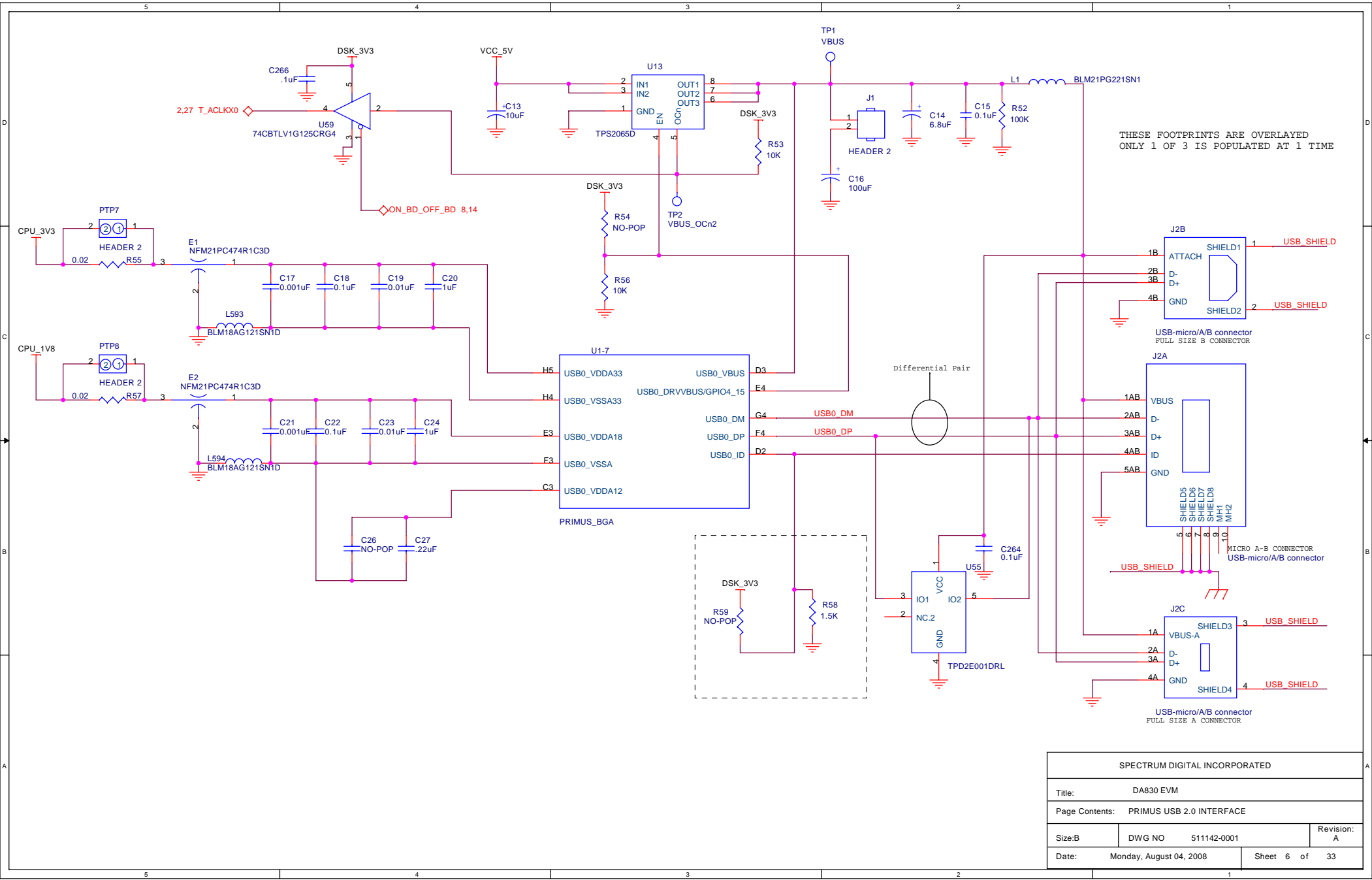
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Title:		DA830 EVM	
Page Contents:		Ethernet Muxes	
Size: B	DWG NO	511142-0001	Revision: A
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SPECTRUM DIGITAL INCORPORATED			
Title: DA830 EVM			
Page Contents: Serial I/O			
Size: B	DWG NO	511142-0001	Revision: A
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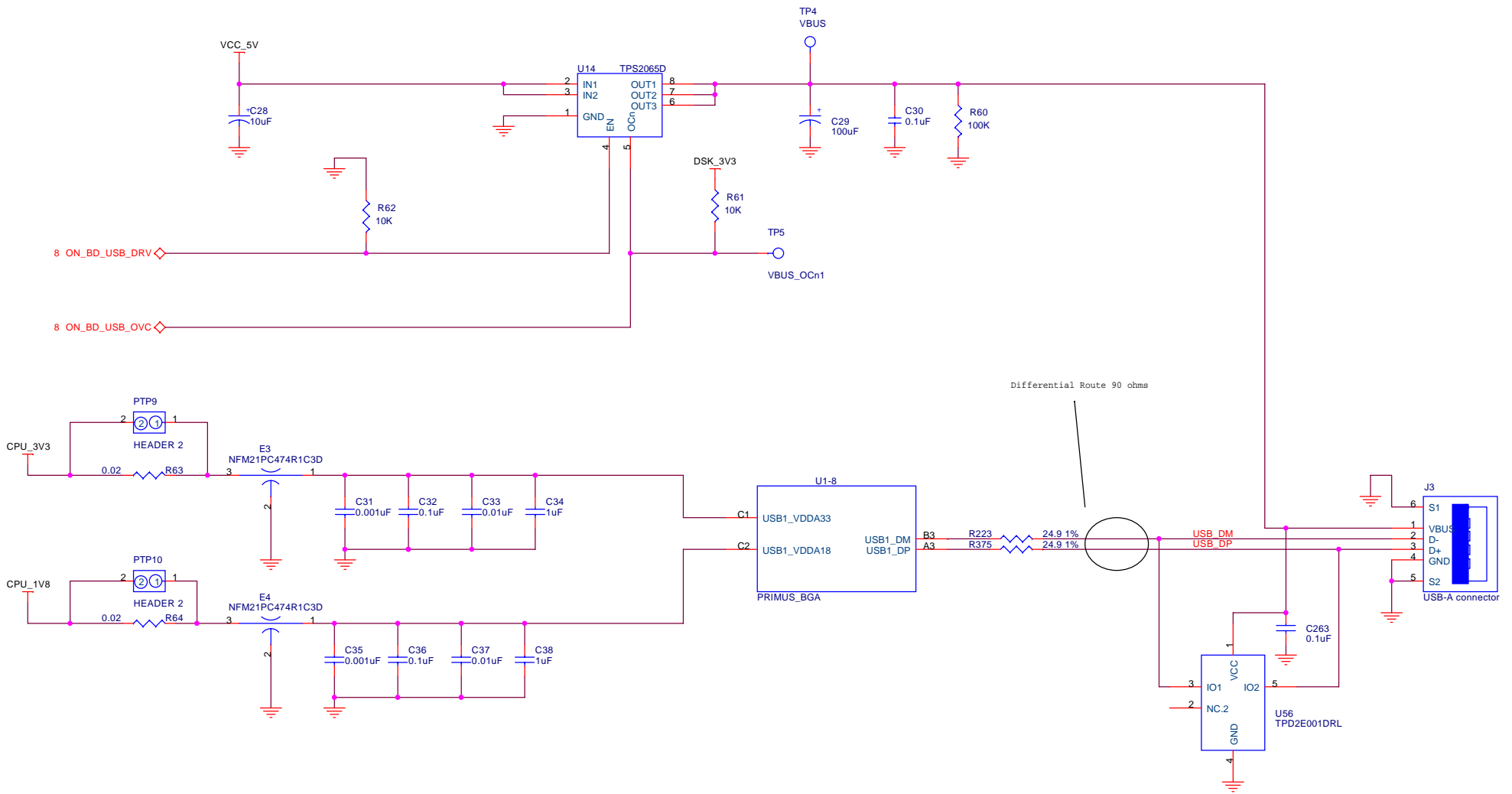


SPECTRUM DIGITAL INCORPORATED			
Title:		DA830 EVM	
Page Contents: SPI MUXING			
Size: B	DWG NO	511142-0001	Revision: A
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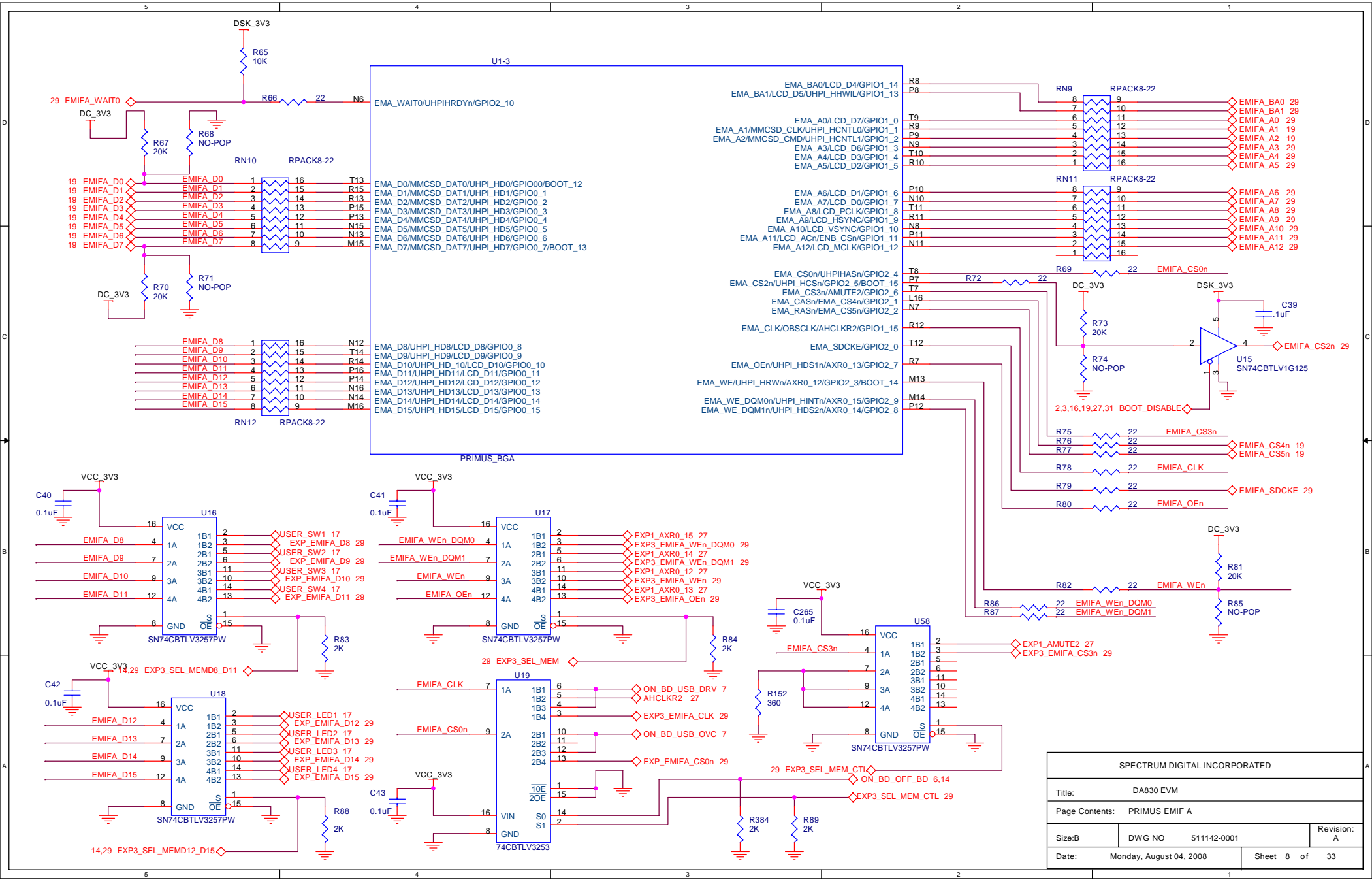


THESE FOOTPRINTS ARE OVERLAYED ONLY 1 OF 3 IS POPULATED AT 1 TIME

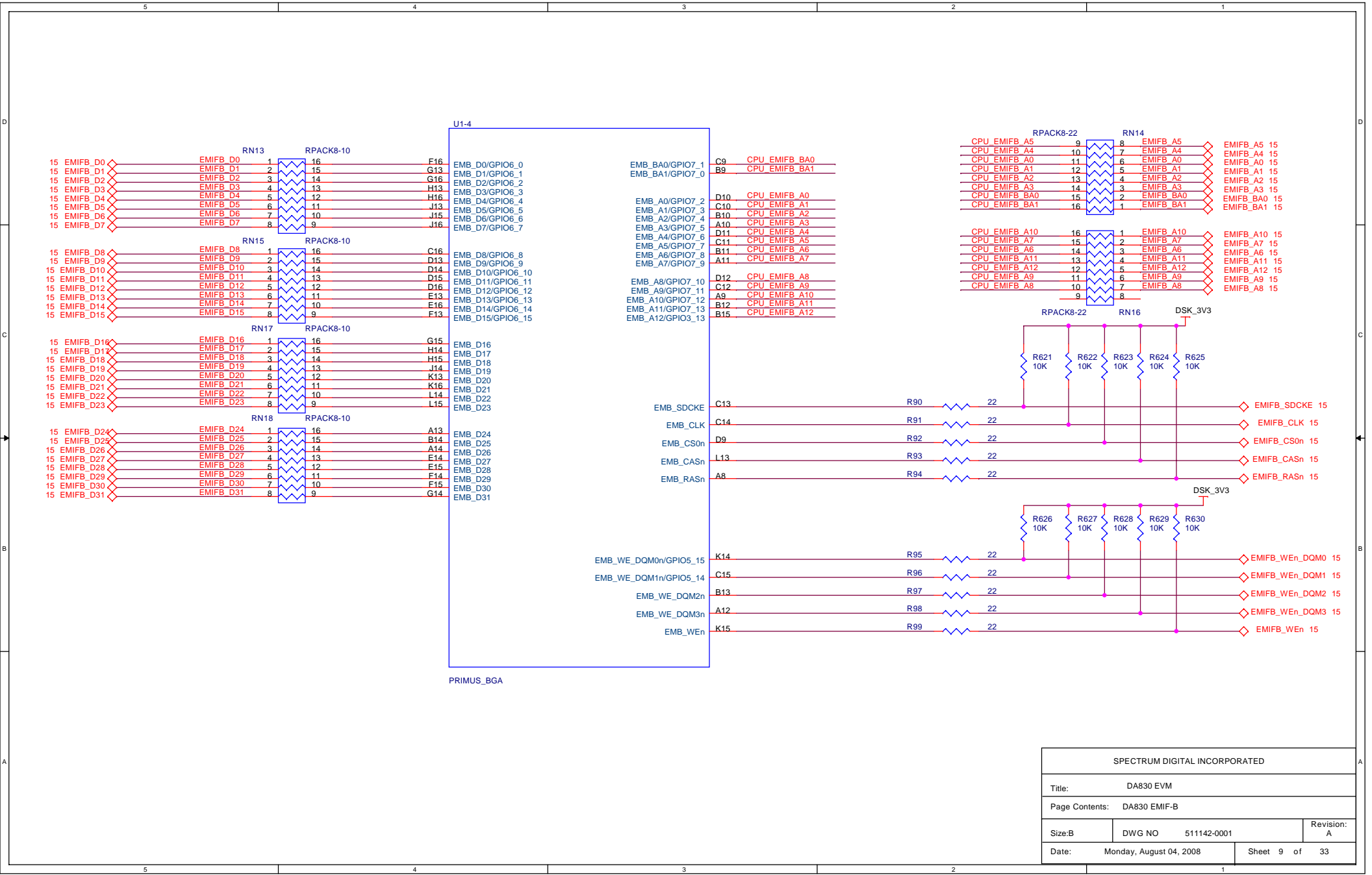
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Title:		DA830 EVM	
Page Contents: PRIMUS USB 2.0 INTERFACE			
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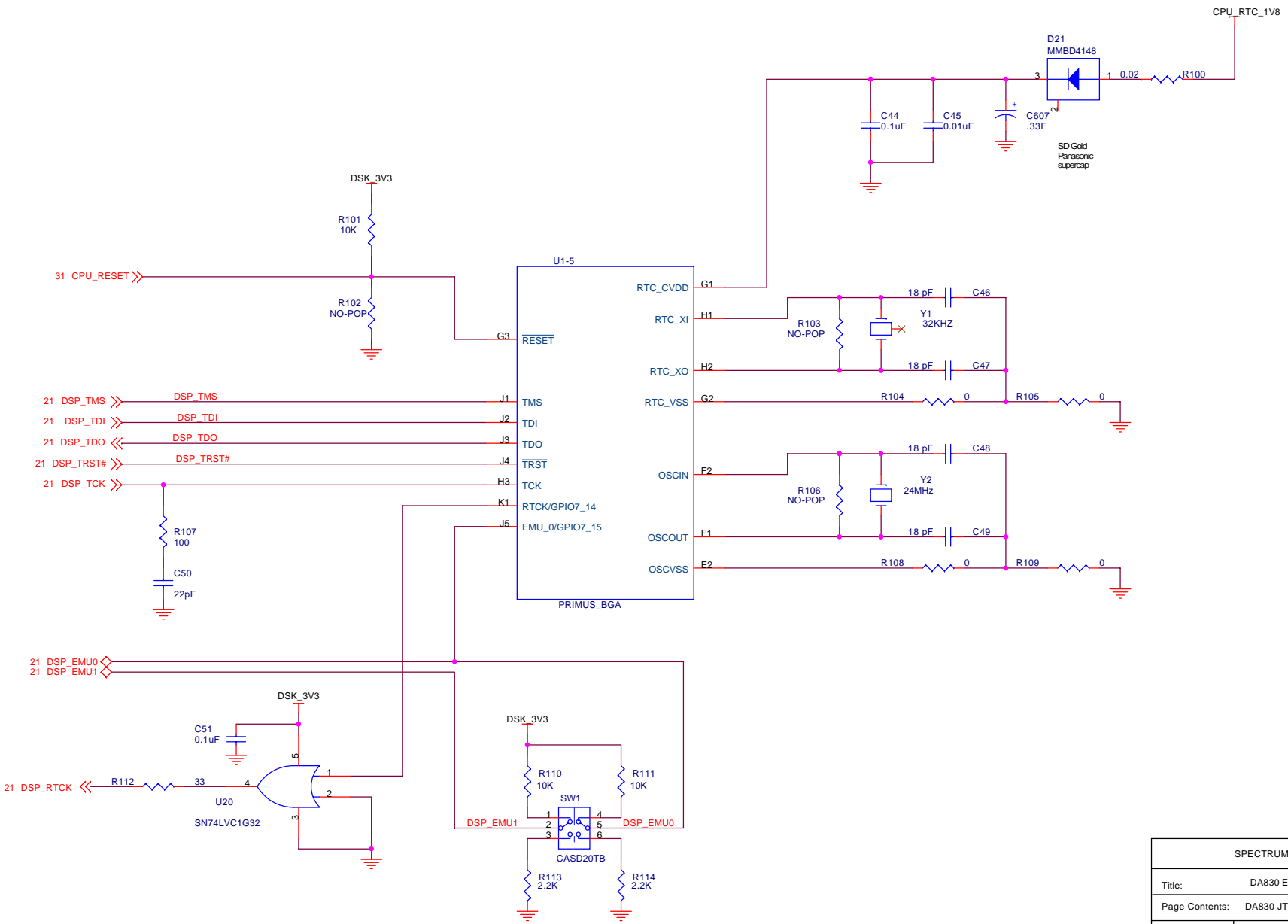
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Page Contents: USB 1.0 HOST INTERFACE			
Size: B	DWG NO	511142-0001	Revision: A
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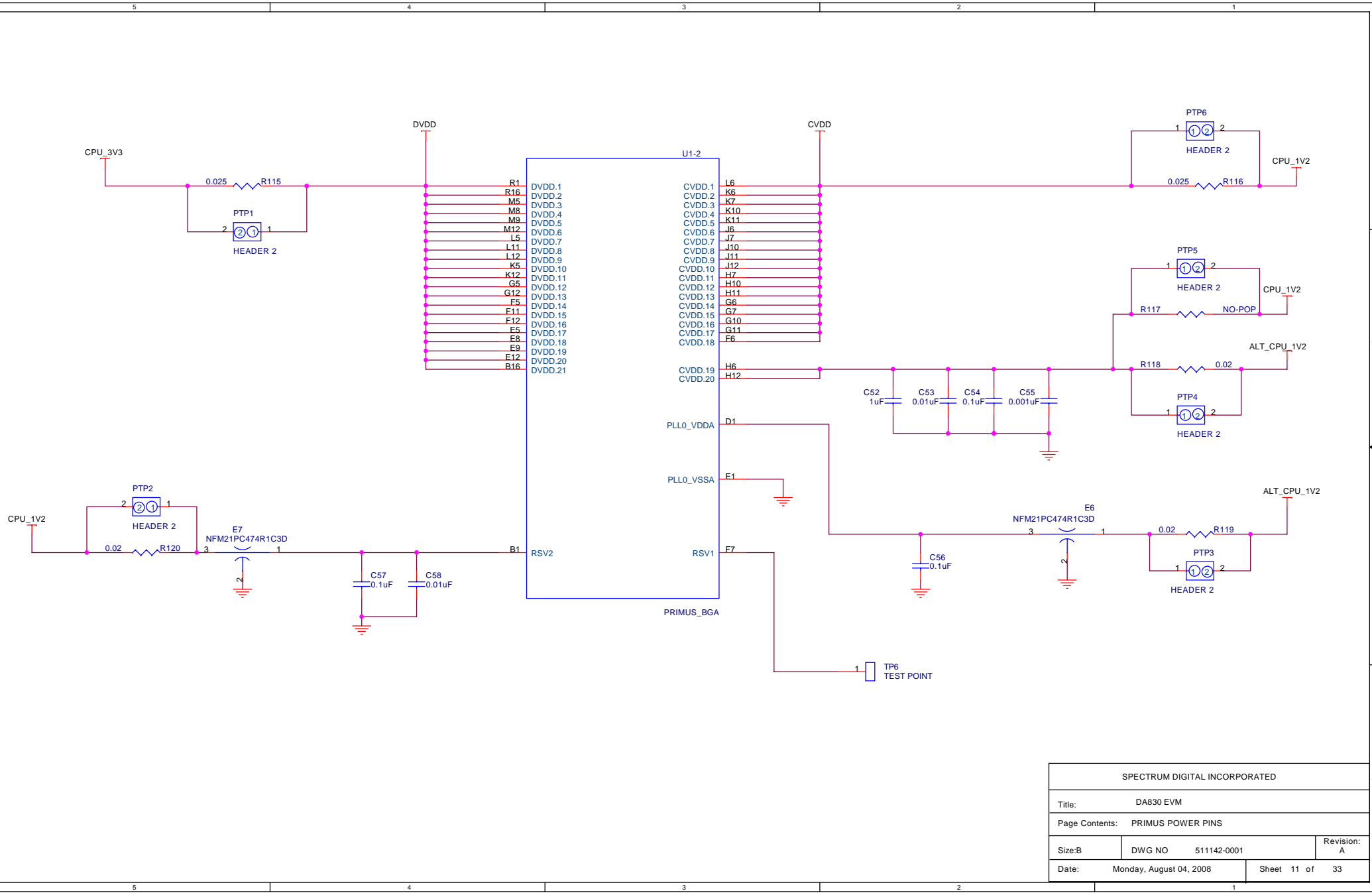
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Page Contents: PRIMUS EMIF A			
Size: B	DWG NO: 511142-0001	Revision: A	
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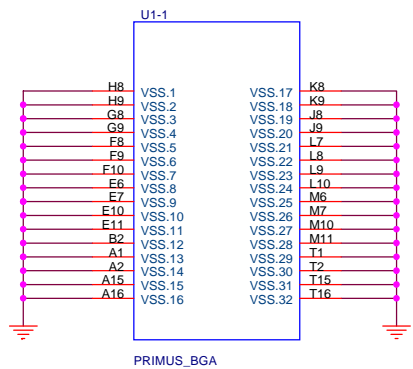
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Page Contents: DA830 EMIF-B			
Size: B	DWG NO	511142-0001	Revision: A
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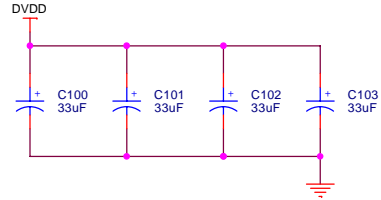
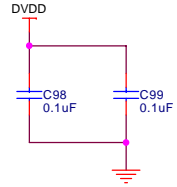
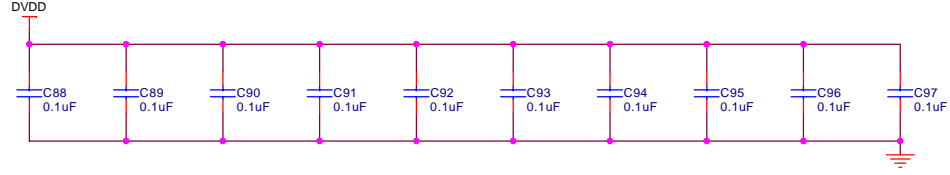
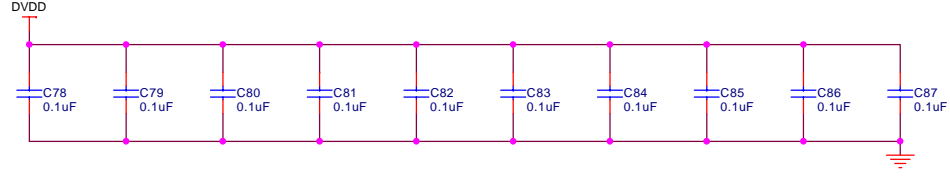
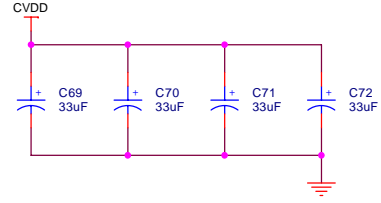
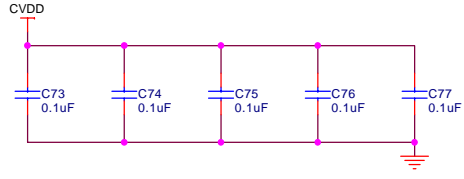
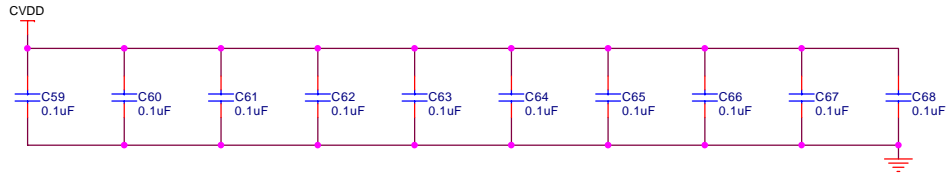
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Page Contents: DA830 JTAG/CLOCKS			
Size: B	DWG NO	511142-0001	Revision: A
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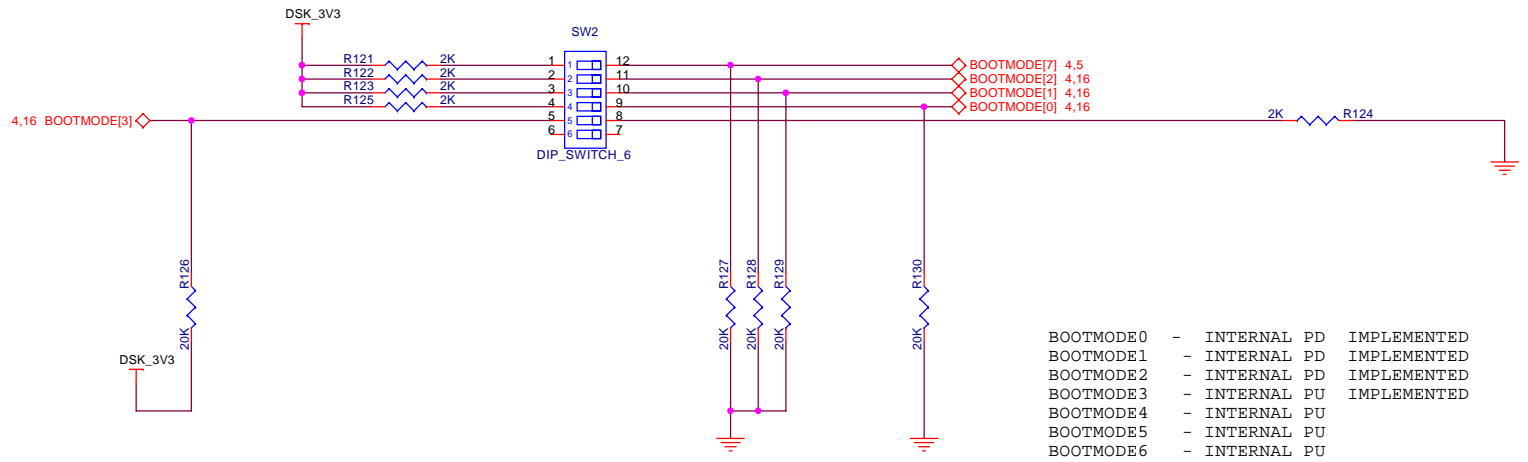
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Page Contents: PRIMUS POWER PINS			
Size: B	DWG NO	511142-0001	Revision: A
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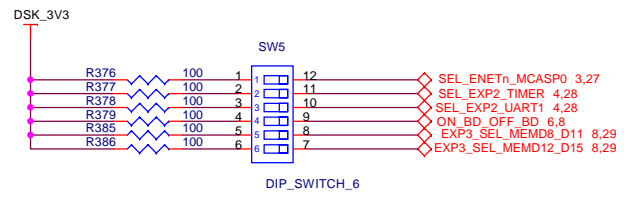
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Page Contents: PRIMUS GROUND PINS		
Size: B	DWG NO 511142-0001	Revision: A
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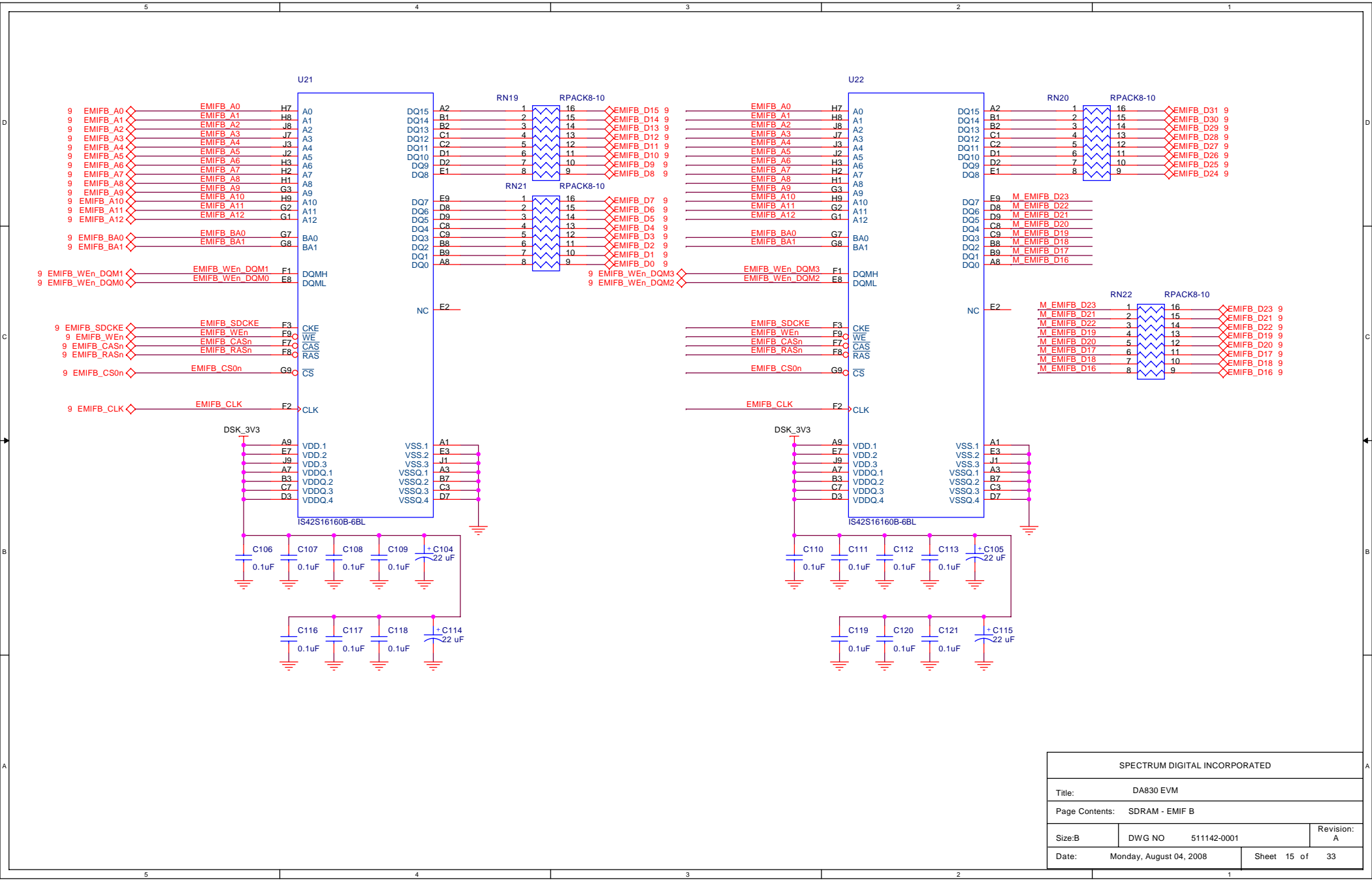
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Title:		DA830 EVM	
Page Contents: PRIMUS DECOUPLING CAPACITORS			
Size: B	DWG NO	511142-0001	Revision: A
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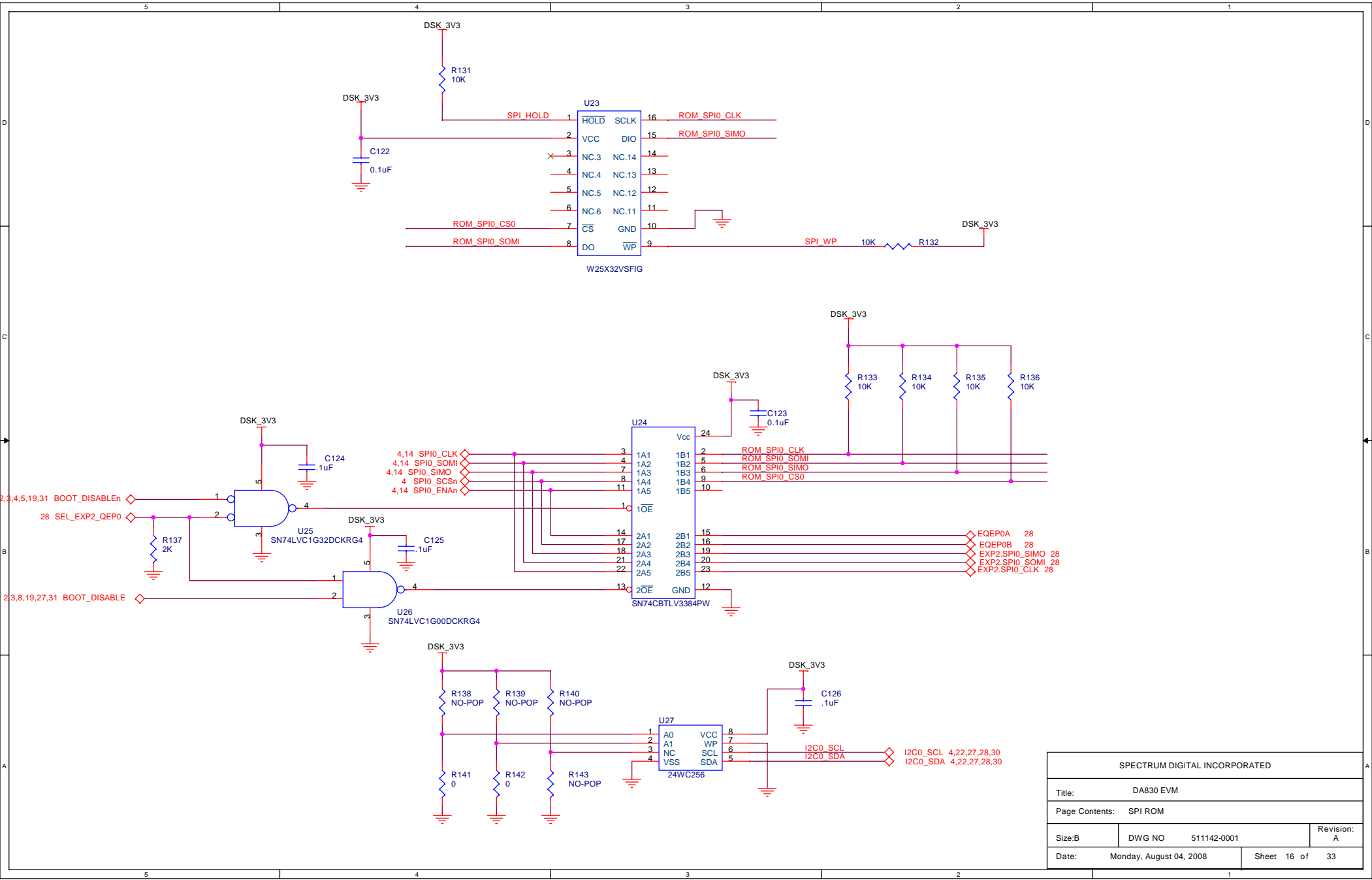
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BOOTMODE1	-	INTERNAL PD	IMPLEMENTED	
BOOTMODE2	-	INTERNAL PD	IMPLEMENTED	
BOOTMODE3	-	INTERNAL PU	IMPLEMENTED	
BOOTMODE4	-	INTERNAL PU		DONE
BOOTMODE5	-	INTERNAL PU		DONE
BOOTMODE6	-	INTERNAL PU		DONE
BOOTMODE7	-	INTERNAL PD	IMPLEMENTED	
BOOTMODE8	-	INTERNAL PU		DONE
BOOTMODE9	-	INTERNAL PU		DONE
BOOTMODE10	-	INTERNAL PD		DONE
BOOTMODE11	-	INTERNAL PD		DONE
BOOTMODE12	-	INTERNAL PU		DONE
BOOTMODE13	-	INTERNAL PU		DONE
BOOTMODE14	-	INTERNAL PU		DONE
BOOTMODE15	-	INTERNAL PU		DONE



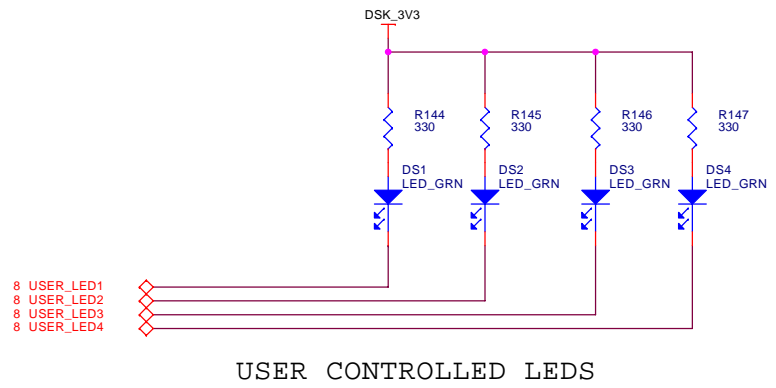
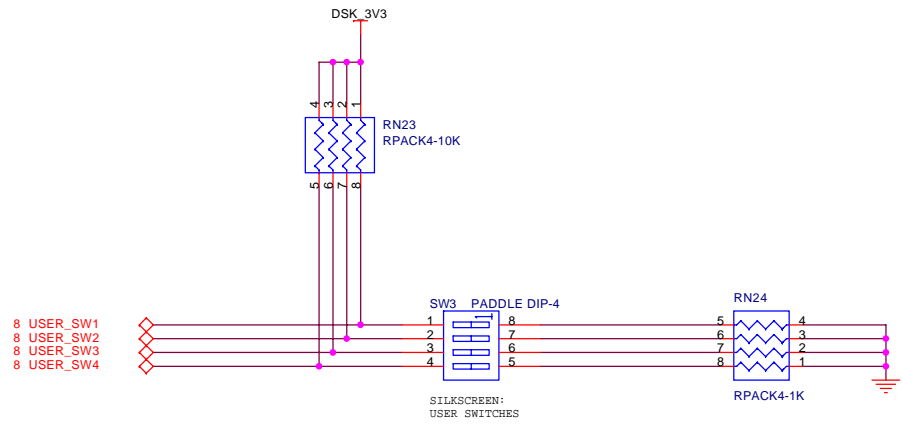
SPECTRUM DIGITAL INCORPORATED			
Title: DA830 EVM			
Page Contents: BOOT MODES			
Size: B	DWG NO	511142-0001	Revision: A
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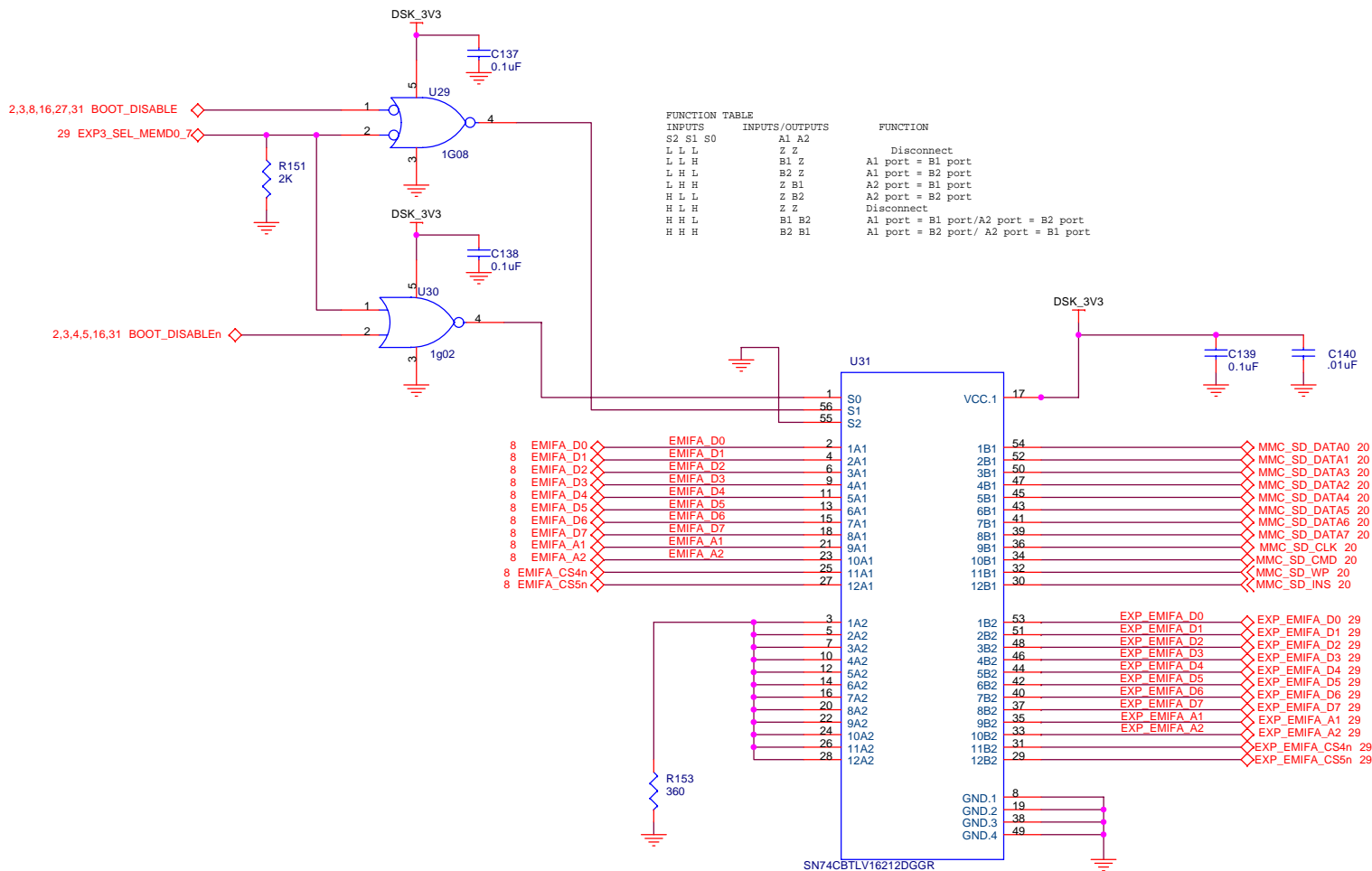
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Title: DA830 EVM			
Page Contents: SDRAM - EMIF B			
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SPECTRUM DIGITAL INCORPORATED			
Title: DA830 EVM			
Page Contents: SPI ROM			
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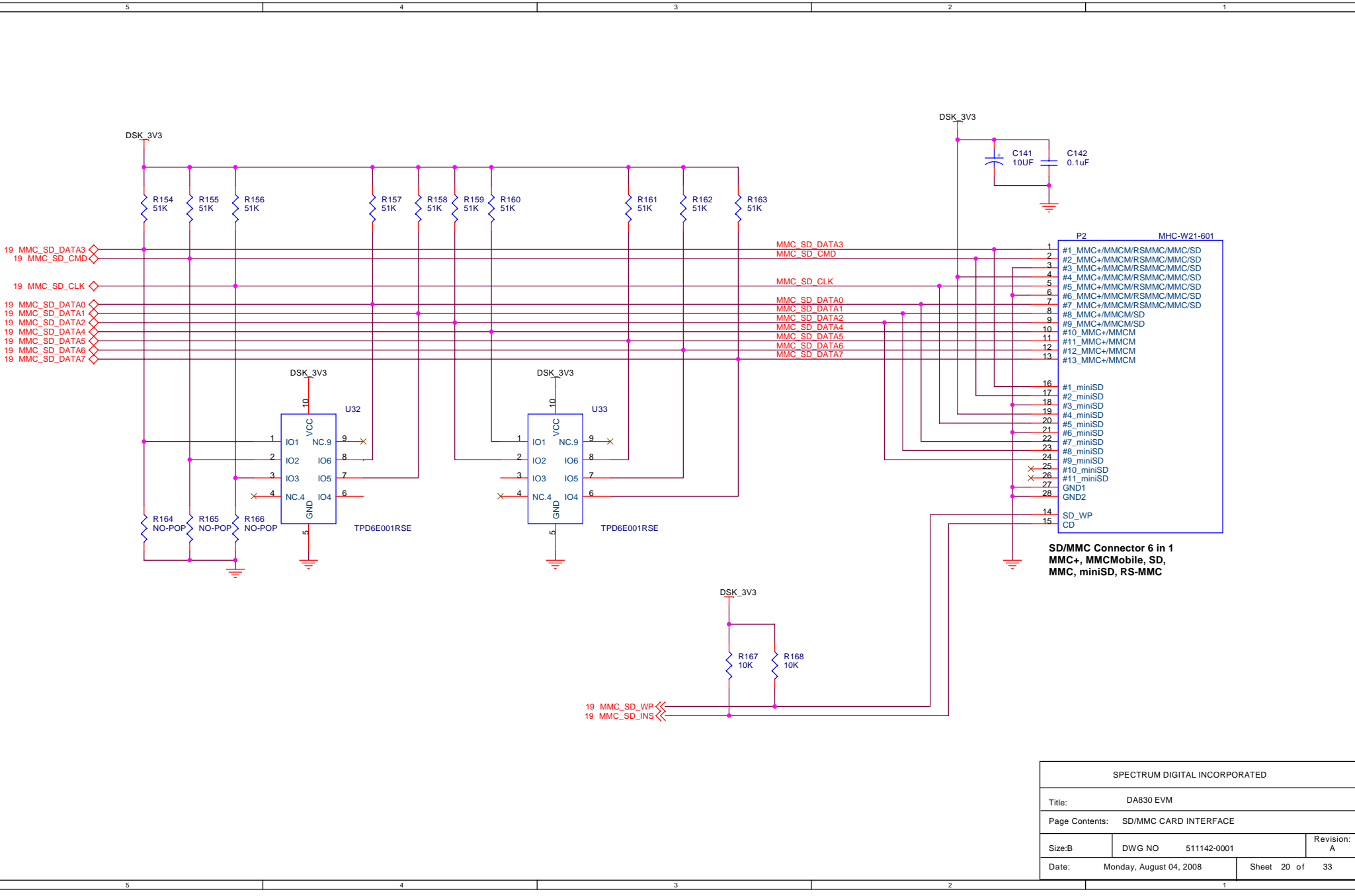
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Title: DA830 EVM			
Page Contents: USER SWITCHES LEDS			
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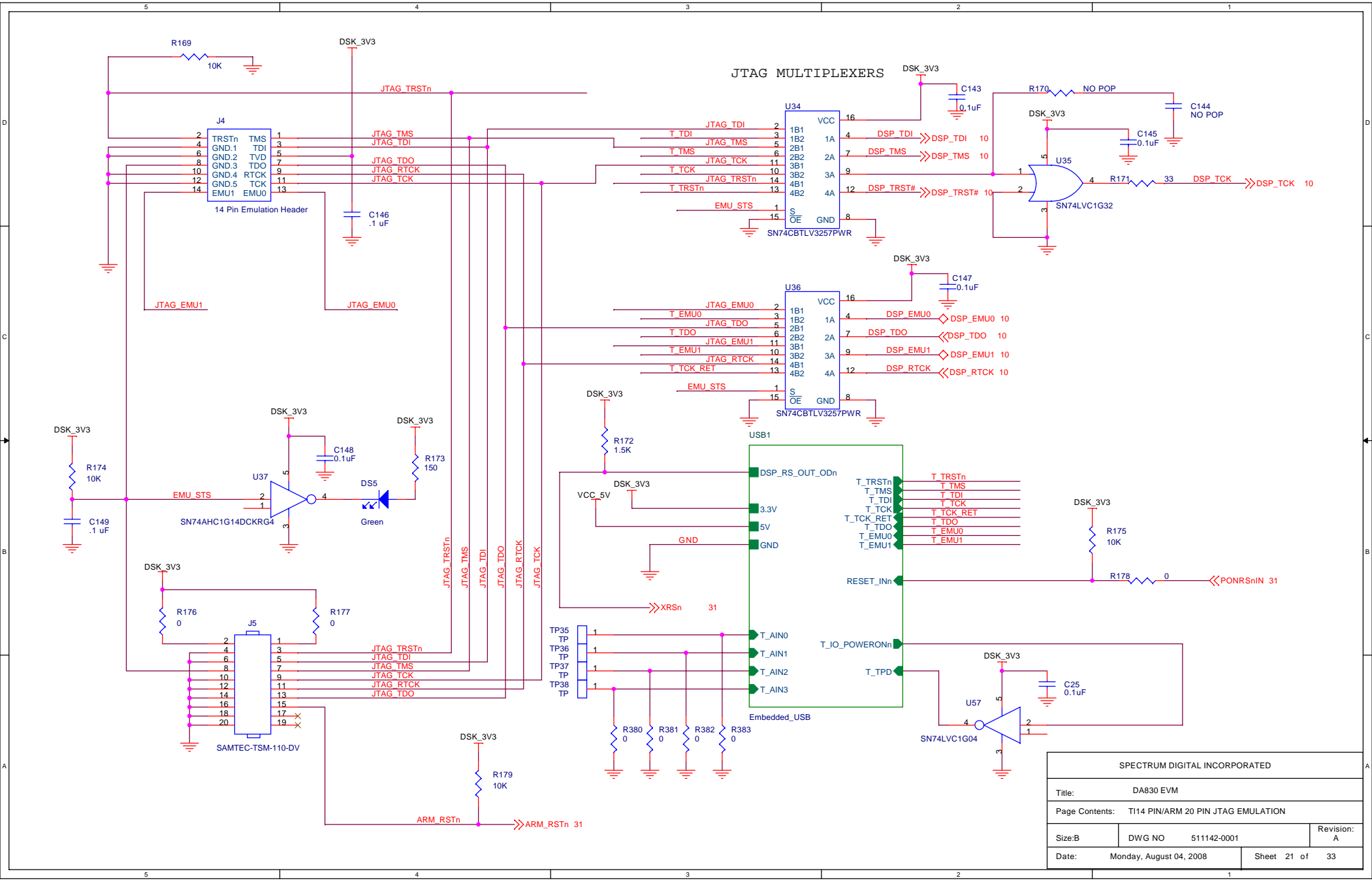
FUNCTION TABLE

INPUTS			INPUTS/OUTPUTS		FUNCTION
S2	S1	S0	A1	A2	
L	L	L	Z	Z	Disconnect
L	L	H	B1	Z	A1 port = B1 port
L	H	L	B2	Z	A1 port = B2 port
L	H	H	Z	B1	A2 port = B1 port
H	L	L	Z	B2	A2 port = B2 port
H	L	H	Z	Z	Disconnect
H	H	L	B1	B2	A1 port = B1 port/A2 port = B2 port
H	H	H	B2	B1	A1 port = B2 port/A2 port = B1 port

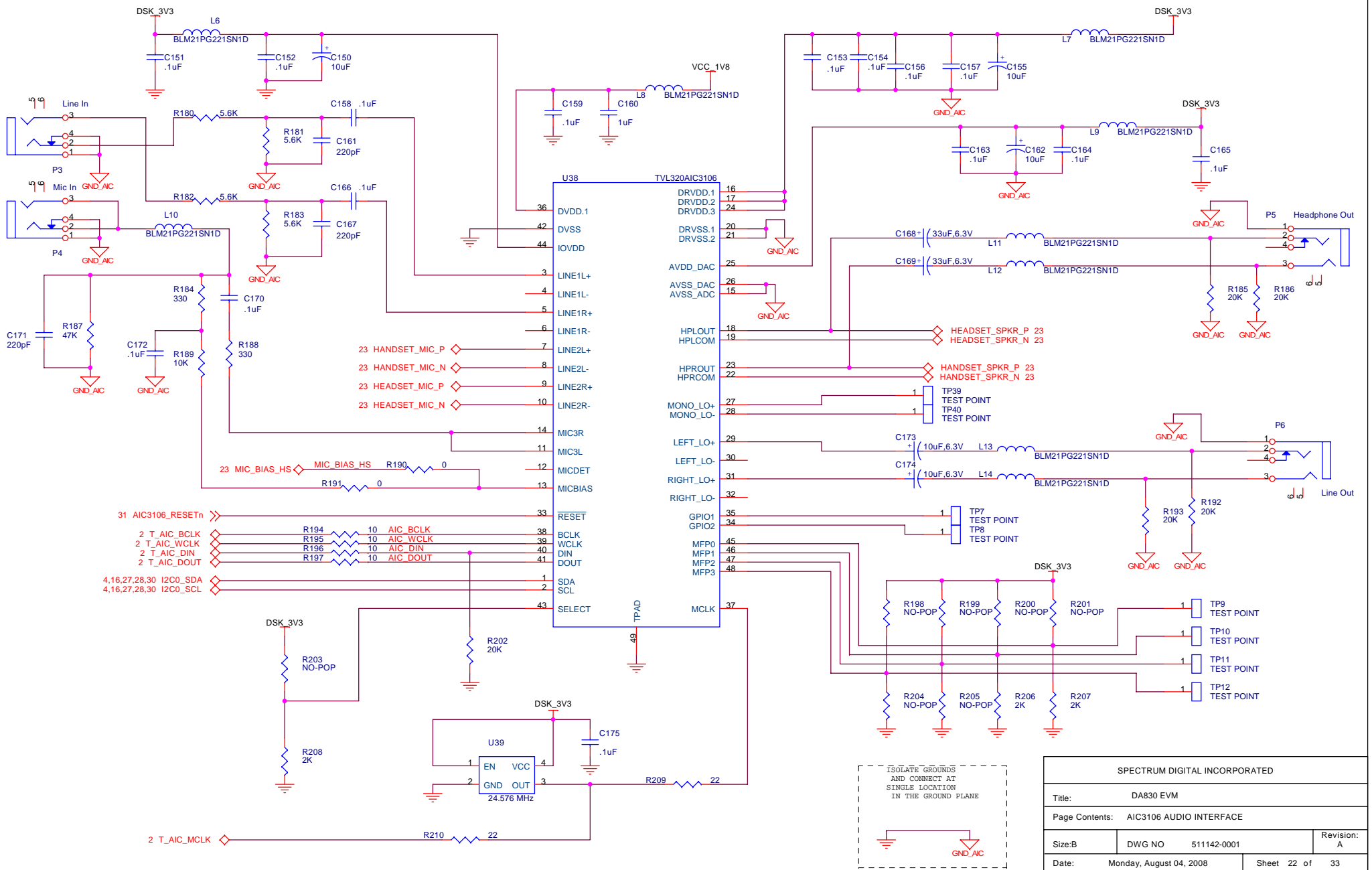
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Page Contents: SD/MMC CARDMUXING			
Size: B	DWG NO	511142-0001	Revision: A
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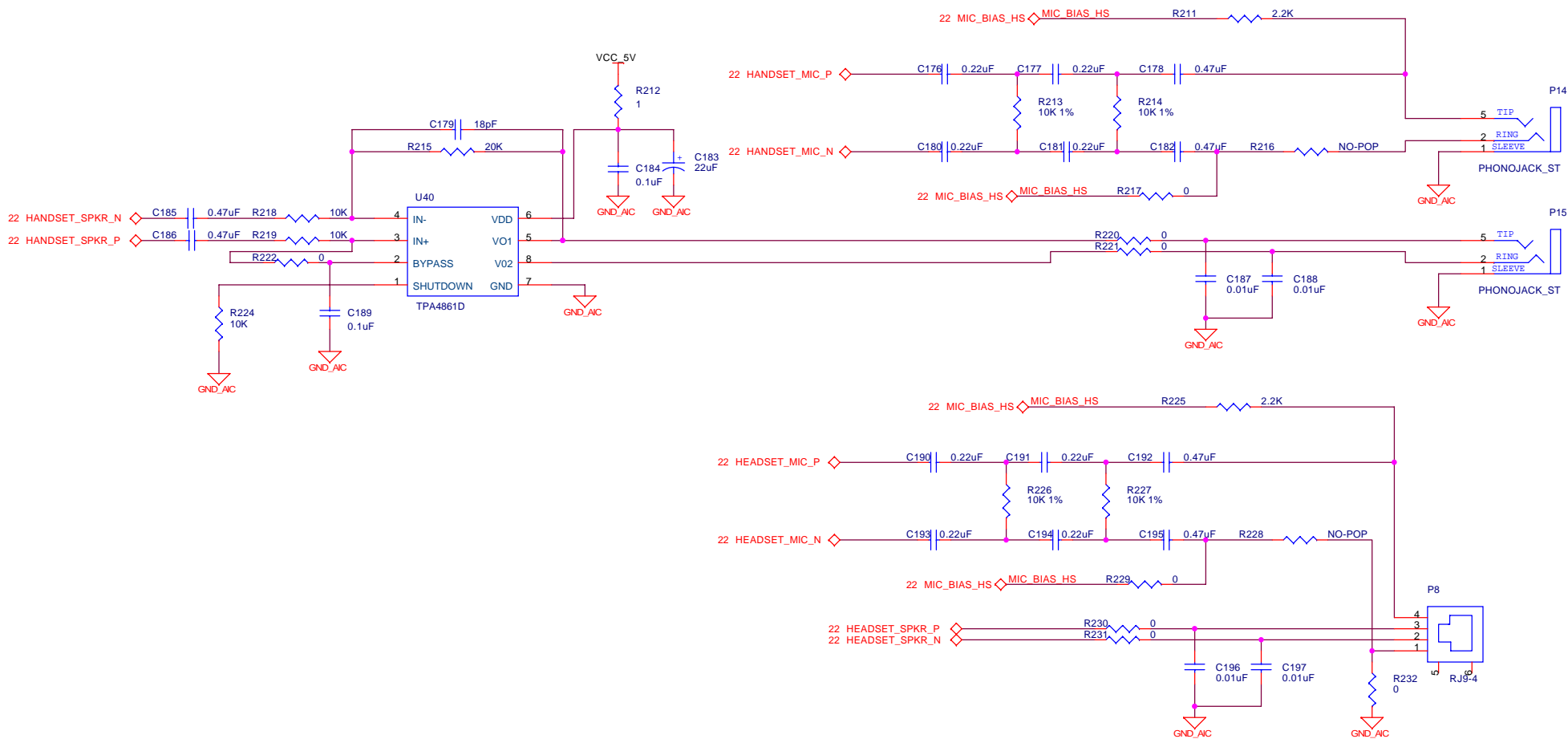
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Page Contents: SD/MMC CARD INTERFACE			
Size: B	DWG NO	511142-0001	Revision: A
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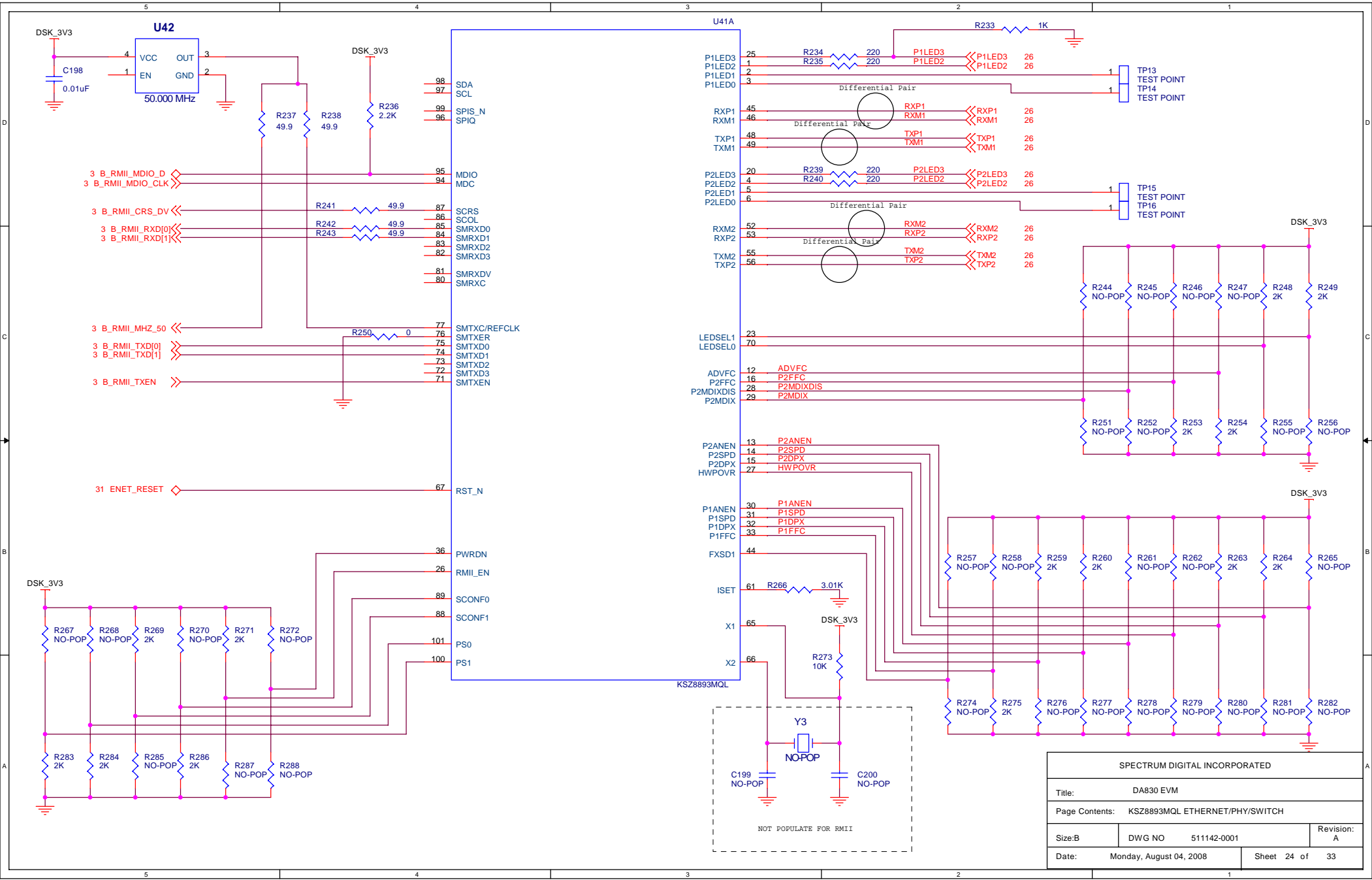
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Title: DA830 EVM			
Page Contents: T114 PIN/ARM 20 PIN JTAG EMULATION			
Size: B	DWG NO	511142-0001	Revision: A
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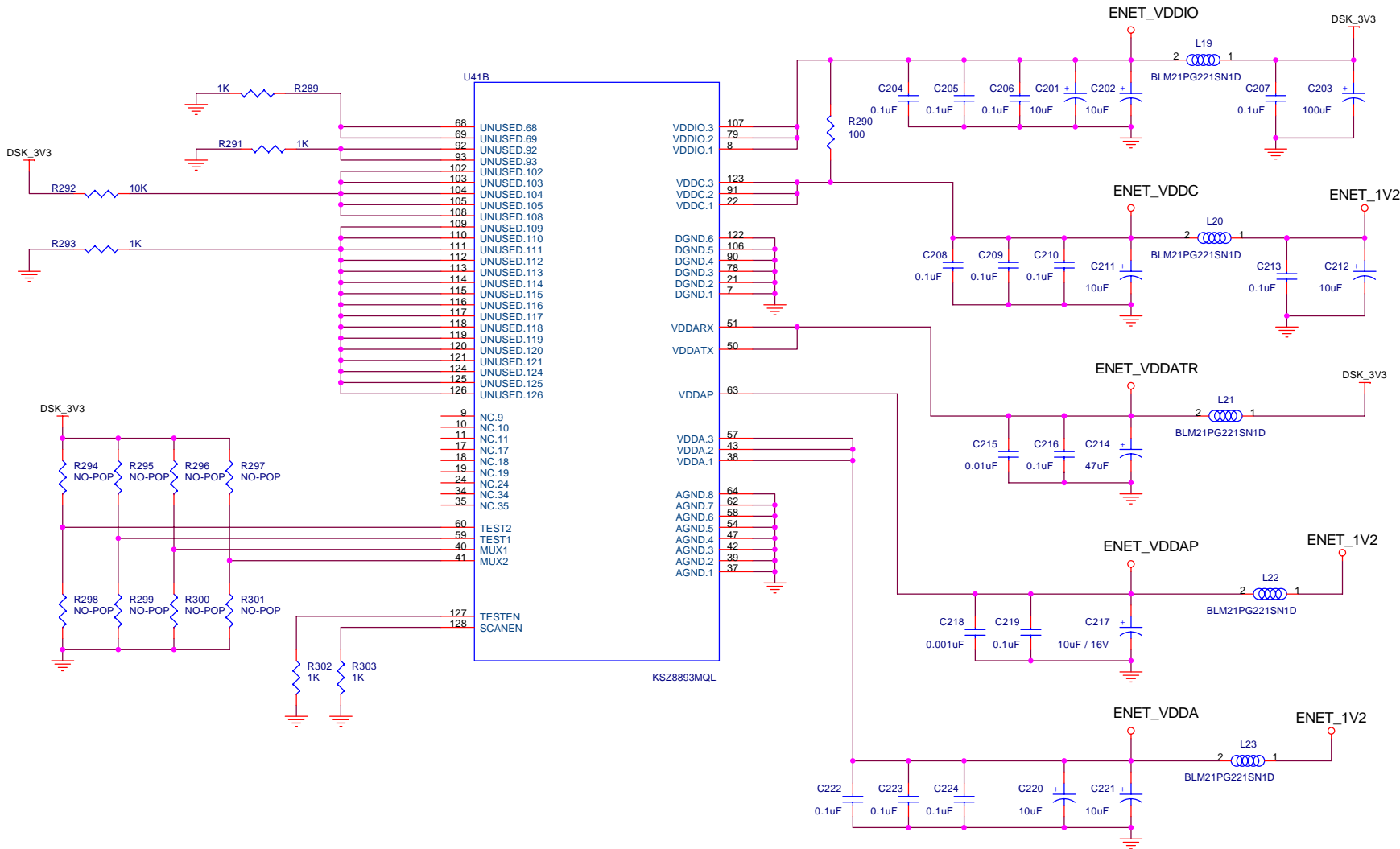
SPECTRUM DIGITAL INCORPORATED			
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Page Contents: AIC3106 AUDIO INTERFACE			
Size: B	DWG NO	511142-0001	Revision: A
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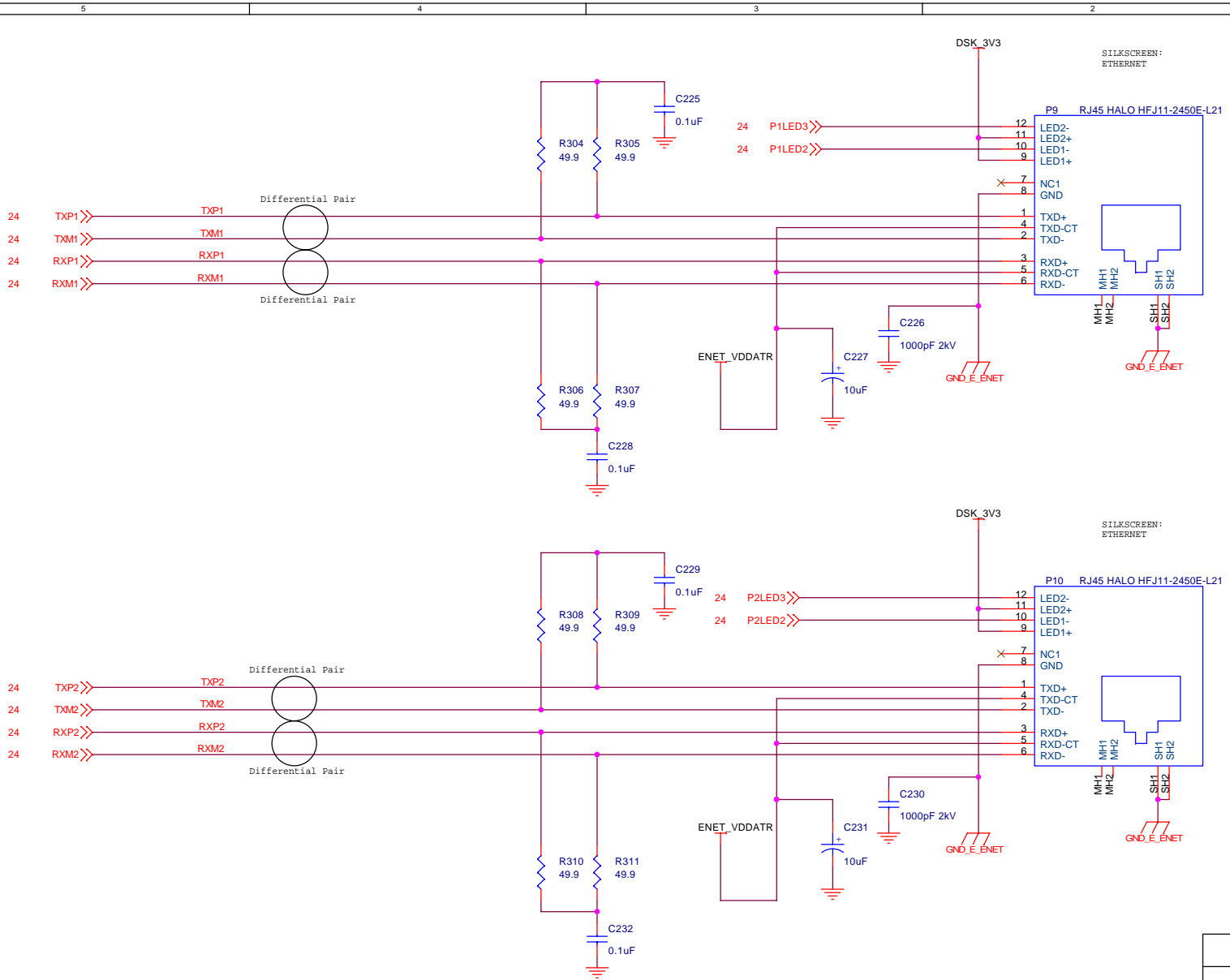
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Title: DA830 EVM			
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Size: B	DWG NO	511142-0001	Revision: A
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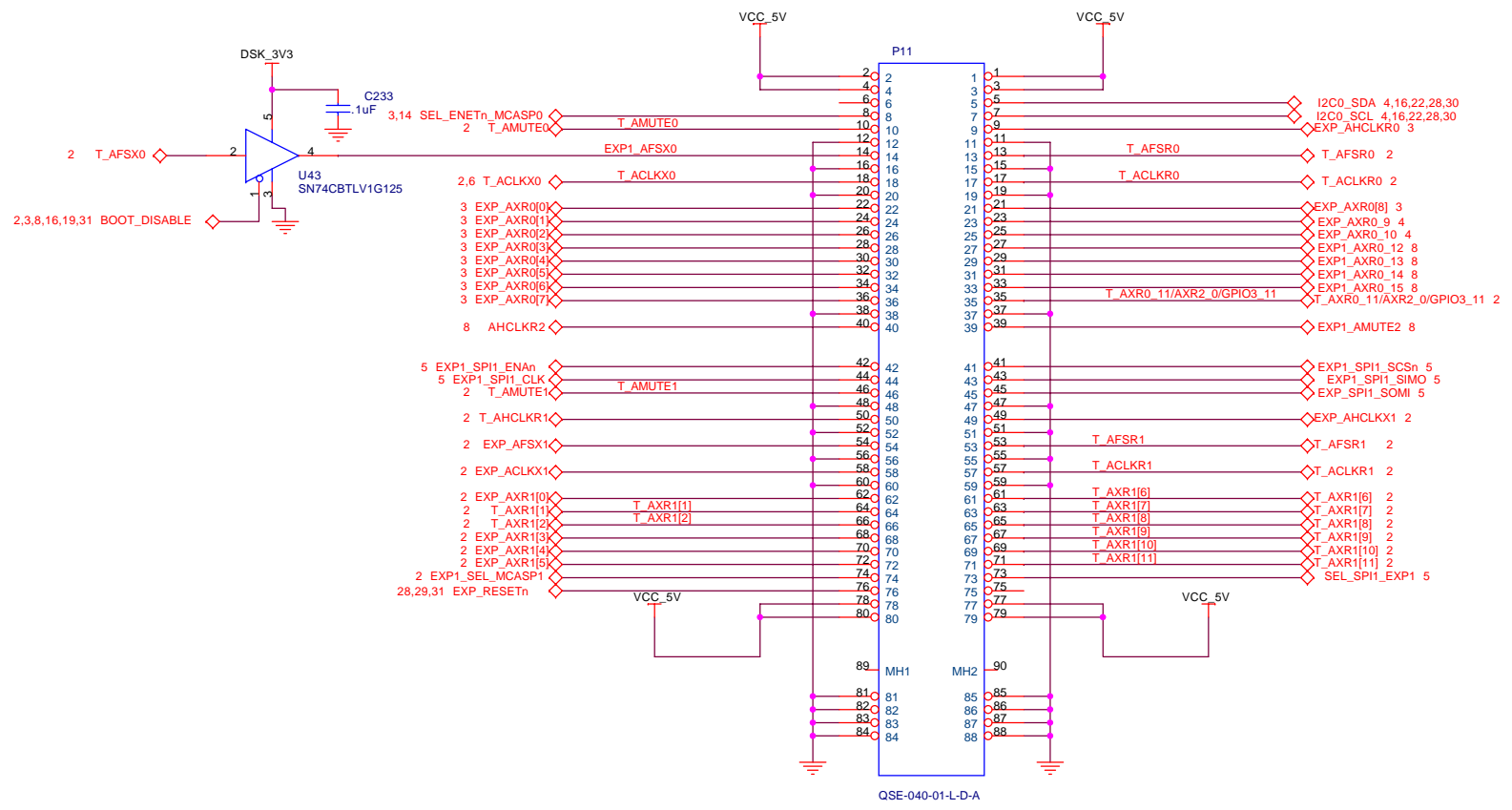
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Title: DA830 EVM			
Page Contents: KSZ8893MQL ETHERNET/PHY/SWITCH			
Size: B	DWG NO	511142-0001	Revision: A
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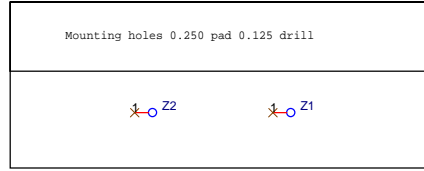
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Page Contents: KSZ8893MQL ETHERNET POWER			
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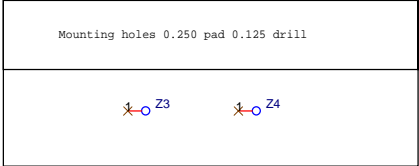
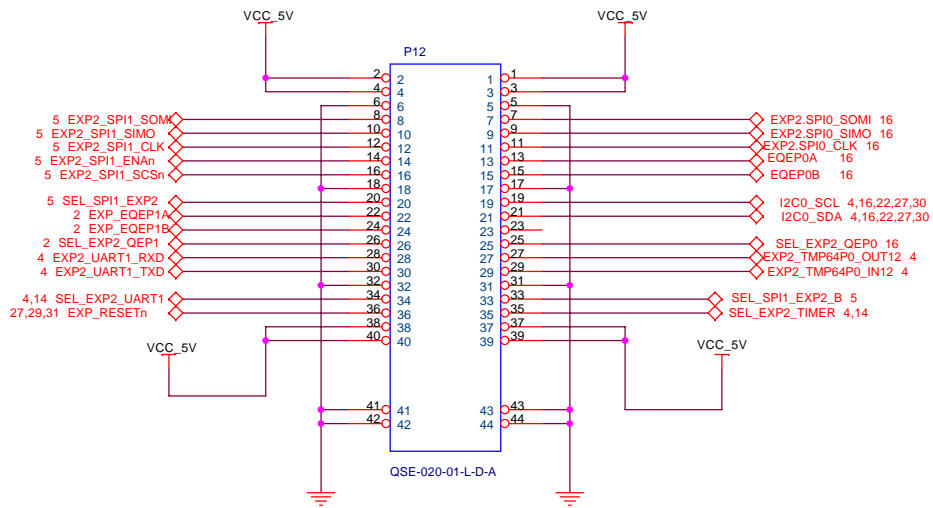
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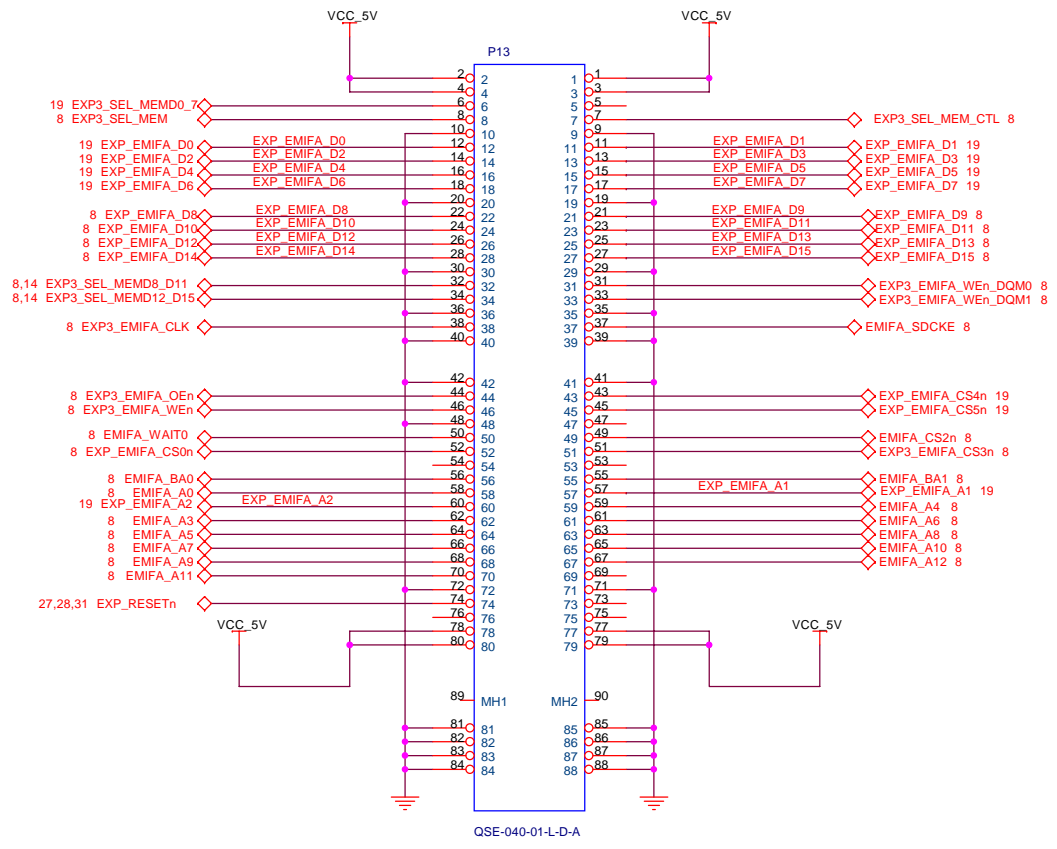
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SPECTRUM DIGITAL INCORPORATED			
Title: DA830 EVM			
Page Contents: AUDIO / EXPANSION COINNECTOR 1			
Size: B	DWG NO	511142-0001	Revision: A
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SPECTRUM DIGITAL INCORPORATED			
Title:		DA830 EVM	
Page Contents: EXPANSION 2			
Size: B	DWG NO	511142-0001	Revision: A
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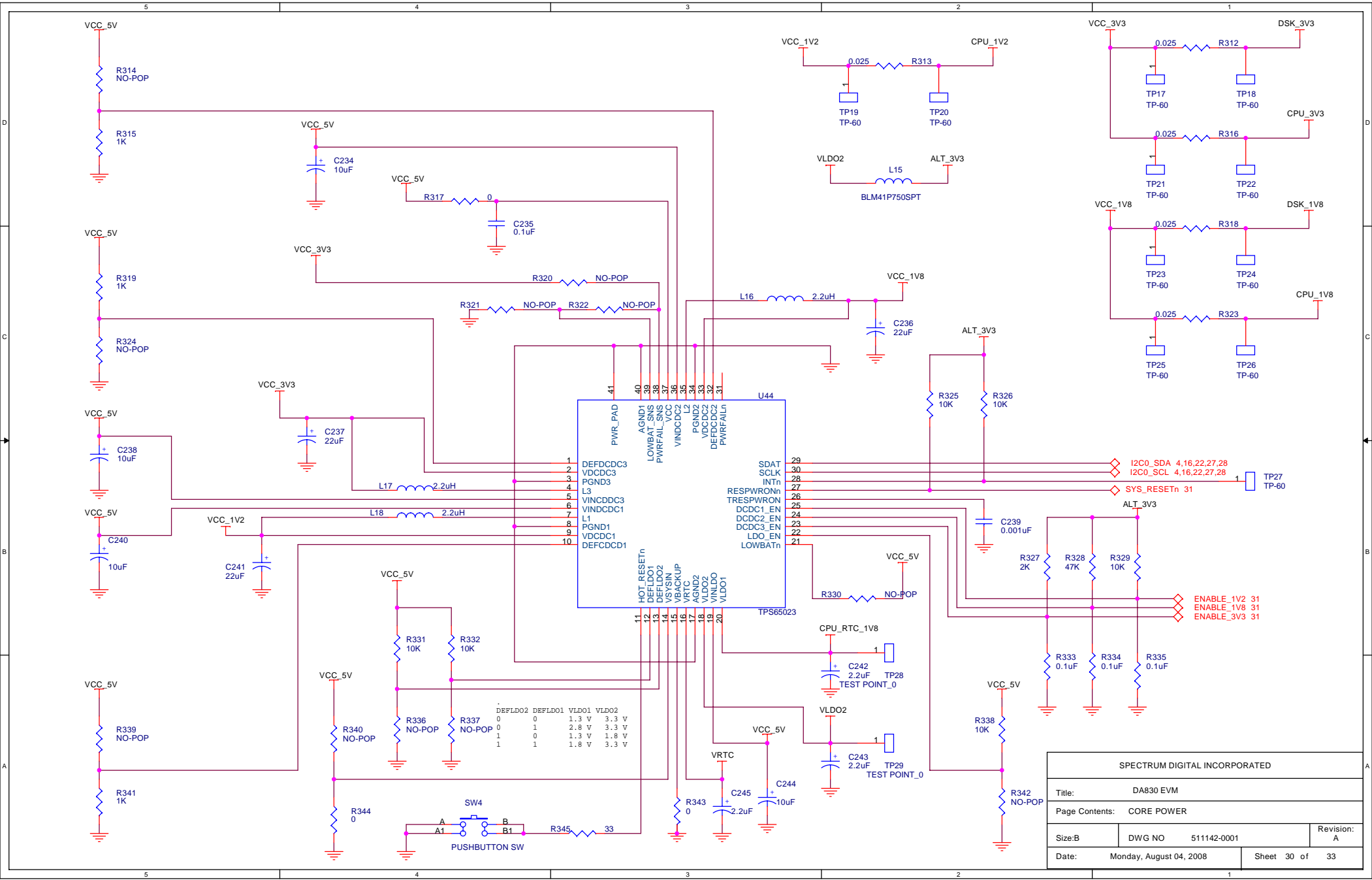


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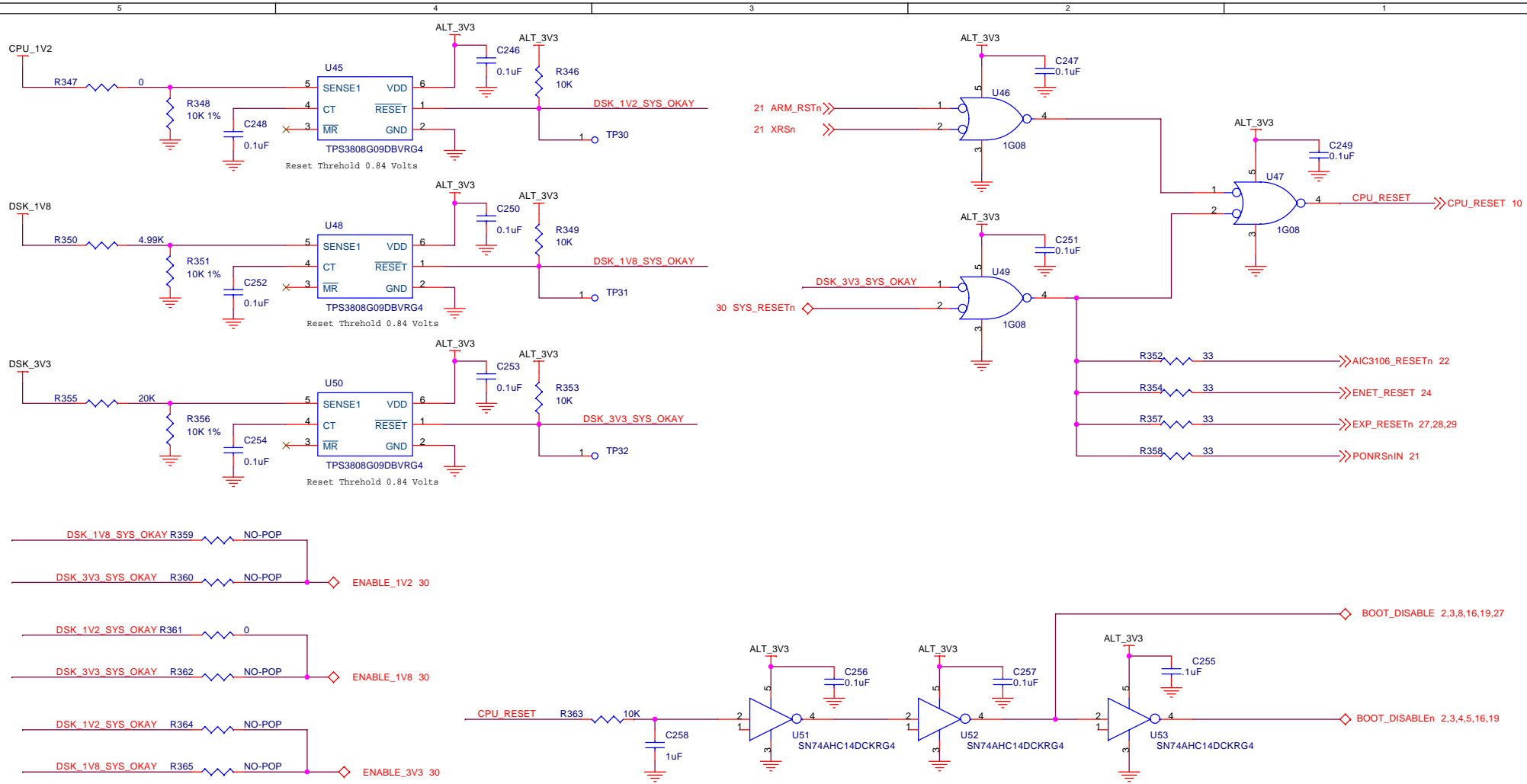
Mounting holes 0.250 pad 0.125 drill

✕ Z5

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Page Contents: EXPANSION 3			
Size: B	DWG NO	511142-0001	Revision: A
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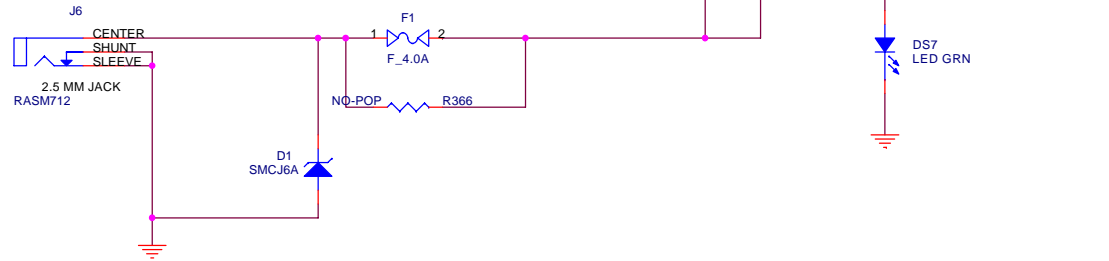


SPECTRUM DIGITAL INCORPORATED			
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Page Contents: CORE POWER			
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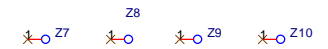


SPECTRUM DIGITAL INCORPORATED			
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Page Contents: POWER IN			
Size: B	DWG NO	511142-0001	Revision: A
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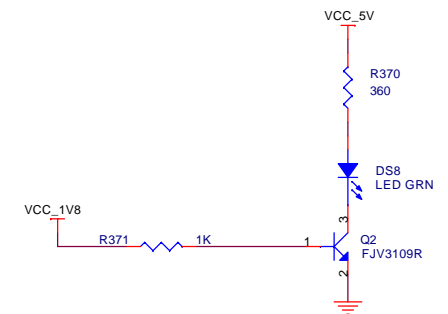
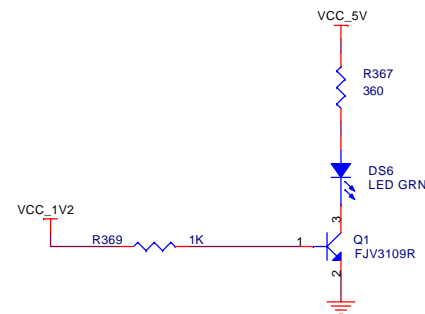
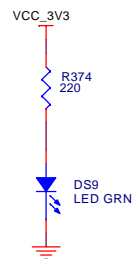
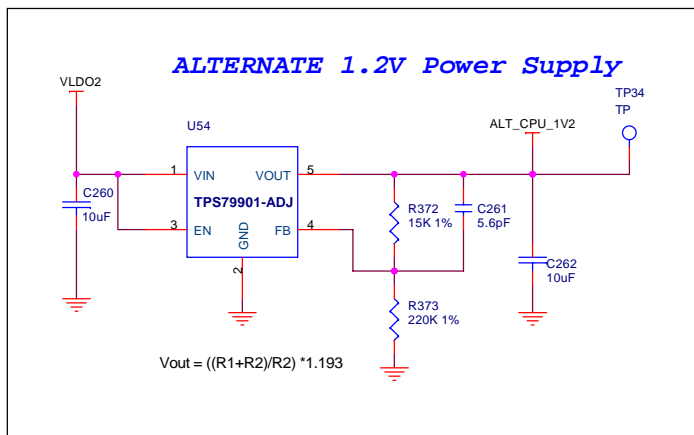
SILKSCREEN:
5V IN



Mounting holes 0.250 pad 0.125 drill

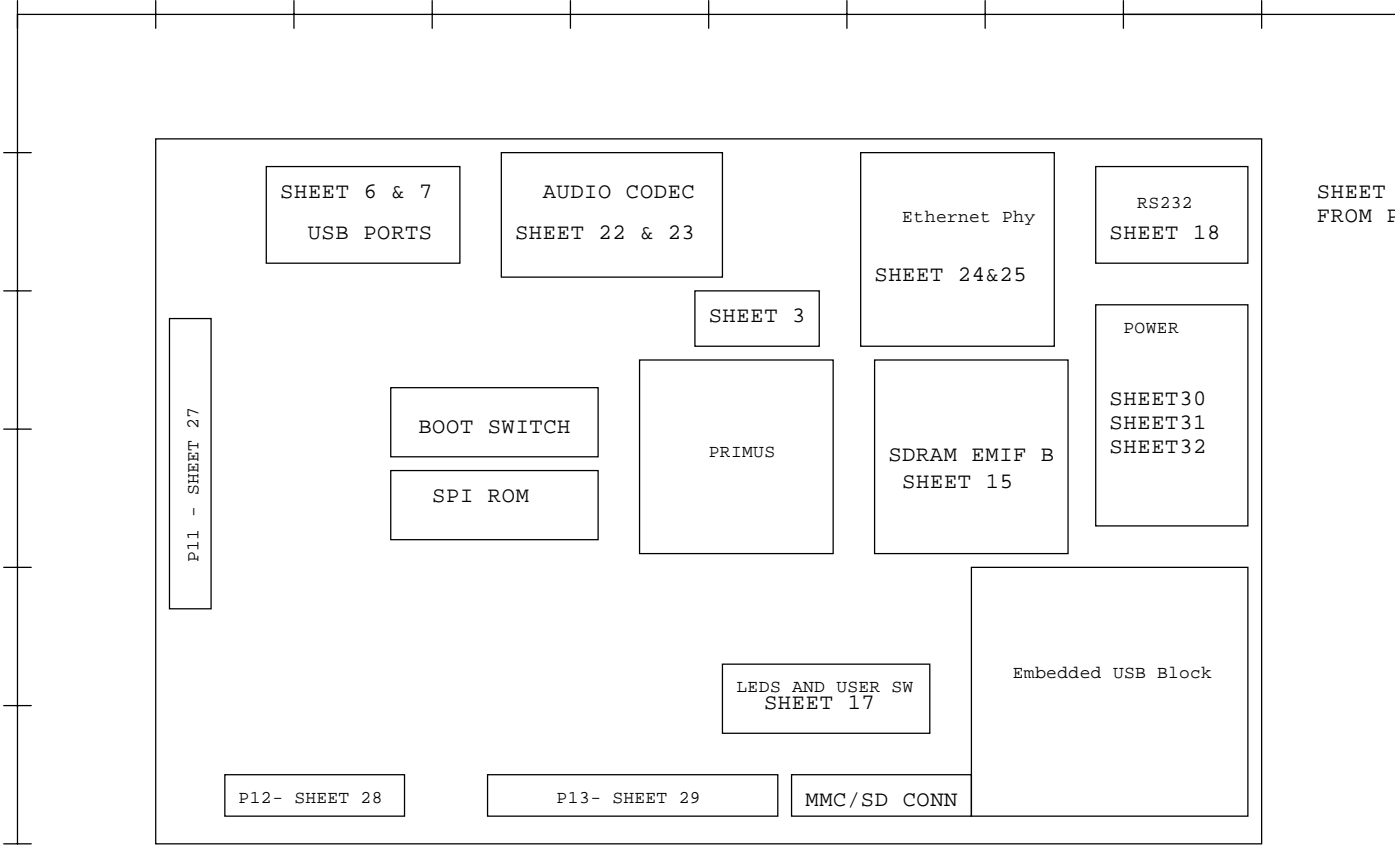


ALTERNATE 1.2V Power Supply



SPECTRUM DIGITAL INCORPORATED

Title: DA830 EVM			
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SHEET 15 - CAN TAKE FROM PRIMUS VDB2.5 DC

SPECTRUM DIGITAL INCORPORATED			
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Page Contents: LAYOUT			
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