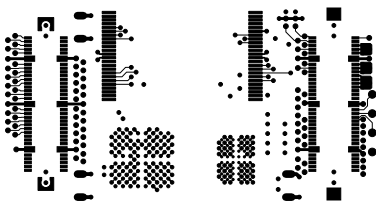

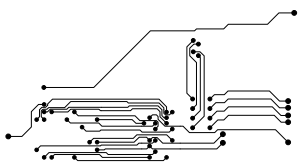

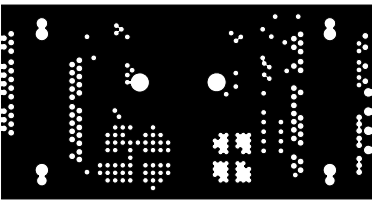
 <p>TURNING CONCEPT INTO REALITY</p> <p>4410 SHOALWOOD AUSTIN, TEXAS 78756 (512)260-5778</p>	SPECTRUM DIGITAL	
	BSLK	C5505 3.3V
	DATE: 03/14/08	




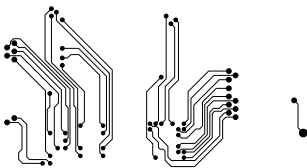
 ionDesign	TURNING CONCEPT INTO REALITY		SPECTRUM DIGITAL	
	4410 SHOALWOOD AUSTIN, TEXAS 78756 (512)260-5778		LAYER 1 COMPONENT	C5505 3.3V
			DATE: 03/14/08	




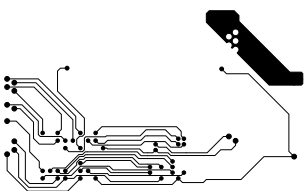
 ionDesign	TURNING CONCEPT INTO REALITY		SPECTRUM DIGITAL	
	4410 SHOALWOOD AUSTIN, TEXAS 78756 (512)260-5778		LAYER 2 SIGNAL	C5505 3.3V
			DATE: 03/14/08	




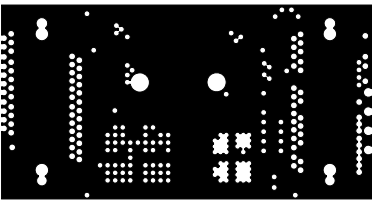
 ionDesign	TURNING CONCEPT INTO REALITY		SPECTRUM DIGITAL	
	4410 SHOALWOOD AUSTIN, TEXAS 78756 (512)260-5778		LAYER 3 GROUND	C5505 3.3V
			DATE: 03/14/08	




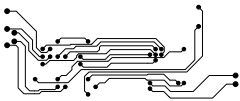
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			DATE: 03/14/08	




	TURNING CONCEPT INTO REALITY		SPECTRUM DIGITAL	
	4410 SHOALWOOD AUSTIN, TEXAS 78756 (512)260-5778		LAYER 5 SIGNAL	C5505 3.3V
			DATE: 03/14/08	




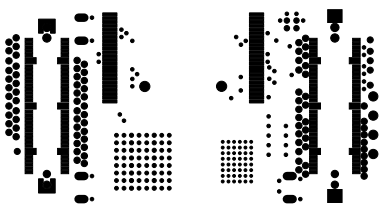
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	4410 SHOALWOOD AUSTIN, TEXAS 78756 (512)260-5778		LAYER 6 POWER	C5505 3.3V
			DATE: 03/14/08	




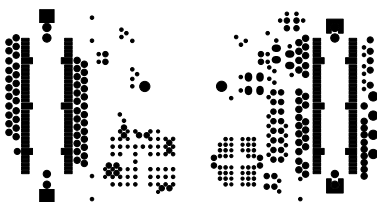
 ionDesign	TURNING CONCEPT INTO REALITY		SPECTRUM DIGITAL	
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			DATE: 03/14/08	




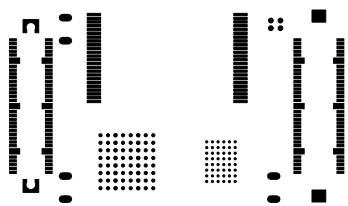
 ionDesign	TURNING CONCEPT INTO REALITY		SPECTRUM DIGITAL	
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			DATE:	03/14/08




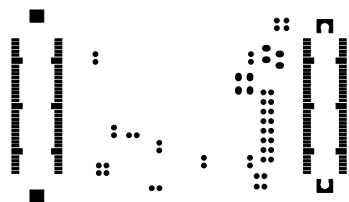
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			DATE: 03/14/08	




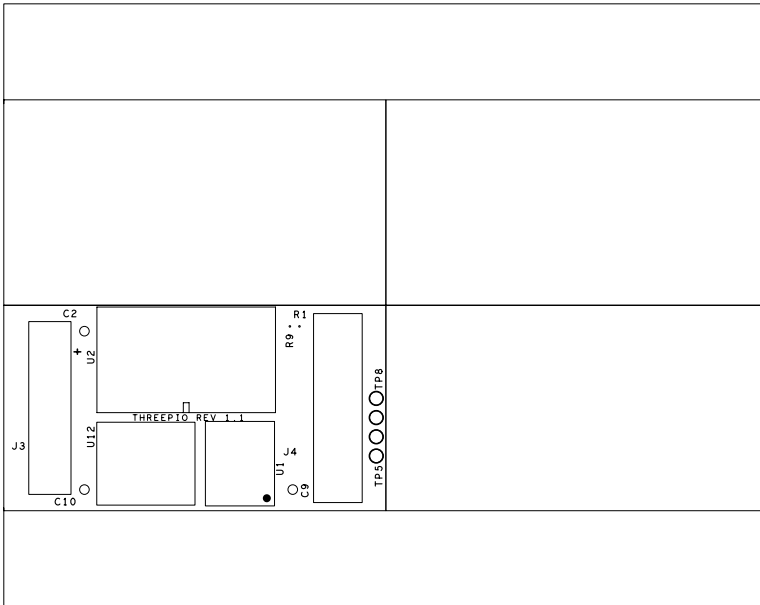
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			DATE:	03/14/08




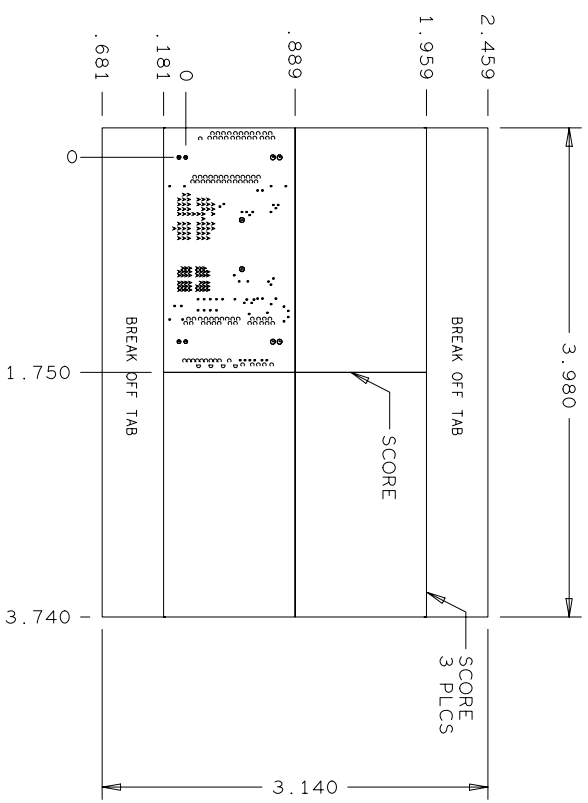
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			DATE:	03/14/08



 ionDesign	TURNING CONCEPT INTO REALITY		SPECTRUM DIGITAL	
	4410 SHOALWOOD AUSTIN, TEXAS 78756 (512)260-5778		SPS	C5505 3.3V
			DATE:	03/14/08



 <p>ionDesign</p>	<p>TURNING CONCEPT INTO REALITY</p> <p>4410 SHOALWOOD AUSTIN, TEXAS 78756 (512)260-5778</p>		<p>SPECTRUM DIGITAL</p>	
	<p>TSLK</p>	<p>C5505 3.3V</p>	<p>DATE: 03/14/08</p>	
	<p>DATE: 03/14/08</p>			



DRILL CHART: TOP to BOTTOM

ALL UNITS ARE IN MILS

FIGURE	SIZE	PLATING	QTY
A	8.0	PLATED	101
B	10.0	PLATED	56
C	15.0	PLATED	92
D	20.0	PLATED	4
E	35.0	NON-PLATED	4
F	40.0	NON-PLATED	4
G	50.0	NON-PLATED	2

- TSLK
- SMC
- LAYER 1
- LAYER 2
- LAYER 3
- LAYER 4
- LAYER 5
- LAYER 6
- LAYER 7
- LAYER 8
- SMS
- BSLK

LAYER SCHEDULE

SCALE: NONE

9. ALL SIGNAL LAYERS TO BE CONTROLLED IMPEDANCE. 50 OHM SINGLE ENDED.
8. SURFACE FINISH SHALL BE IMMERSION GOLD.
7. FINISHED BOARD SHALL BE FREE OF BURRS ON EDGES.
6. DRC'S MUST BE RUN ON THE GERBERs BEFORE BUILDING BOARDS UNLESS PRIOR APPROVAL IS GIVEN IN WRITING.
5. PLATED THRU HOLE SIZE TOLERANCES SHALL BE +/- .003 INCH. UNPLATED THRU HOLE SIZE TOLERANCES SHALL BE +/- .002 INCH.
4. SOLDERMASK ON BOTH SIDES OF THE BOARD SHALL BE S.M.O.B.C., LPI, COLOR GREEN. MINIMUM CLEARANCE ON THE BOARD SHALL BE .003 OF AN INCH.
3. MINIMUM TRACE WIDTH SHALL BE .003 OF AN INCH.
2. COPPER PLATED SURFACE CONDUCTORS SHALL BE THE EQUIVALENT OF 0.5 OZ. COPPER AFTER PLATING. PLANE LAYERS SHALL BE 1 OZ. COPPER. AVERAGE COPPER ON THE WALL OF THE PLATED HOLE SHALL BE .001 INCH.
1. TOTAL BOARD THICKNESS SHALL BE .062 +/- .007 INCHES. ALL MATERIAL SHALL BE NEMA GRADE FR-4 OR EQUIVALENT.

NOTES: UNLESS OTHERWISE SPECIFIED

DATE	03/14/08
DESIGN ENGR	
PROJECT ENGR	
ENGR NGR	
NET ASSEMBLY	

APPROVED	
CHECKED	
DRAWN	
DATE	
PROJECT ENGR	
ENGR NGR	
DO NOT SCALE DRAWING	

SPECTRUM DIGITAL	
FABRICATION DRAWING	
THREEPIO REV 1.1	
CS505 MEMORY BOARD 3.3V	
SCALE	NONE
SHEET	1 OF 1
DATE	5/10/61 REV B
TYPE	D



1 2 3 4 5 6 7 8 9 10 11