

NOTES, UNLESS OTHERWISE SPECIFIED:


1. RESISTANCE VALUES IN OHMS.
2. CAPACITANCE VALUES IN MICROFARADS.
3. REFERENCE DESIGNATORS USED:
4. ALL 0.1 μ F AND 0.01 μ F CAPACITORS ARE DECOUPLING CAPS UNLESS OTHERWISE NOTED. THEY ARE SHOWN ON THE PAGE WITH THE INTEGRATED CIRCUITS THEY SHOULD BE PLACED NEAR.

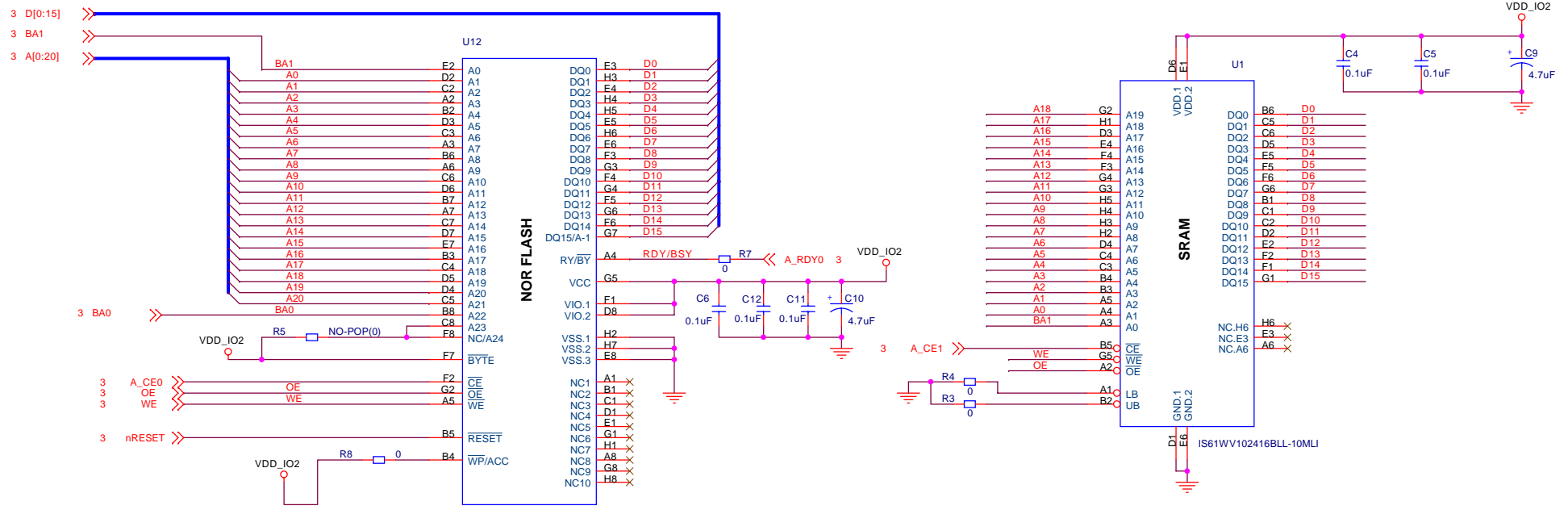
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1.0A	Initial Release.	01/14/08	
1.1A	-Changed signal going to NAND RE from RnW to OE. -Added support for NAND with 2 CEs.	02/04/08	

3.3V EMIF DAUGHTER CARD

Rev 1.1A

** To be used with C5505 EVM*

	Page Title:		Title Block	
	Schematic Name:			3.3V EMIF DC
	Size	DWG#:	Rev	
	B	51062-0002	I 1.1A	
Schematic last Modified:		Designed by:	Sheet	
Thursday, March 13, 2008		Scott Beltram	1 of 5	

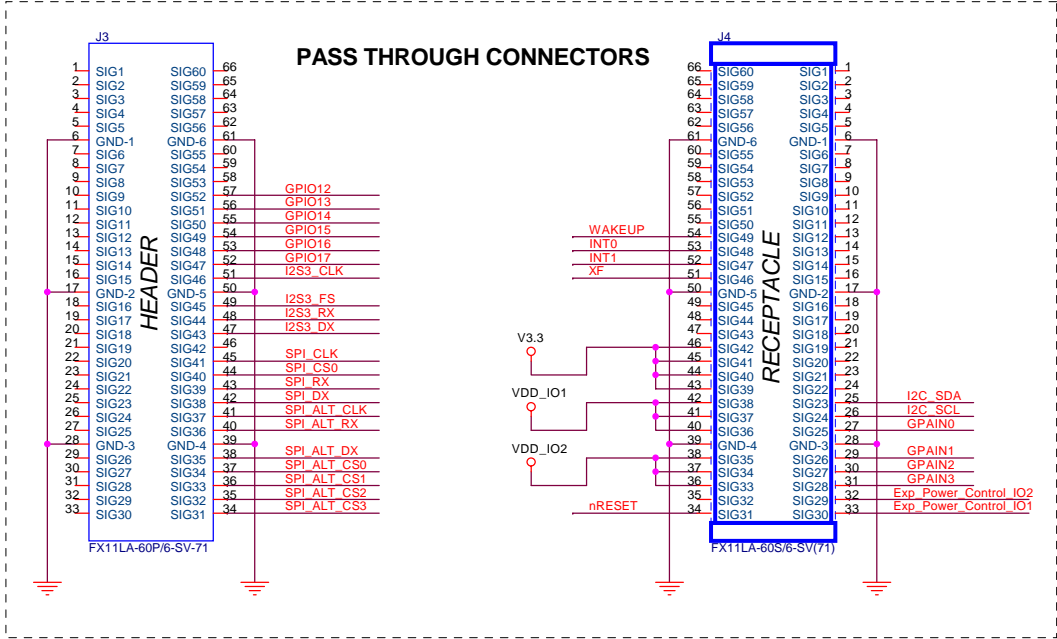
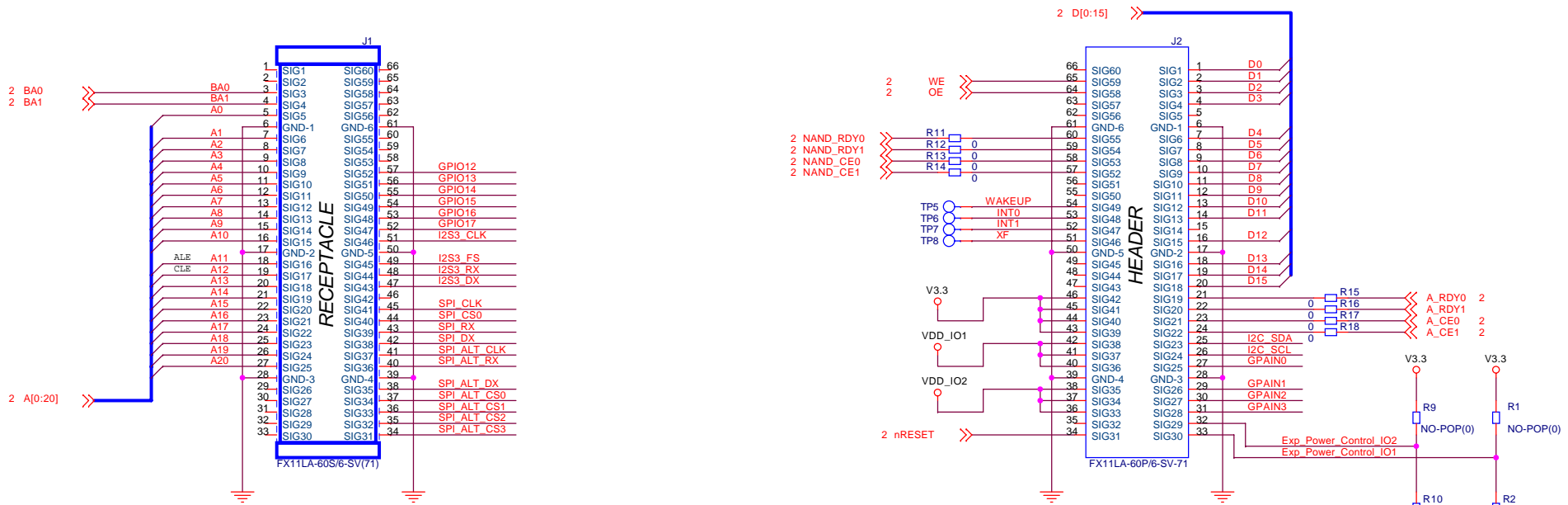


NOR FLASH

SRAM

MLC NAND

	Page Title:		NAND / NOR / SRAM
	Schematic Name:		3.3V EMIF DC
	Size:	DWG#:	51062-0002
	Designed by:		Scott Beltram
Schematic last Modified:		Thursday, March 13, 2008	Sheet 2 of 5



	Page Title:	Expansion Conn
	Schematic Name:	3.3V EMIF DC
	Size:	DWG#: 51062-0002
	Rev:	1.1.A
Schematic last Modified:		Thursday, March 13, 2008
Designed by:		Scott Beltram
Sheet		3 of 5