

NOTES, UNLESS OTHERWISE SPECIFIED:


1. RESISTANCE VALUES IN OHMS.
2. CAPACITANCE VALUES IN MICROFARADS.
3. REFERENCE DESIGNATORS USED:
4. ALL 0.1 μ F AND 0.01 μ F CAPACITORS ARE DECOUPLING CAPS UNLESS OTHERWISE NOTED. THEY ARE SHOWN ON THE PAGE WITH THE INTEGRATED CIRCUITS THEY SHOULD BE PLACED NEAR.

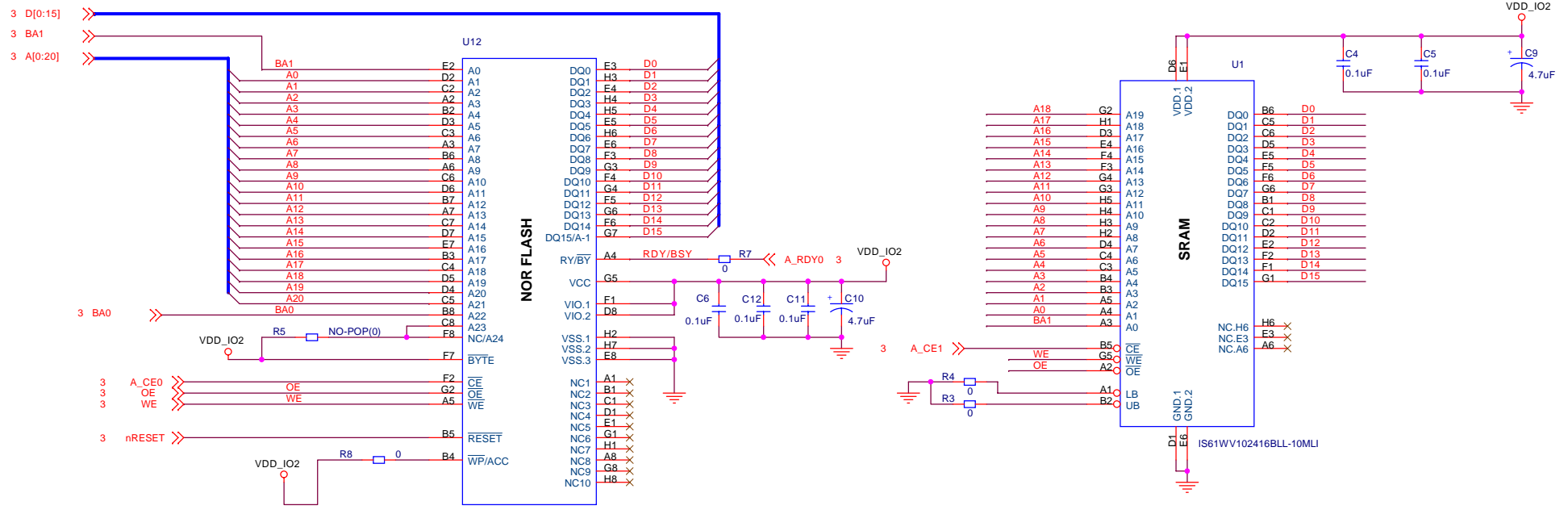
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1.0A	Initial Release.	01/14/08	
1.1A	-Changed signal going to NAND RE from RnW to OE. -Added support for NAND with 2 CEs.	02/04/08	

3.3V EMIF DAUGHTER CARD

Rev 1.1A

** To be used with C5505 EVM*

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	B	51062-0002	I 1.1A	
Schematic last Modified:		Designed by:	Sheet	
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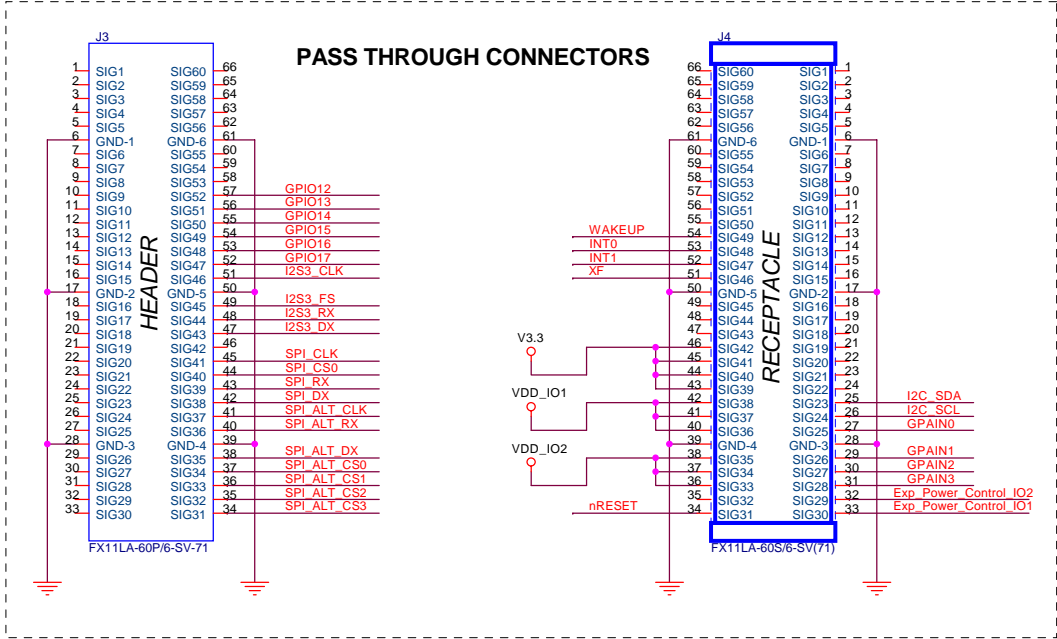
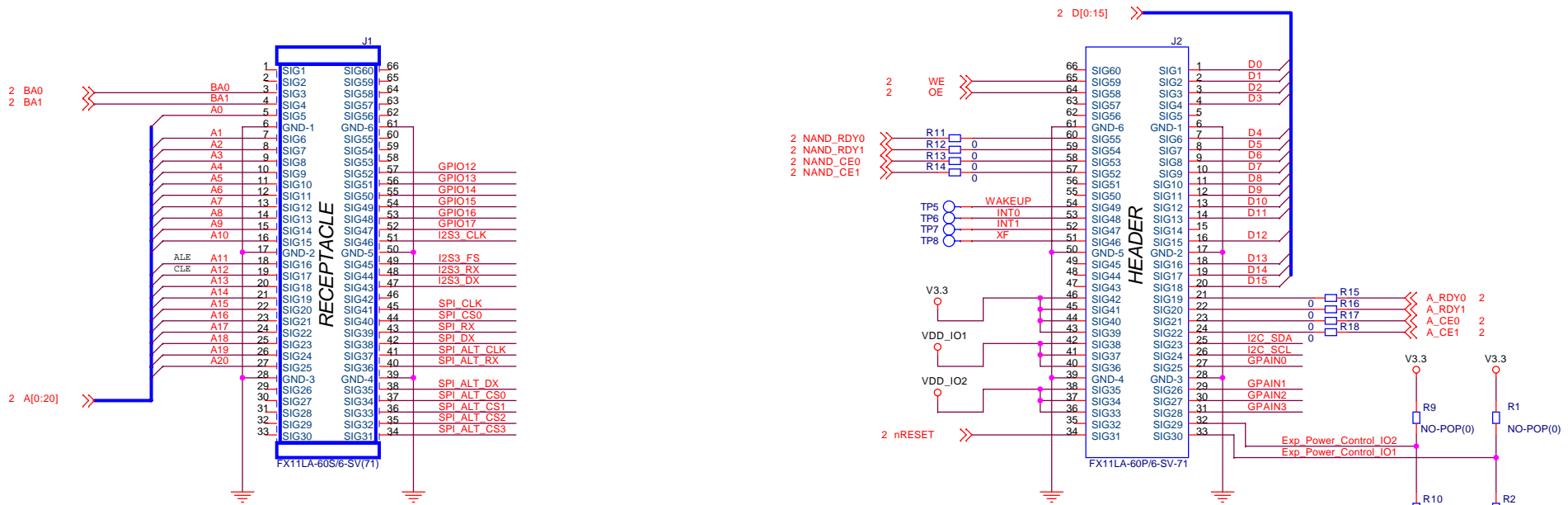


NOR FLASH

SRAM

MLC NAND

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