

NOTES, UNLESS OTHERWISE SPECIFIED:

1. RESISTANCE VALUES IN OHMS.
2. CAPACITANCE VALUES IN MICROFARADS.
3. REFERENCE DESIGNATORS USED:
4. ALL 0.1 uF AND 0.01uF CAPACITORS ARE DECOUPLING CAPS UNLESS OTHERWISE NOTED. THEY ARE SHOWN ON THE PAGE WITH THE INTEGRATED CIRCUITS THEY SHOULD BE PLACED NEAR.

REV	DESCRIPTION	DATE	APPROVED
A	Initial schematic for layout	10/15/2009	RRP
B	Update TPS65023 Enable Logic	12/15/2009	RRP

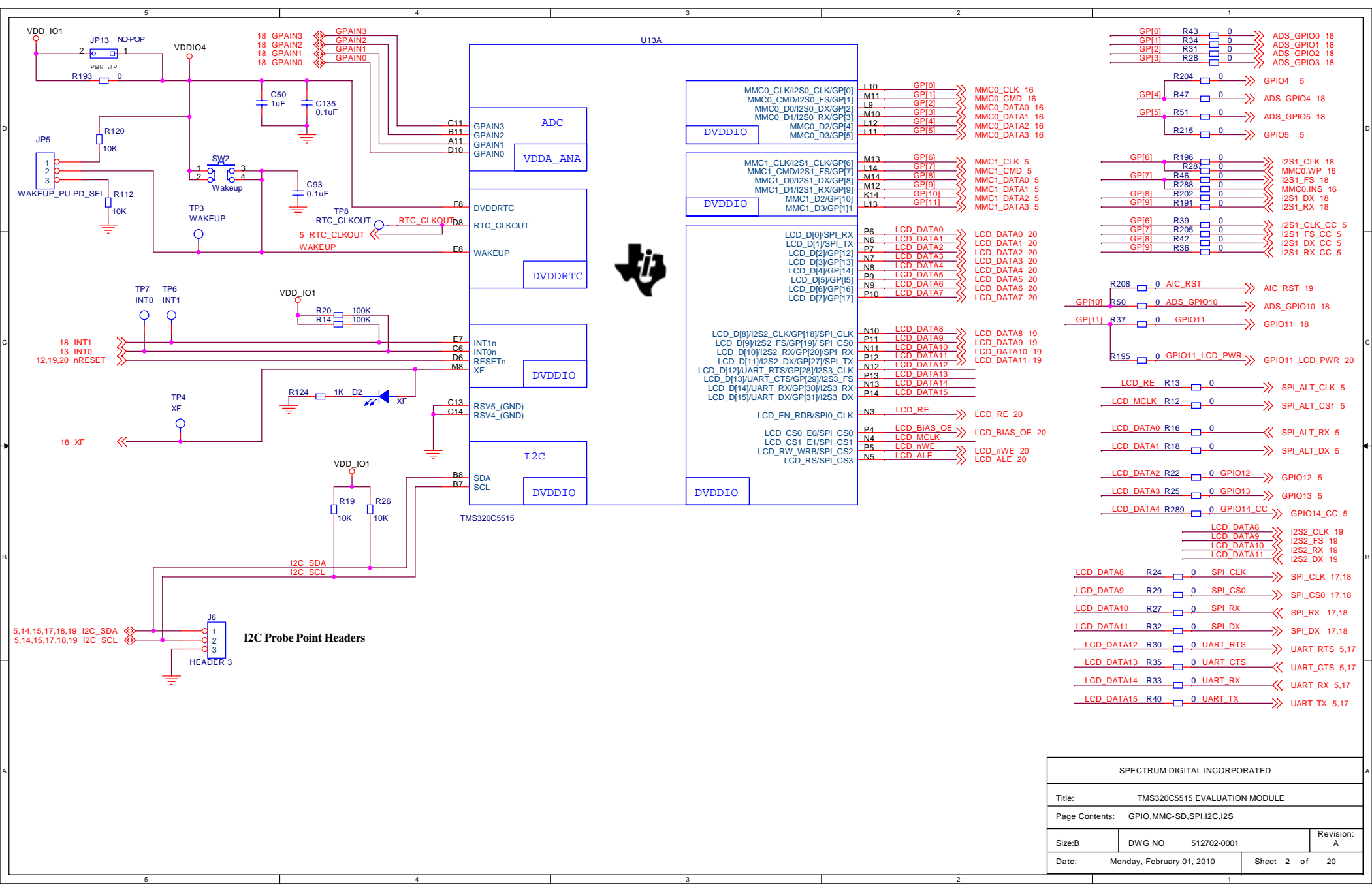
Schematic Contents

- 01 - TITLE PAGE
- 02 - TMS320C5515 GPIO / MMC / SPI / I2C
- 03 - TMS320C5515 CLOCKS / JTAG / USB
- 04 - EMULATION - JTAG
- 05 - CC Board Interface
- 06 - TMS320C5515 EMIF
- 07 - Mobile DRAM Interface
- 08 - NAND Flash Interface
- 09 - NOR Flash Interface
- 10 - TMS320C5515 Power
- 11 - CPU Decoupling Caps
- 12 - RESET
- 13 - Power Input
- 14 - TPS65023 Power Management
- 15 - POWER Router
- 16- MMC / SD
- 17 - UART / EEPROMs
- 18 - SAR Resistor Network
- 19 - CODEC
- 20 - COLOR LCD INTERFACE

REVISION STATUS OF SHEETS

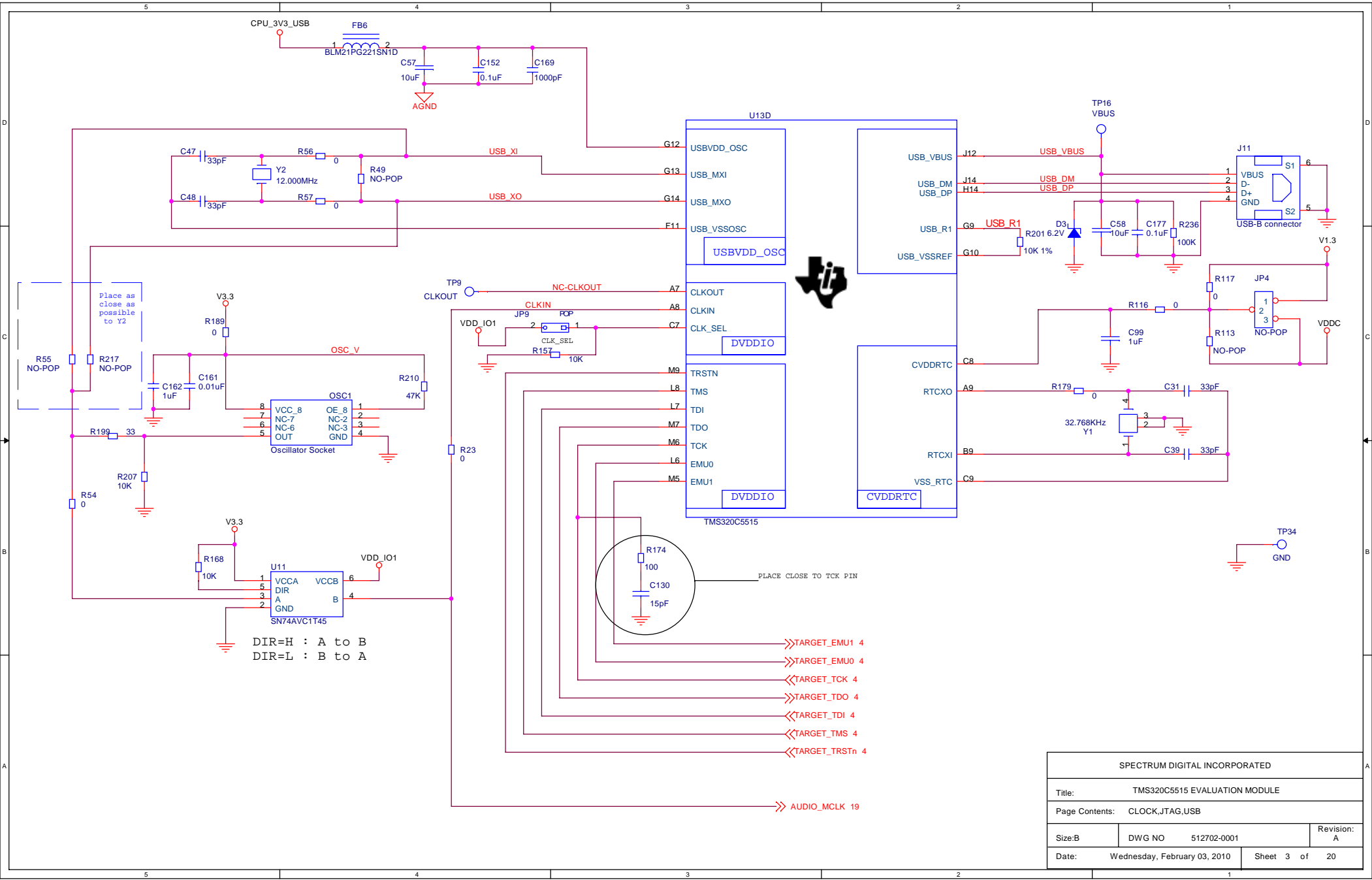
REV	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	DATE	BY	DESCRIPTION			
SHEET																							DWN	R.R.P.	10/15/2009	
REV																								CHK	T.W.K.	10/15/2009
SHEET																								ENGR	R.R.P.	10/15/2009
REV	A	B	A	B	A	A	A	A	A	A														ENGR-MGR	R.R.P.	10/15/2009
SHEET	11	12	13	14	15	16	17	18	19	20														QA	C.M.D.	10/15/2009
REV	B	A	A	A	A	A	A	A	A	A	NEXT ASSY	USED ON												MFG	R.R.P.	10/15/2009
SHEET	1	2	3	4	5	6	7	8	9	10	APPLICATION													RLSE	R.R.P.	10/15/2009

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: TITLE SHEET			
Size: B	DWG NO	512702-0001	Revision: B
Date: Monday, February 01, 2010		Sheet 1 of 20	

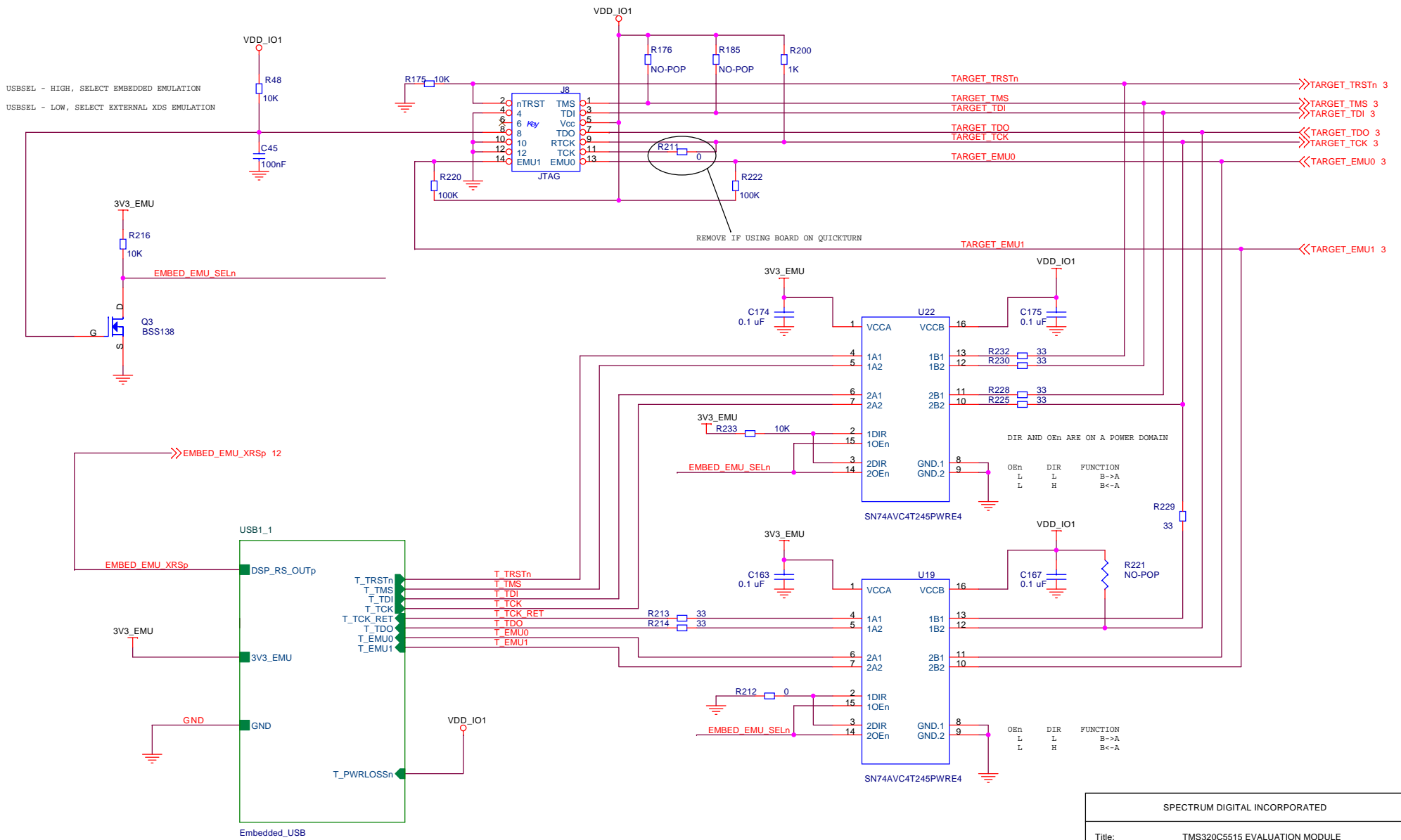


I2C Probe Point Headers

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: GPIO,MMC-SD,SPI,I2C,I2S			
Size: B	DWG NO	512702-0001	Revision: A
Date:	Monday, February 01, 2010	Sheet 2 of	20

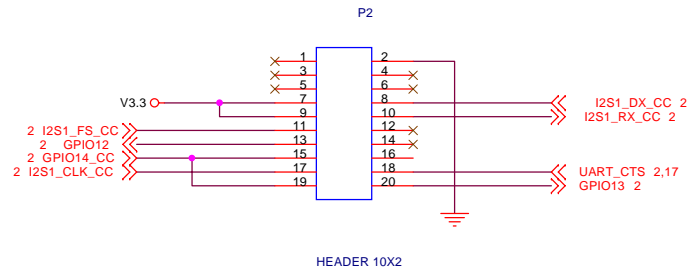
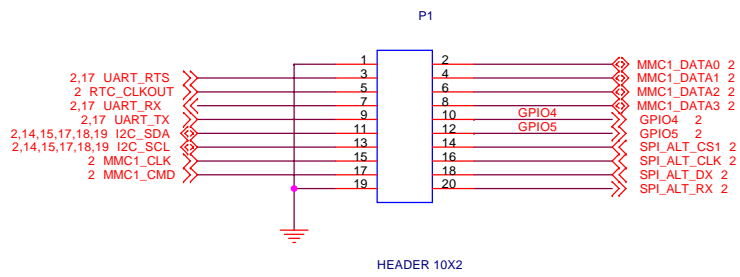


SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: CLOCK,JTAG,USB			
Size:B	DWG NO	512702-0001	Revision: A
Date:	Wednesday, February 03, 2010	Sheet 3 of	20

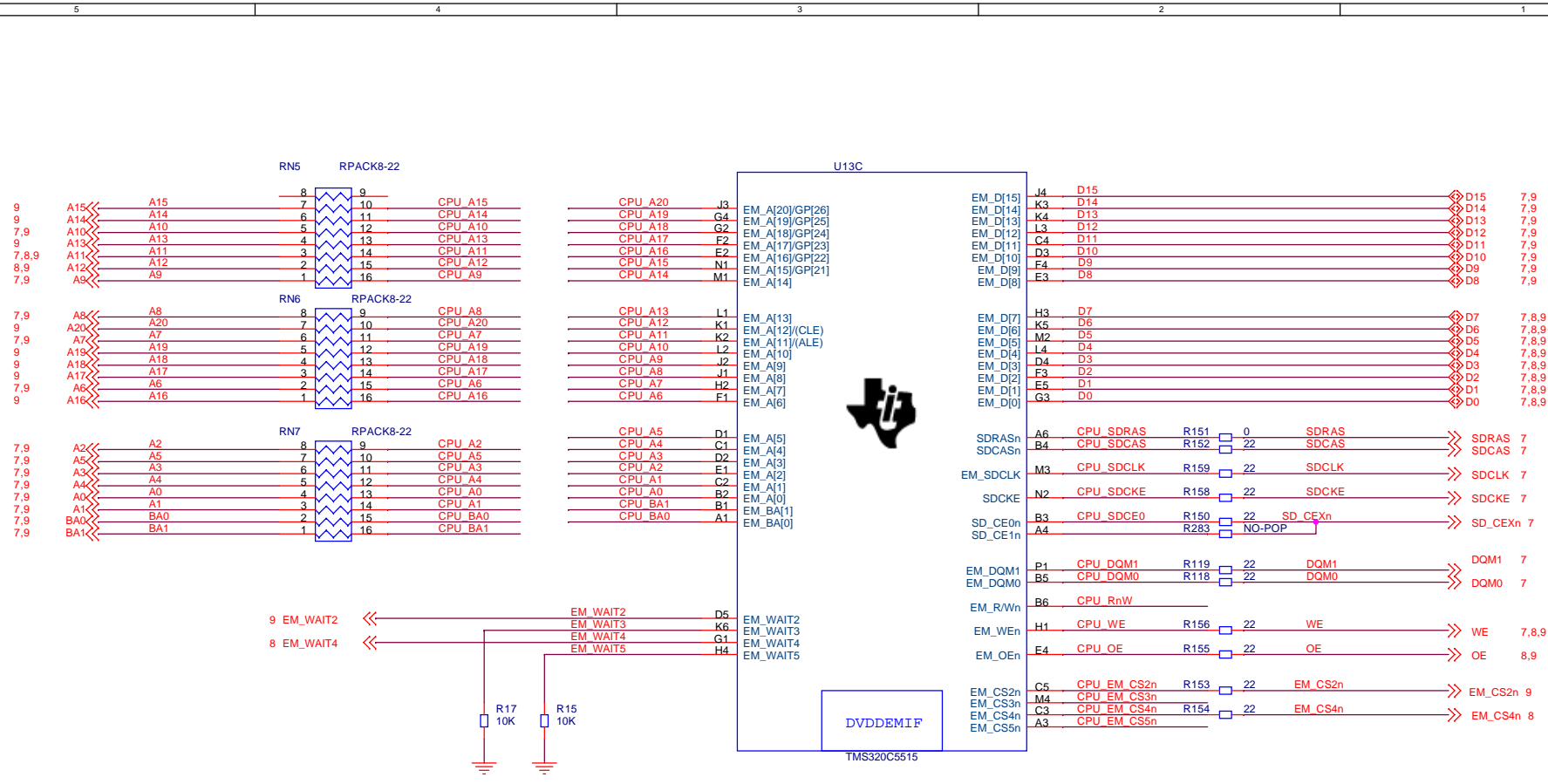


SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: JTAG INTERFACE			
Size: B	DWG NO	512702-0001	Revision: A
Date:	Monday, February 01, 2010	Sheet 4	of 20

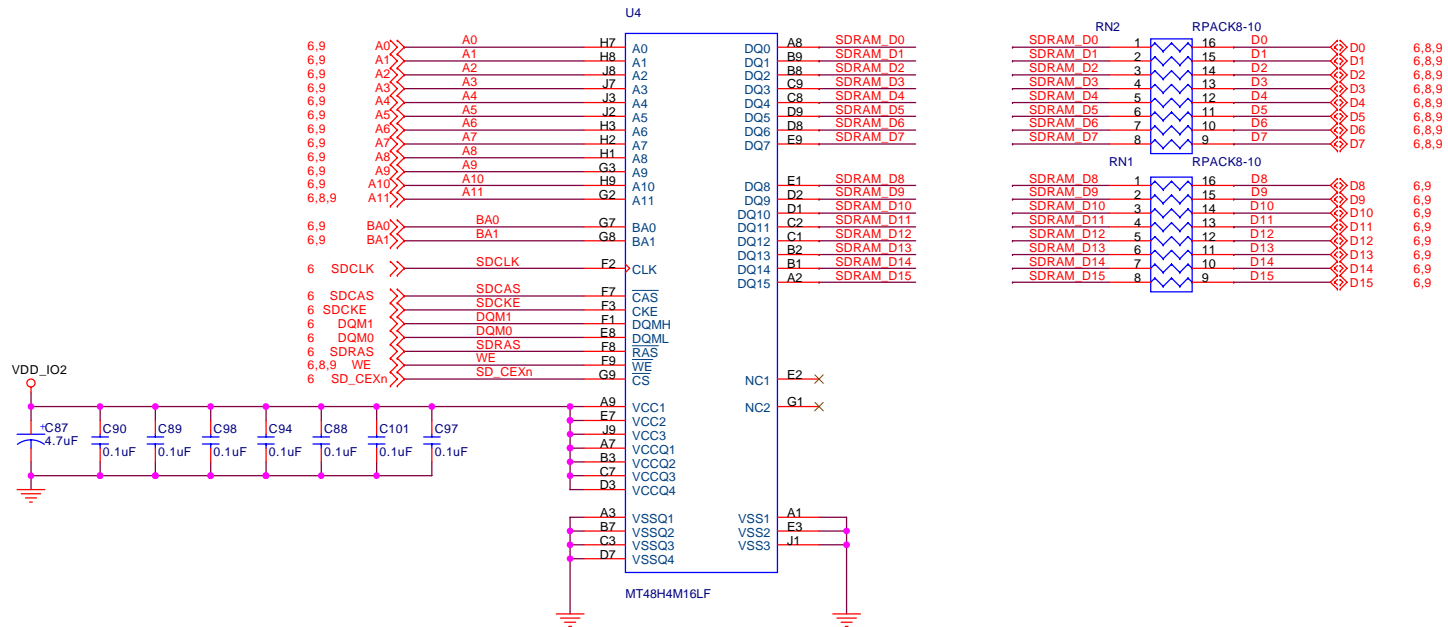
NOTE: DIMENSIONS AND LOCATIONS OF THESE CONNECTORS MUST MEET SPECIFICATION FOR INTERFACE MODULES



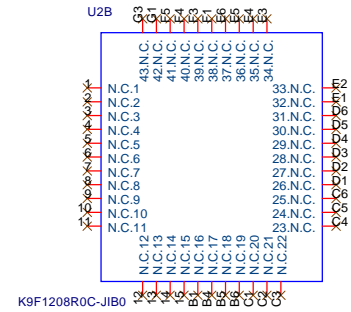
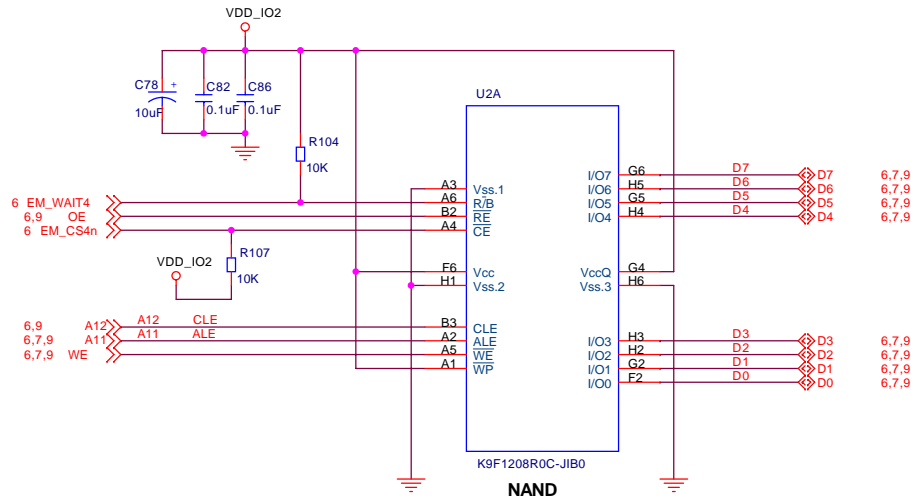
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: CC BOARD INTERFACE			
Size: B	DWG NO	512702-0001	Revision: A
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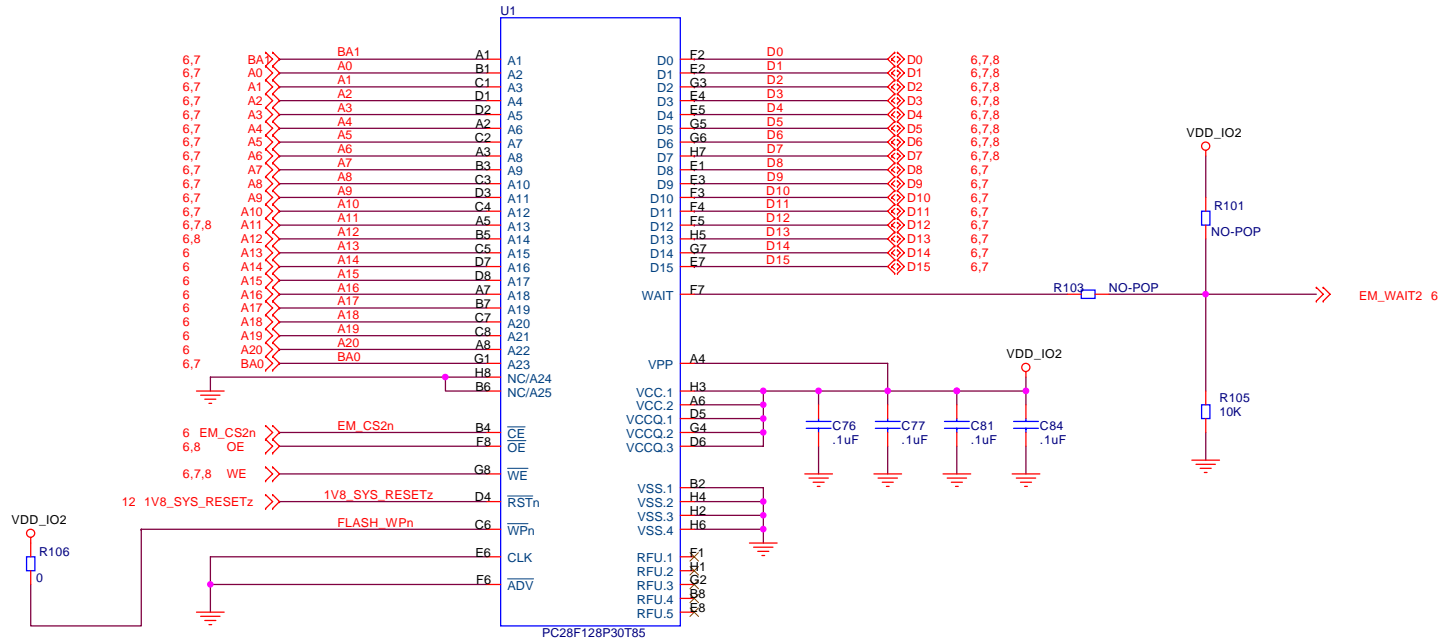
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Title: TMS320C5515 EVALUATION MODULE			
Page Contents: TMS320C5515 EMIF			
Size: B	DWG NO	512702-0001	Revision: A
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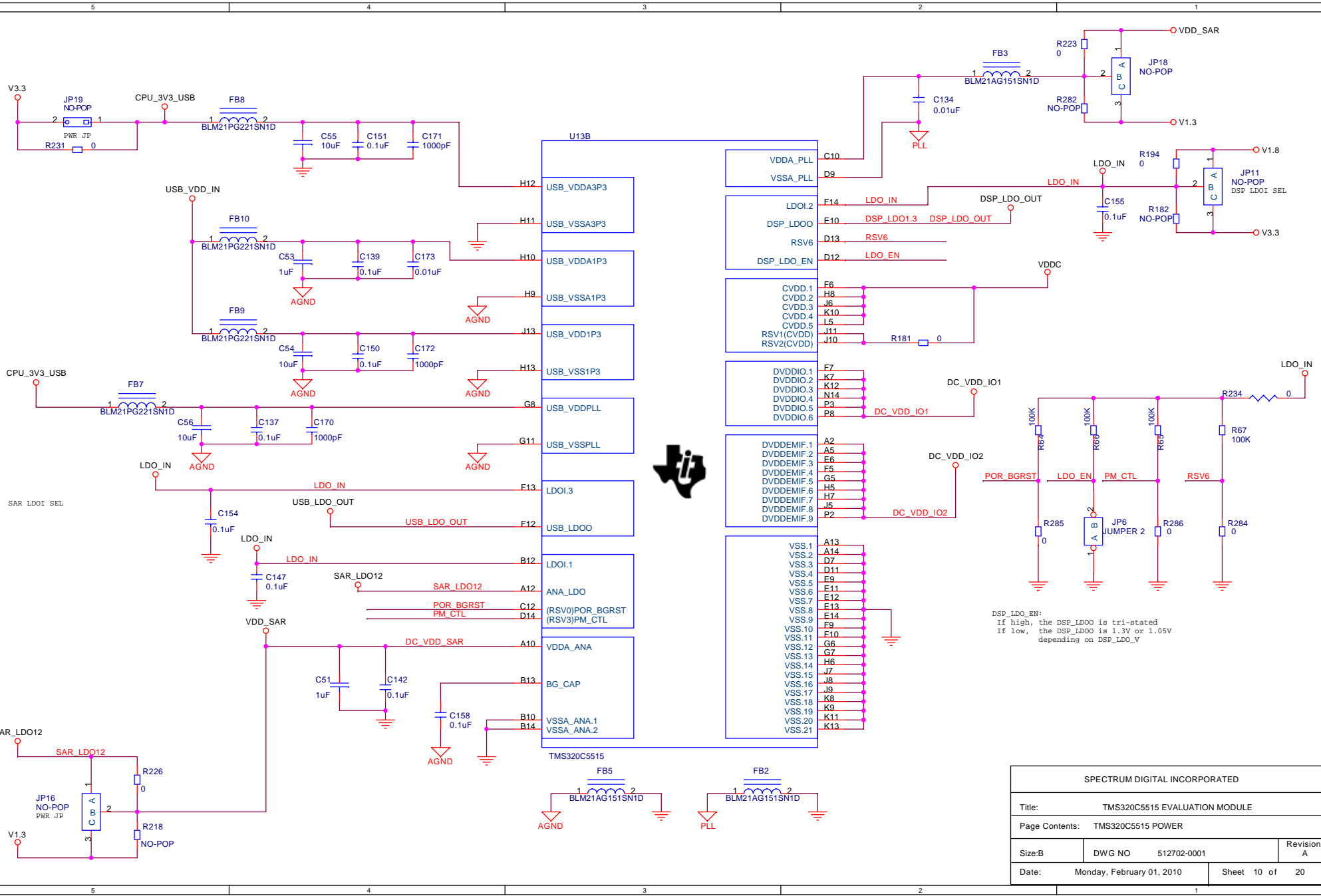
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: MOBILE SDRAM			
Size: B	DWG NO	512702-0001	Revision: A
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SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: NAND FLASH			
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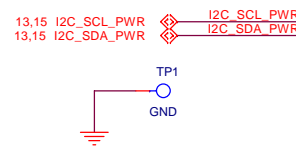
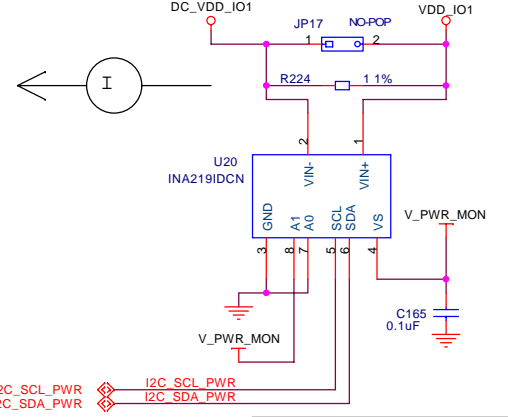
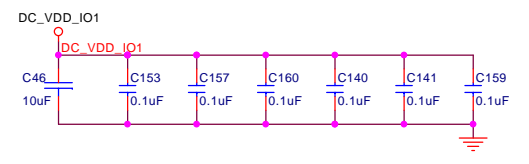
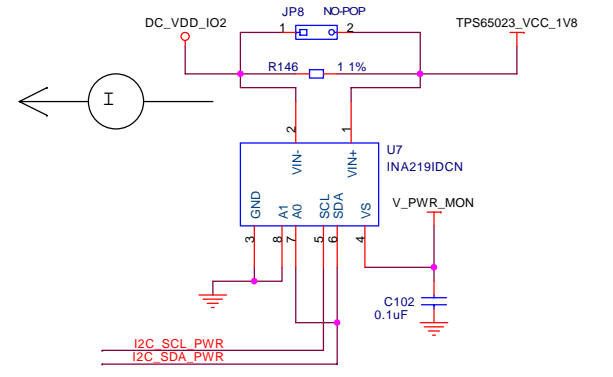
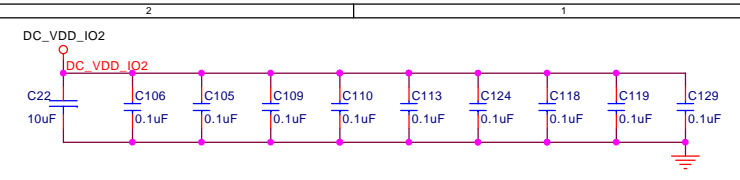
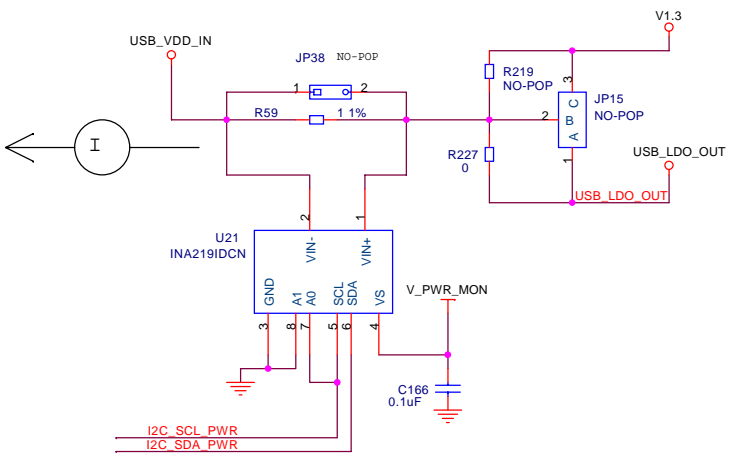
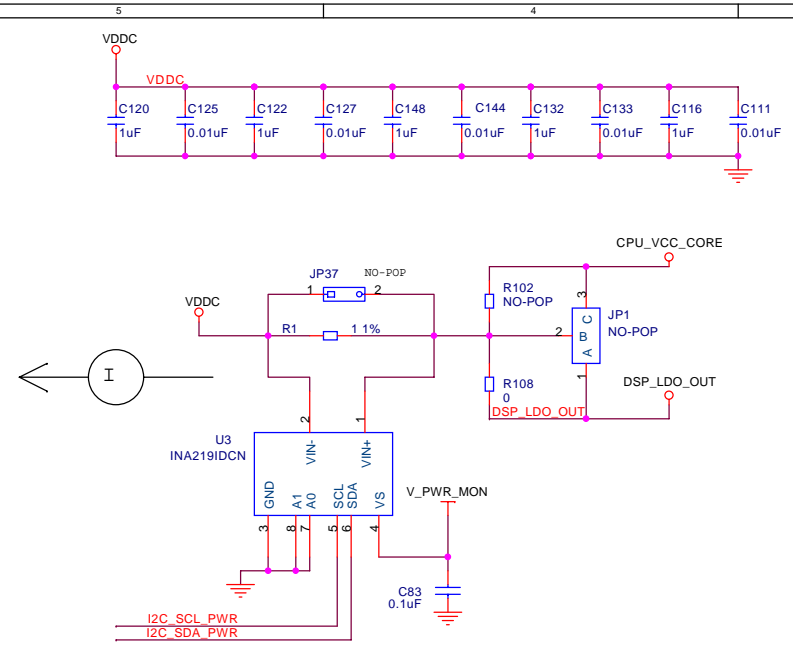


SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: NOR FLASH			
Size: B	DWG NO	512702-0001	Revision: A
Date:	Monday, February 01, 2010	Sheet 9 of 20	

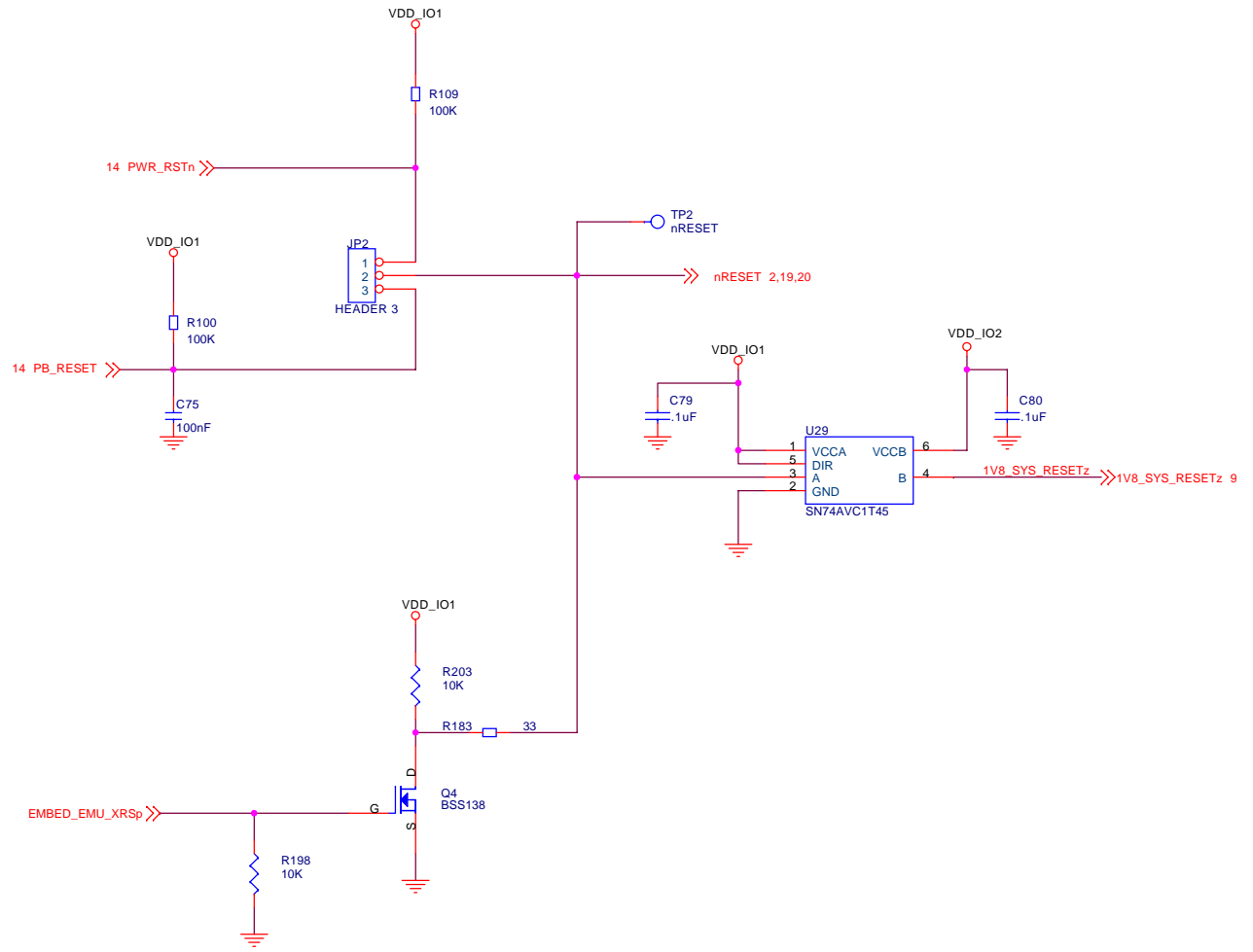


DSP_LDO_EN:
 If high, the DSP_LDO0 is tri-stated
 If low, the DSP_LDO0 is 1.3V or 1.05V
 depending on DSP_LDO_V

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: TMS320C5515 POWER			
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Date: Monday, February 01, 2010	Sheet 10 of 20		

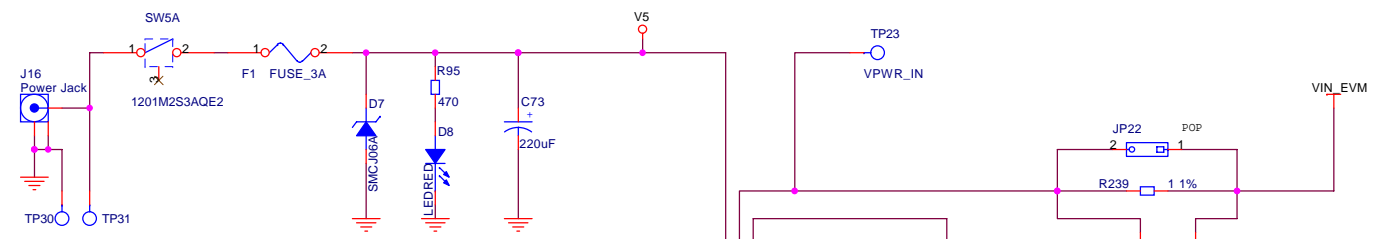


SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: CPU DECOUPLING CAPS			
Size: B	DWG NO	512702-0001	Revision: A
Date:	Monday, February 01, 2010	Sheet	11 of 20

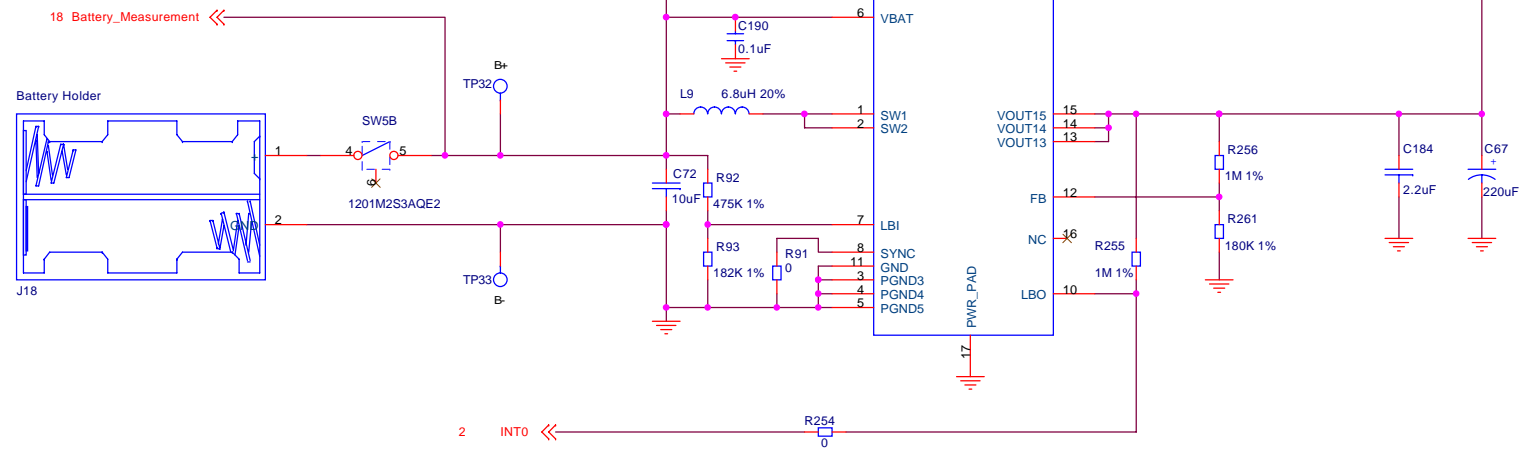
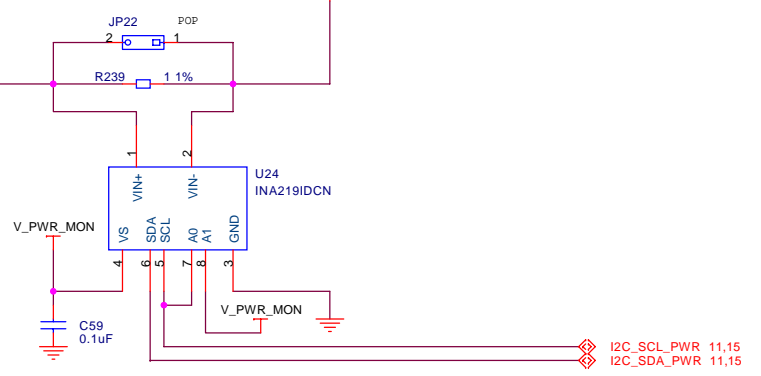


SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: RESET			
Size: B	DWG NO	512702-0001	Revision: B
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5 4 3 2 1



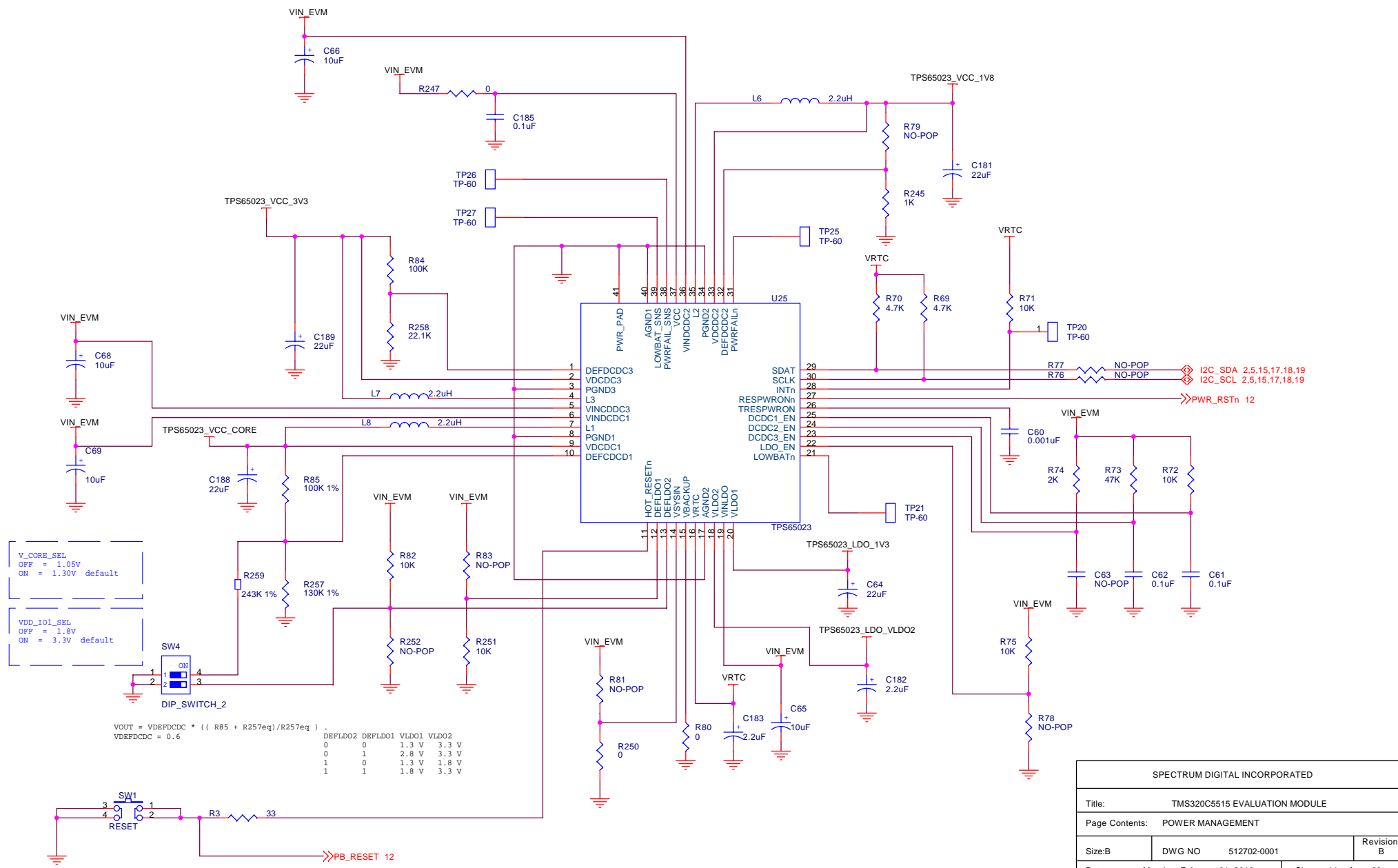
Max Input: 5VDC, 4 A.
Use only recommended power supplies.



18 Battery_Measurement <<<

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: POWER IN			
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5 4 3 2 1



V_CORE_SEL
 OFF = 1.05V
 ON = 1.30V default

VDD_IO1_SEL
 OFF = 1.8V
 ON = 3.3V default

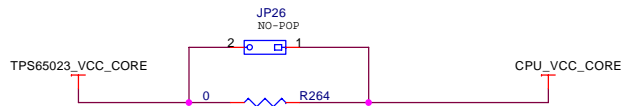
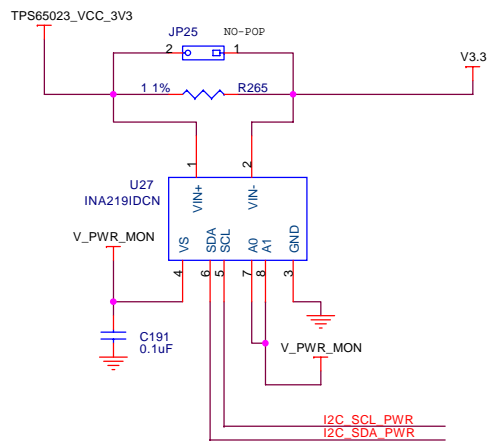
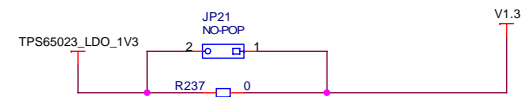
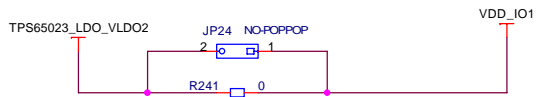
$$V_{OUT} = V_{DEFDCDC} * ((R_{85} + R_{257eq}) / R_{257eq})$$

$$V_{DEFDCDC} = 0.6$$

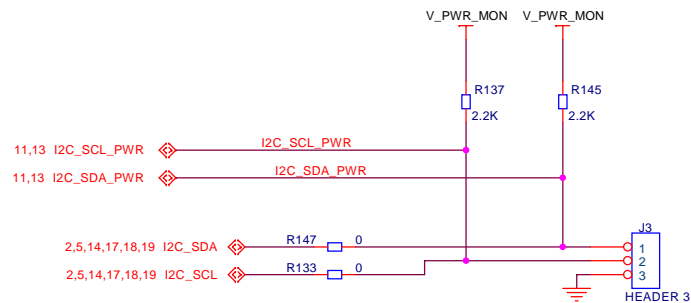
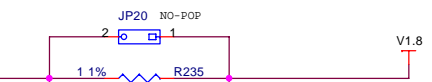
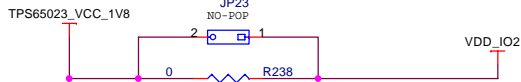
DEFVLD02	DEFVLD01	VLD01	VLD02
0	0	1.3 V	3.3 V
0	1	2.8 V	3.3 V
1	0	1.3 V	1.8 V
1	1	1.8 V	3.3 V

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: POWER MANAGEMENT			
Size: B	DWG NO: 512702-0001	Revision: B	
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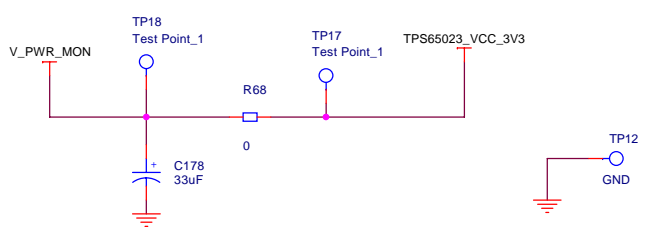
VDD_IO1 Power Supply



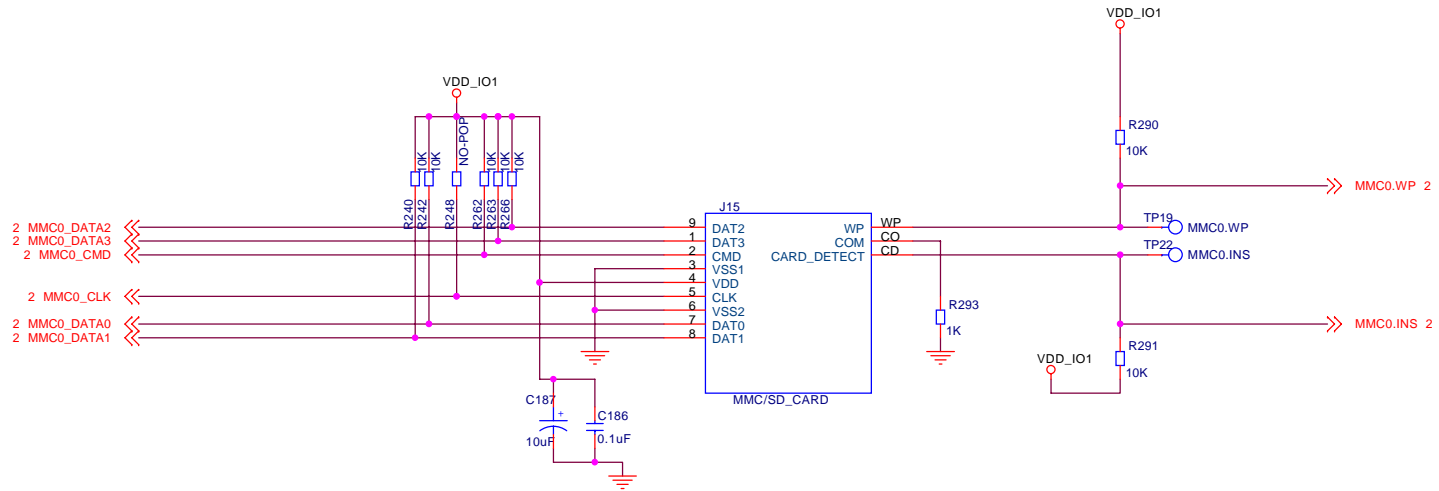
VDD_IO2 Power Supply



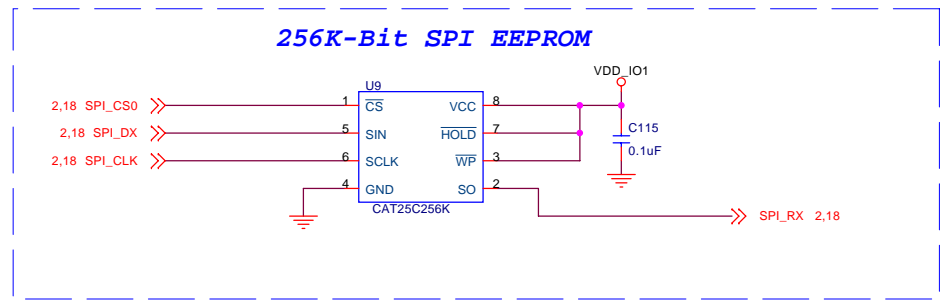
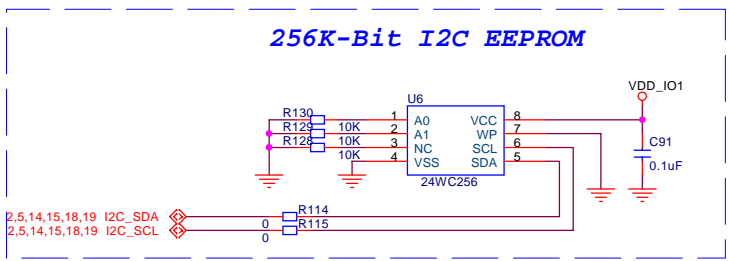
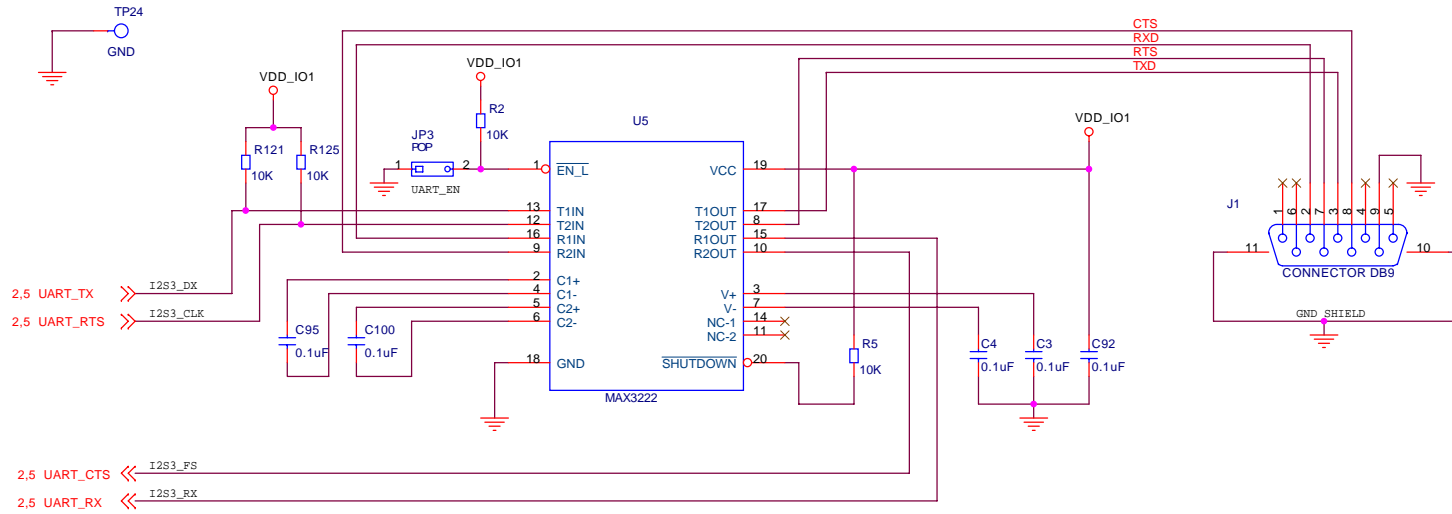
Power Monitor I2C Probe Point Headers



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: POWER ROUTER			
Size: B	DWG NO	512702-0001	Revision: A
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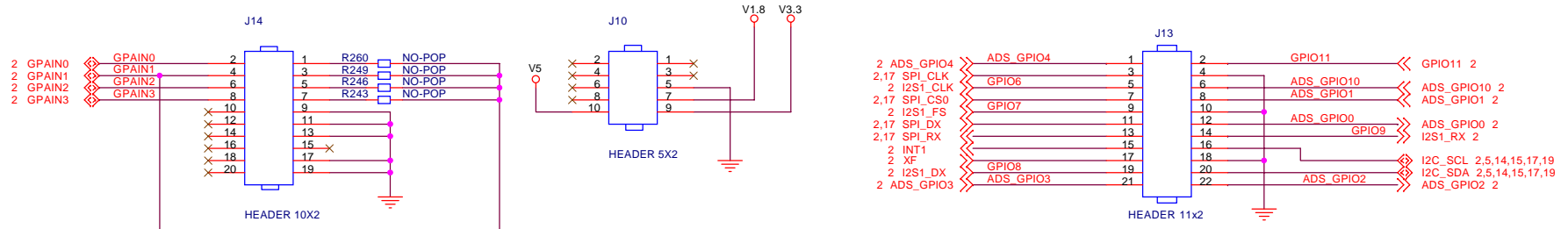
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: MMC/SD SOCKETS			
Size: B	DWG NO	512702-0001	Revision: A
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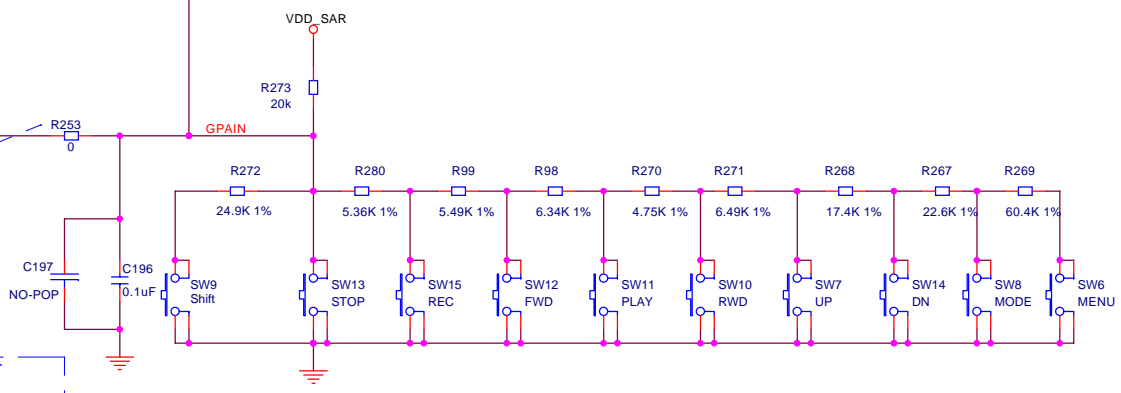
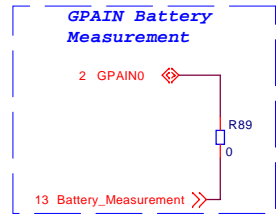
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: RS232(UART)/EEPROM			
Size: B	DWG NO	512702-0001	Revision: A
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LAYOUT NOTE:

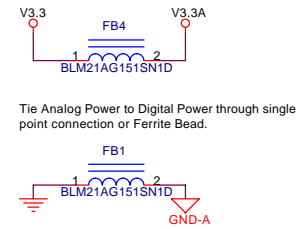
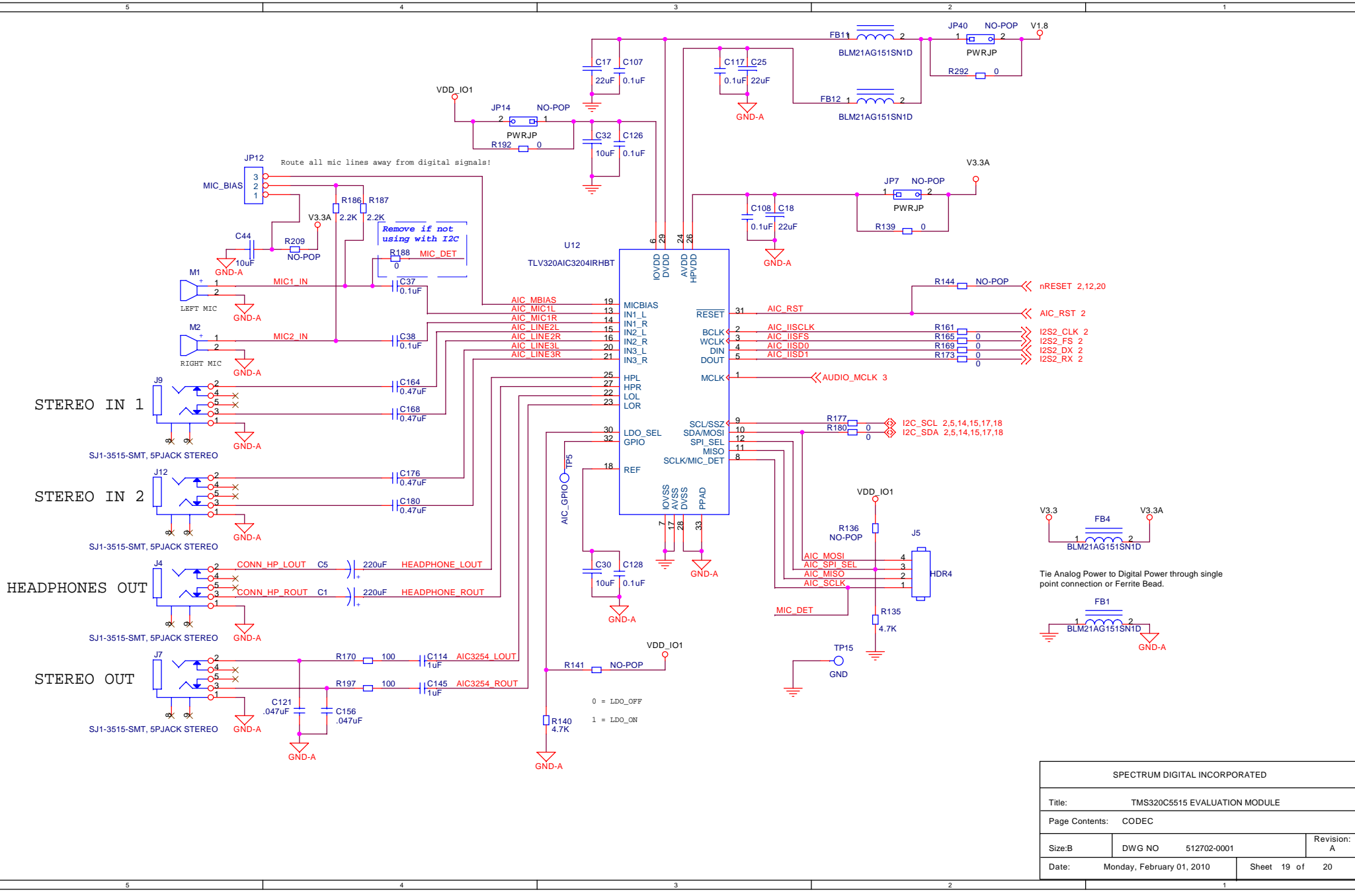
These 3 connectors need to be placed in specific coordinates to allow for Interfacing to the ADS Codec Daughter Card



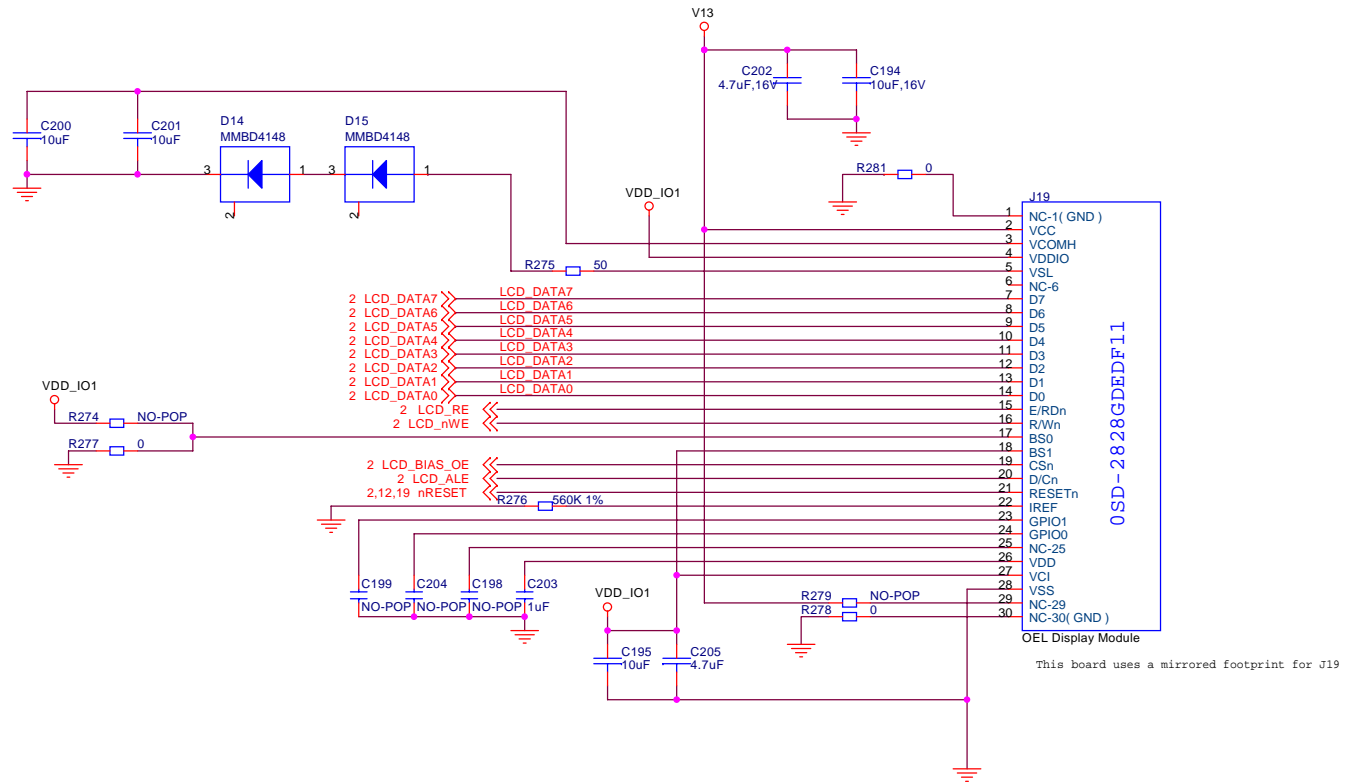
Remove R105 for Internal Voltage Measurement



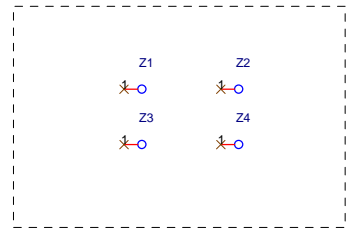
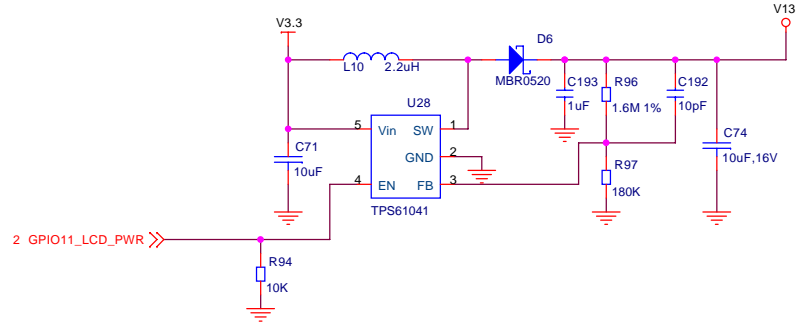
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320C5515 EVALUATION MODULE			
Page Contents: SAR RESISTOR NETWORK			
Size: B	DWG NO	512702-0001	Revision: A
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SPECTRUM DIGITAL INCORPORATED			
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Page Contents: CODEC			
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LCD POWER 13V



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