NOTES, UNLESS OTHERWISE SPECIFIED:
1. RESISTANCE VALUES IN OHMS.
2. CAPACITANCE VALUES IN MICROFARADS.
3. REFERENCE DESIGNATORS USED:
4. ALL 0.1 uF AND 0.01uF CAPACITORS ARE DECOUPLING CAPS UNLESS OTHERWISE NOTED. THEY ARE SHOWN ON THE PAGE WITH THE INTEGRATED CIRCUITS THEY SHOULD BE PLACED NEAR.

Schematic Contents

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20 - COLOR LCD INTERFACE

Update TPS65023 Enable Logic 12/15/2009 RRP
NOTE: DIMENSIONS AND LOCATIONS OF THESE CONNECTORS MUST MEET SPECIFICATION FOR INTERFACE MODULES
Max Input: 5VDC, 4 A.
Use only recommended power supplies.
LAYOUT NOTE:
These 3 connectors need to be placed in specific coordinates to allow for Interfacing to the ADS Codec Daughter Card.

Remove R105 for Internal Voltage Measurement.
Route all mic lines away from digital signals!

Remove if not using with I2C.

0 = LDO_OFF
1 = LDO_ON

Tie Analog Power to Digital Power through single point connection or Ferrite Bead.

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Title: TMS320C3515 EVALUATION MODULE

Page Contents: CODEC

Revision: A

Date: Monday, February 01, 2010  Sheet 19 of 20
This board uses a mirrored footprint for J19.