

PWB 512081 REV B  
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PWB 512081 REV B  
10-27-09  
BOTTOM SIDE - LAYER 8

B49 C22 B78 C24 B28 C22 B11 C20  
 B79 B29 C21 C23 B74 B75 B76 B77 B78 B79  
 B80 B81 B82 B83 B84 B85 B86 B87 B88 B89  
 B90 B91 B92 B93 B94 B95 B96 B97 B98 B99  
 C01 C02 C03 C04 C05 C06 C07 C08 C09 C10  
 C11 C12 C13 C14 C15 C16 C17 C18 C19 C20  
 C21 C22 C23 C24 C25 C26 C27 C28 C29 C30  
 C31 C32 C33 C34 C35 C36 C37 C38 C39 C40  
 C41 C42 C43 C44 C45 C46 C47 C48 C49 C50  
 C51 C52 C53 C54 C55 C56 C57 C58 C59 C60  
 C61 C62 C63 C64 C65 C66 C67 C68 C69 C70  
 C71 C72 C73 C74 C75 C76 C77 C78 C79 C80  
 C81 C82 C83 C84 C85 C86 C87 C88 C89 C90  
 C91 C92 C93 C94 C95 C96 C97 C98 C99 C100

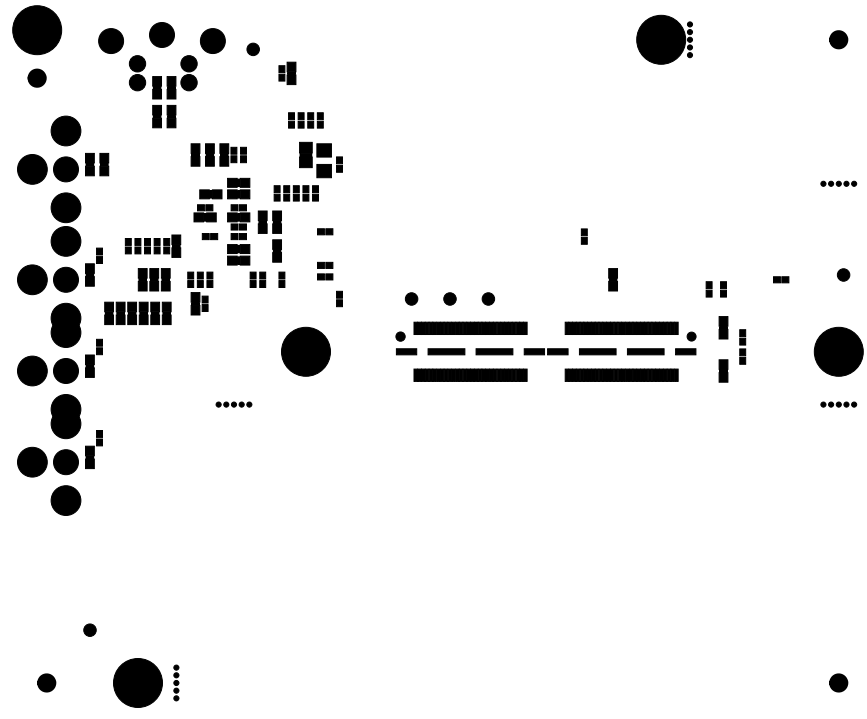


D1  
 P1

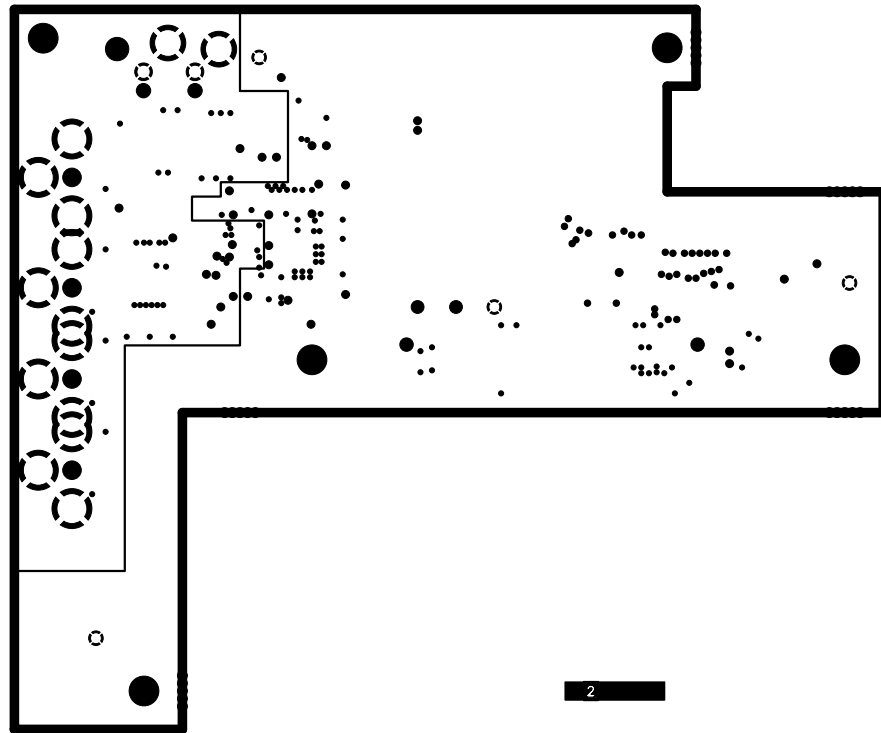
B51 C33

B18 B18 C33  
 B58  
 B45 C28

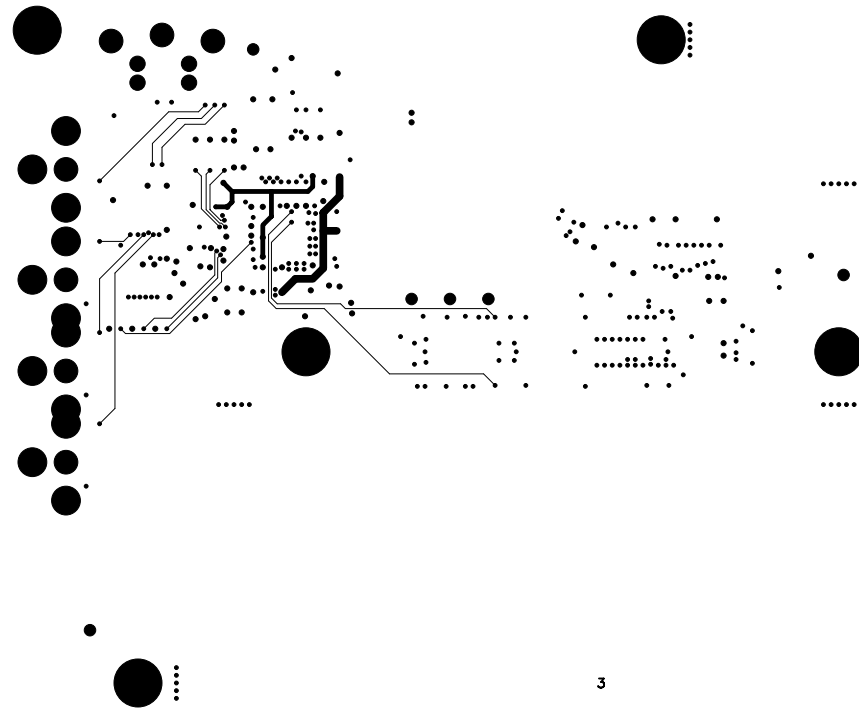
PWB 512081 REV B  
 10-27-09  
 BOTTOM SIDE SILKSCREEN



PWB 512081 REV B  
10-27-09  
BOTTOM SIDE SOLDERMASK

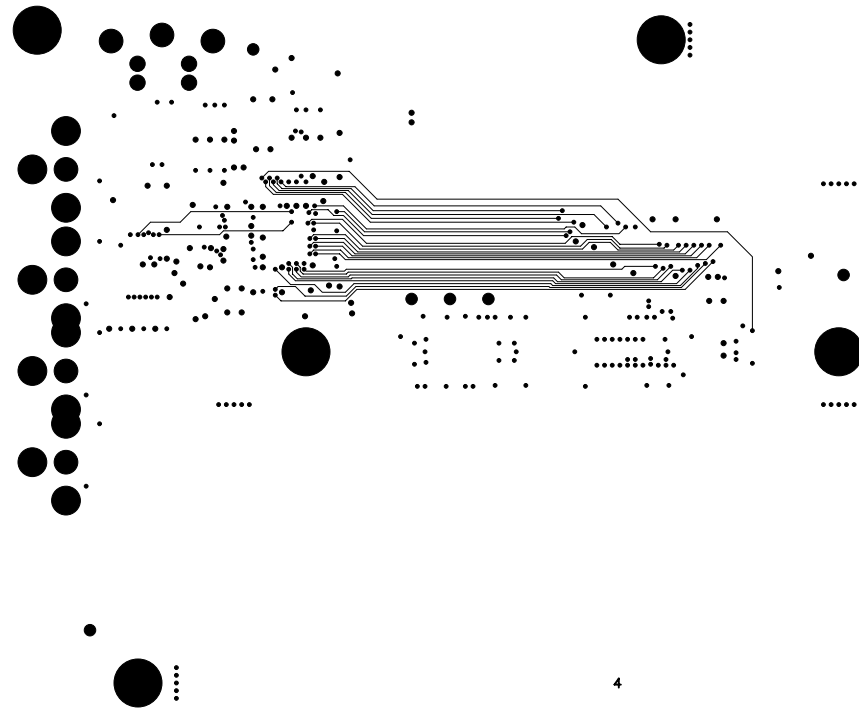


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10-27-09  
GND PLANE - LAYER 2



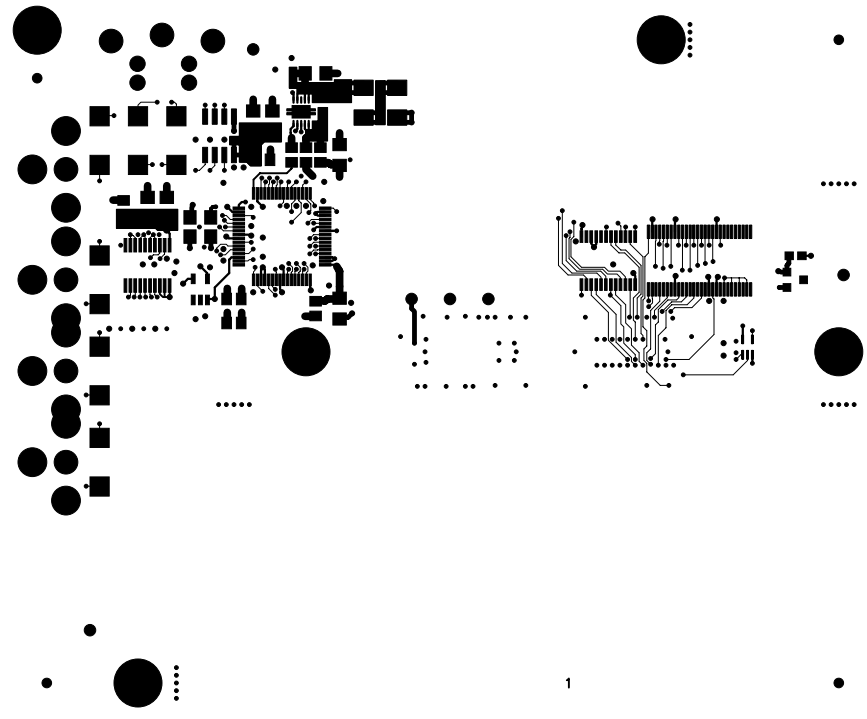
3

PWB 512081 REV B  
10-27-09  
MID LAYER 1 - LAYER 3

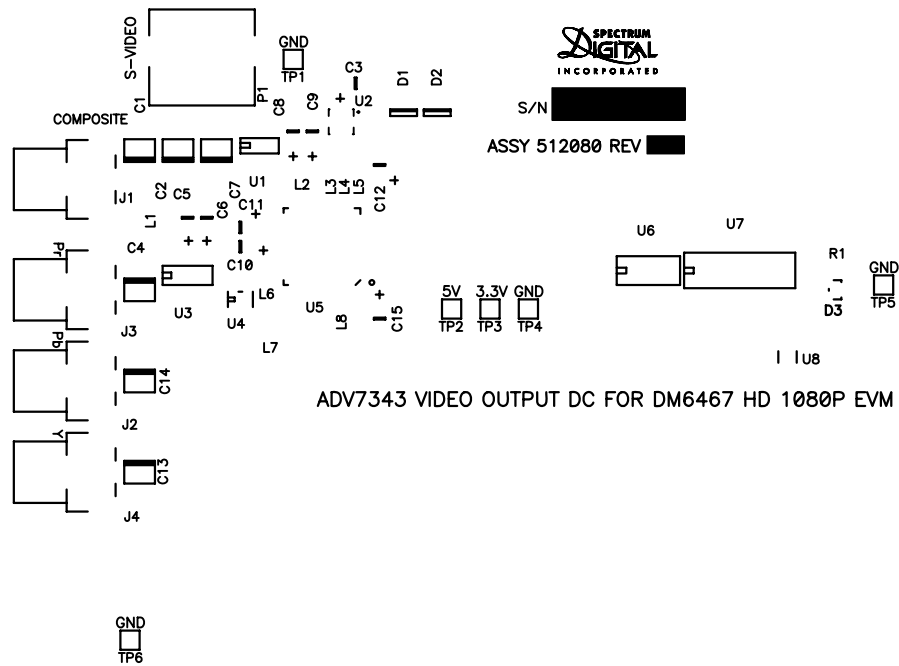


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10-27-09  
MID LAYER 2 - LAYER 4

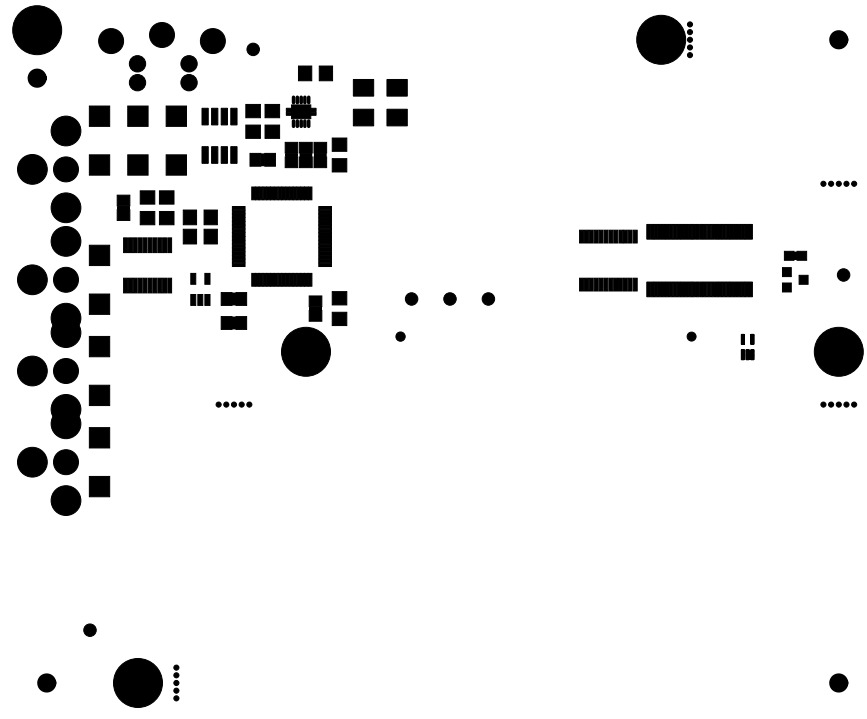




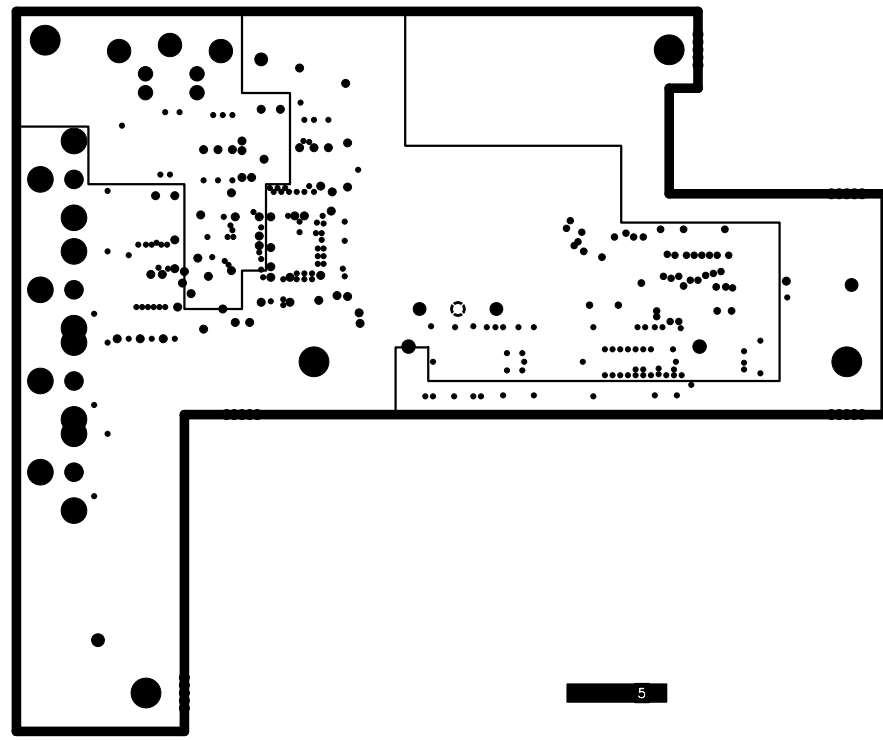
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TOP SIDE - LAYER 1



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 10-27-09  
 TOP SIDE SILKSCREEN



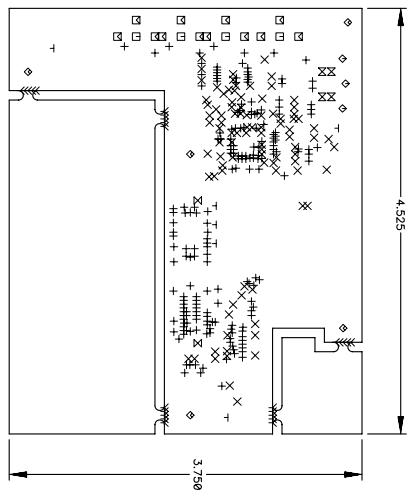
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10-27-09  
TOP SIDE SOLDERMASK



PWB 512081 REV B  
10-27-09  
VCC PLANE - LAYER 5

NOTES: UNLESS OTHERWISE SPECIFIED

- 1 MATERIAL: HIGH TEMP FR4 (TG>=170). THICKNESS: .062.
- 2 DIMENSIONS APPLY TO THE CENTER OF HOLES AND PADS.
- 3 MINIMUM COPPER WALL THICKNESS SHALL BE .001 FOR ALL PLATED THRU HOLES.
- 4 ALL ARTWORK VIEWED FROM COMPONENT SIDE ARE AFTER PLATING.
- 5 MAXIMUM ALLOWABLE LINE REDUCTIONS DUE TO PIN HOLES, NICKS AND LINE SHRINKAGE IS TO BE 20%.
- 6
- 7 INTERNAL AND EXTERNAL CORNERS TO BE .06 RADIUS MAX AT SHIP OPTION.
- 8 EDGE CONNECTION TABS ARE TO BE ELECTROPLATED TO A MINIMUM THICKNESS OF .0005 GOLD OVER A MINIMUM THICKNESS OF .0001 NICKEL.
- 9 SOLDER MASK SHALL BE LIQUID PHOTOIMAGEABLE MATTE FINISH WITH A THICKNESS NOT TO EXCEED 1.5 MILS AND SHALL BE APPLIED TO BOTH SIDES OF BOARD OVER BARE COPPER PER IPC-SM-840, CLASS 2.
- 10 SOLDER MASK COLOR: GREEN.
- 11 EXPOSED CONDUCTOR AREAS TO BE FINISHED WITH ELECTROLESS NICKEL THICKNESS .000050 MINIMUM, PLUS IMMERSION GOLD THICKNESS .000005 MINIMUM.
- 12 SILKSCREEN WITH WHITE EPOXY BASED INK PER SILKSCREEN ARTWORK.
- 13 BOARD IS TO BE 100% ELECTRICALLY TESTED.
- 14 CONTROLLED IMPEDANCE OF 55 OHMS +/- 10 OHMS TO BE WATCHED ON ALL SIGNAL LAYERS ON 5 MIL TRACES.



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FAB DRAWING

TABLE OF LAYERS

NO.	DESCRIPTION	COPPER WT.
1.1	TOP SIDE	1/2 OZ
1.2	GRID PLANE	1.0Z
1.3	MID LAYER 1	1.0Z
1.4	MID LAYER 2	1.0Z
1.5	VCC PLANE	1.0Z
1.6	BOTTOM SIDE	1/2 OZ

Drill Table

Hole Dia (inch)	Symbol	Quantity	Plated
0.010	+	196	Yes
0.012	X	92	Yes
0.022	Y	25	No
0.038	T	6	Yes
0.041	X	2	Yes
0.046	M	4	Yes
0.068	M	3	Yes
0.093	◇	3	Yes
0.104	◇	12	Yes
0.125	◇	5	Yes

UNLESS OTHERWISE SPECIFIED

- DIMENSIONS ARE IN INCHES
- TOLERANCES: ANGLES +/- .1°
- HOLE DIAMETER TOLERANCE
- REMOVE ALL BURRS AND SHARP EDGES
- CONCENTRIC MACHINED DIAMETERS 20/10 R/M
- DIMENSIONAL LIMITS APPLY AFTER PROCESSES

HOLE DIAMETER TOLERANCE

0.13 THRU +.004	0.15 THRU +.006	0.20 THRU +.008
0.25 THRU +.007	0.30 THRU +.008	0.35 THRU +.009
0.40 THRU +.008	0.50 THRU +.009	0.60 THRU +.010
0.75 THRU +.010	1.00 THRU +.012	1.50 THRU +.015

DATE	10-27-09
ENGR-DSSN	RON PETERSON
ENGR-MGR	
QA	
MFG	
SIZE	DRAWING NO 512081
SCALE	NONE



PWB ADV7343 DAUGHTER CARD FOR DM6467 HD 1080P

DMN	OM KRAUSKOPF	DATE	10-27-09
ENGR	DSSN	RON PETERSON	10-27-09
ENGR	MGR		
QA			
MFG			
SIZE	DRAWING NO	512081	REV B
SCALE	NONE		SHEET 1 OF 1