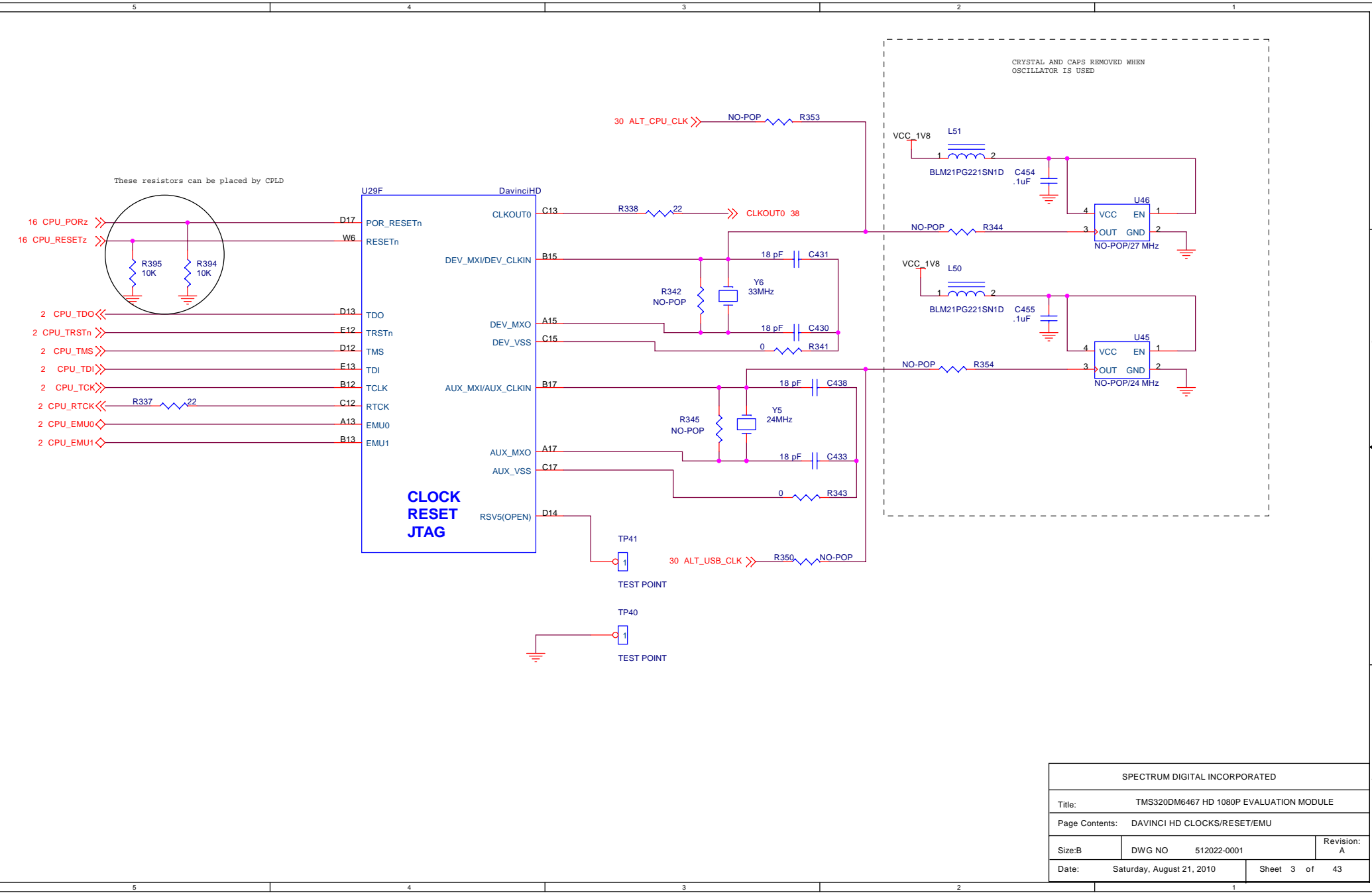
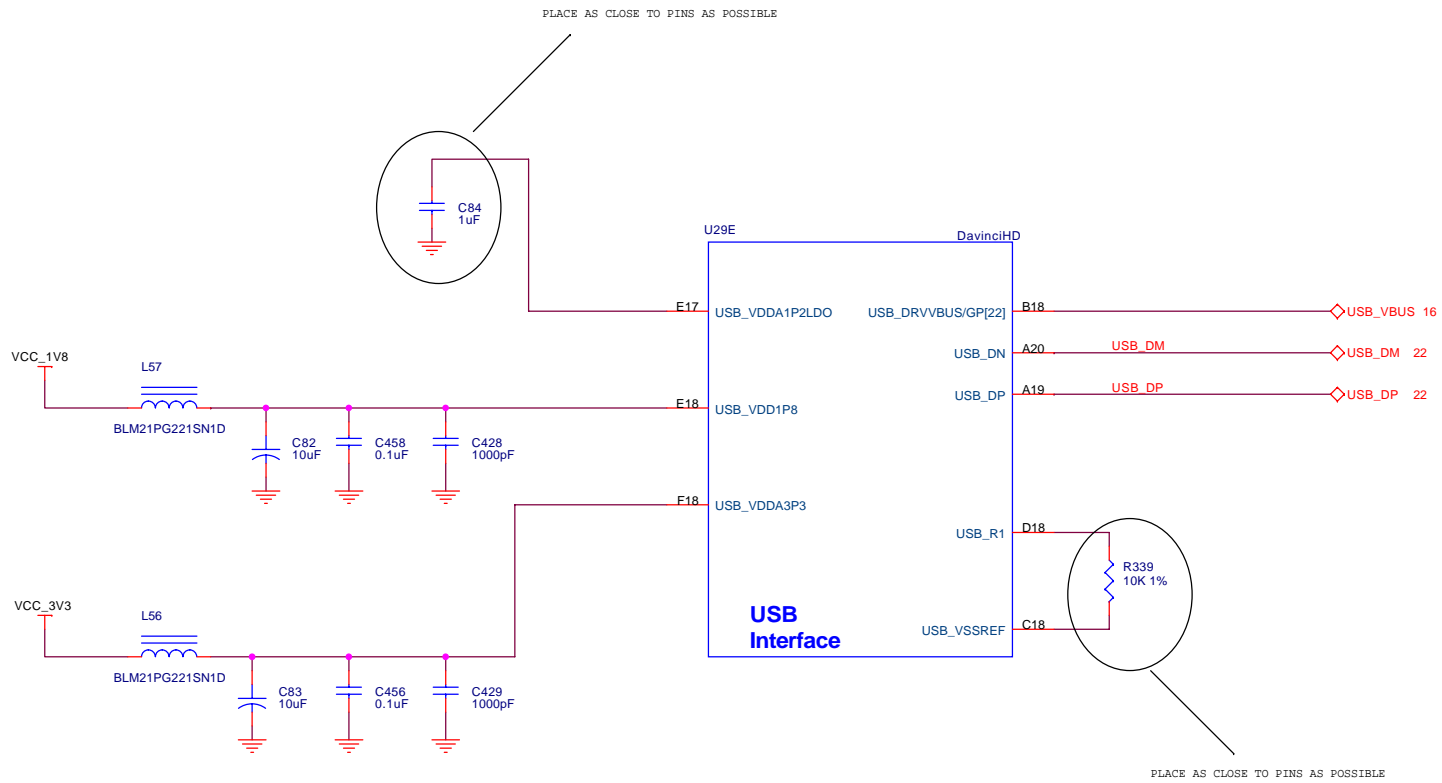


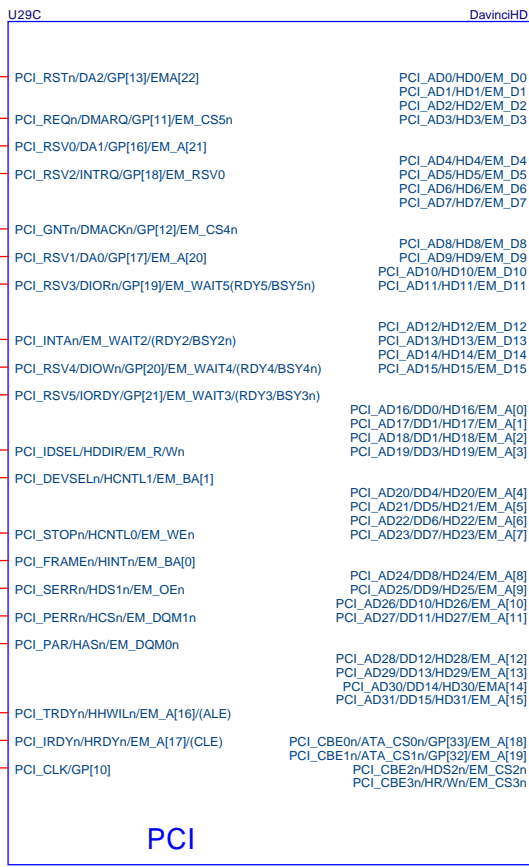
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: TI JTAG14 & ARM JTAG 20 INTERFACES			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010		Sheet 2 of 43



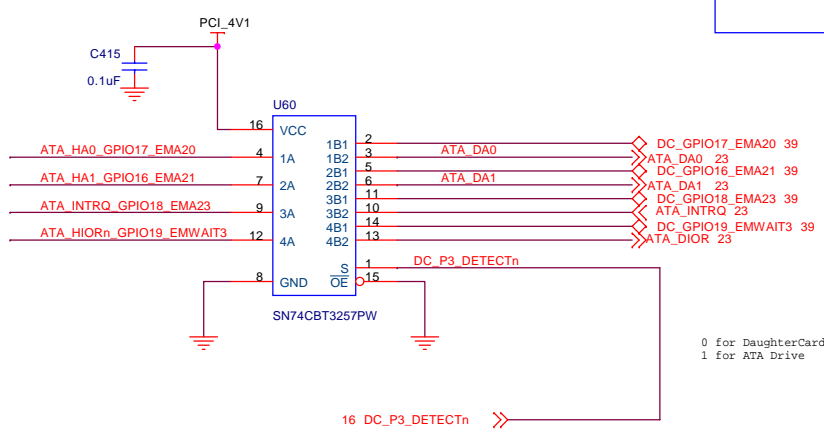
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DAVINCI HD CLOCKS/RESET/EMU			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010		Sheet 3 of 43



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DAVINCI-HD USBII INTERFACE			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 5 of	43



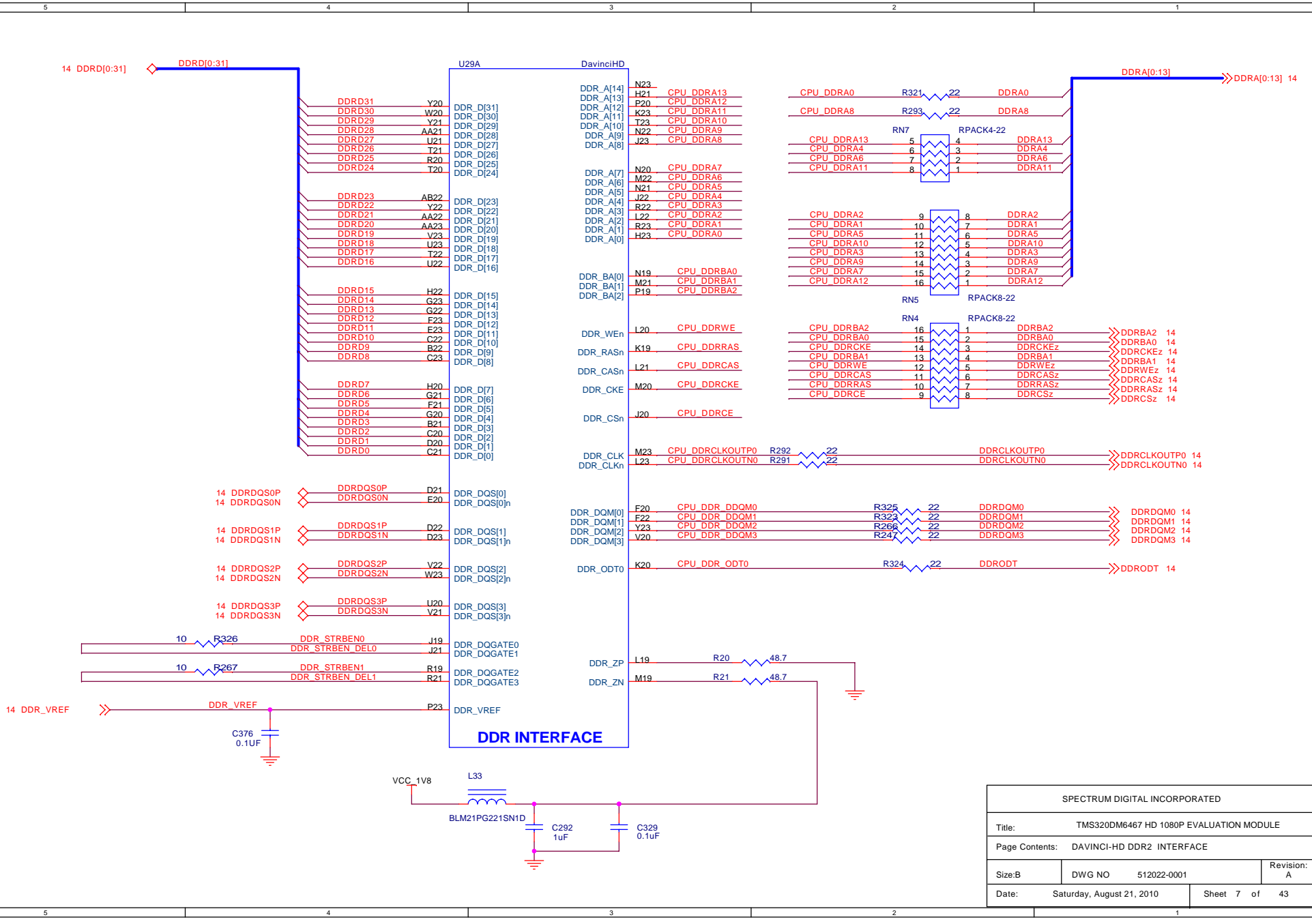
PCI



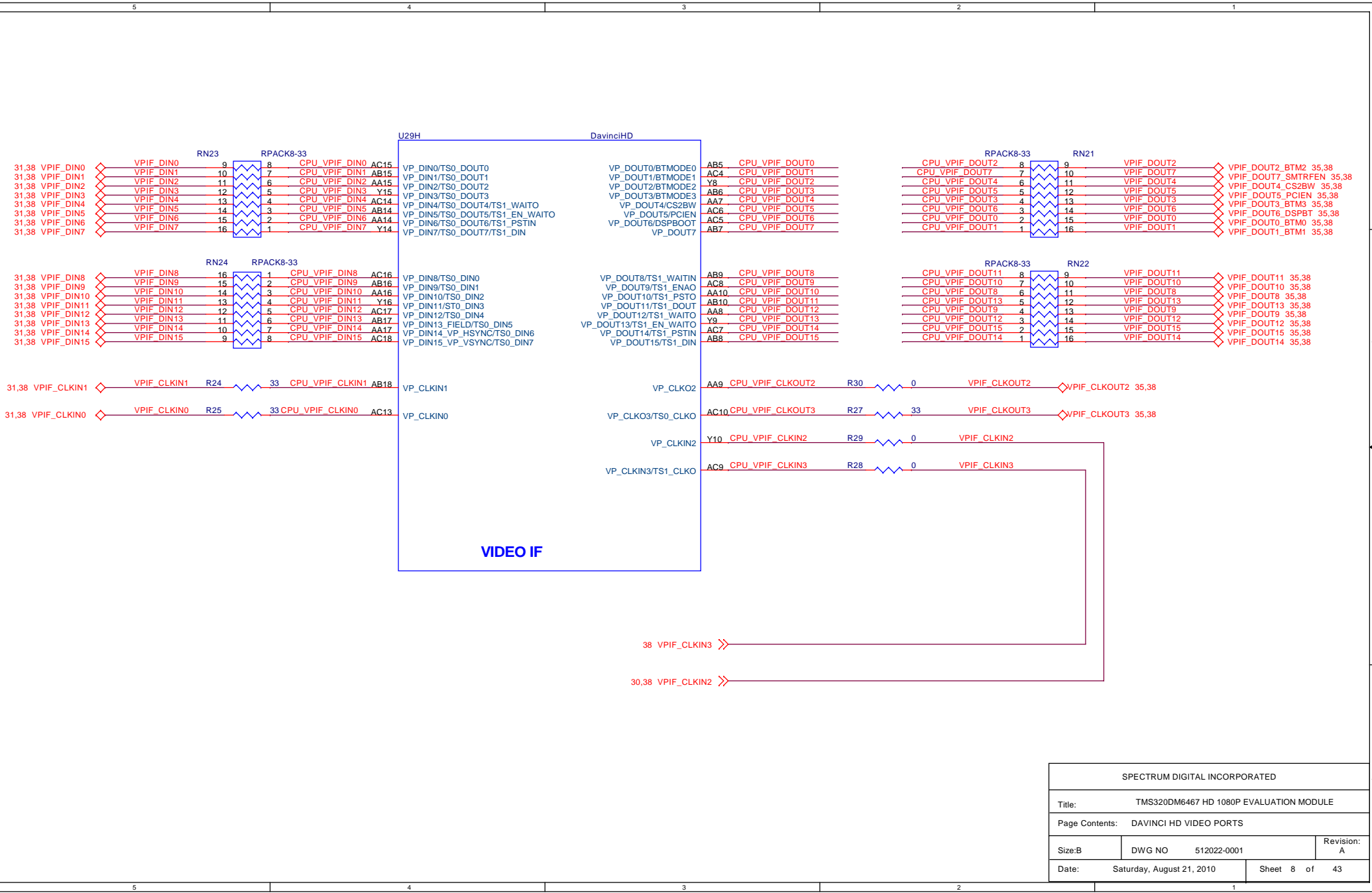
0 for DaughterCard
1 for ATA Drive

16 DC_P3_DETECTn >>

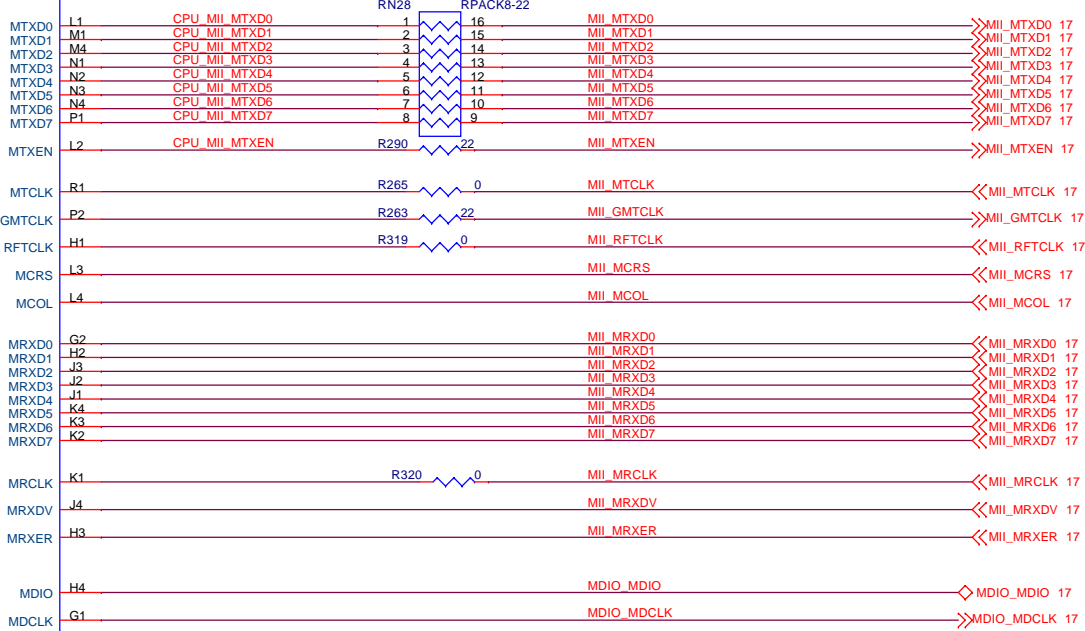
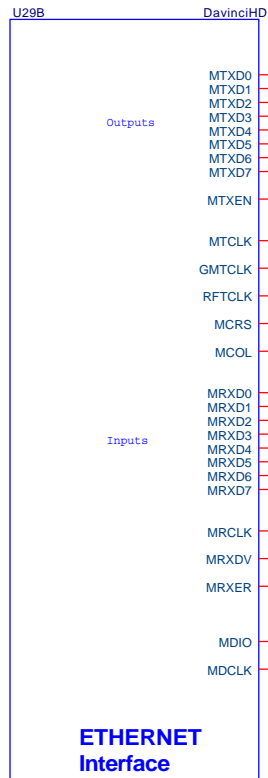
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DAVINCI-HD PCI INTERFACE			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 6 of	43



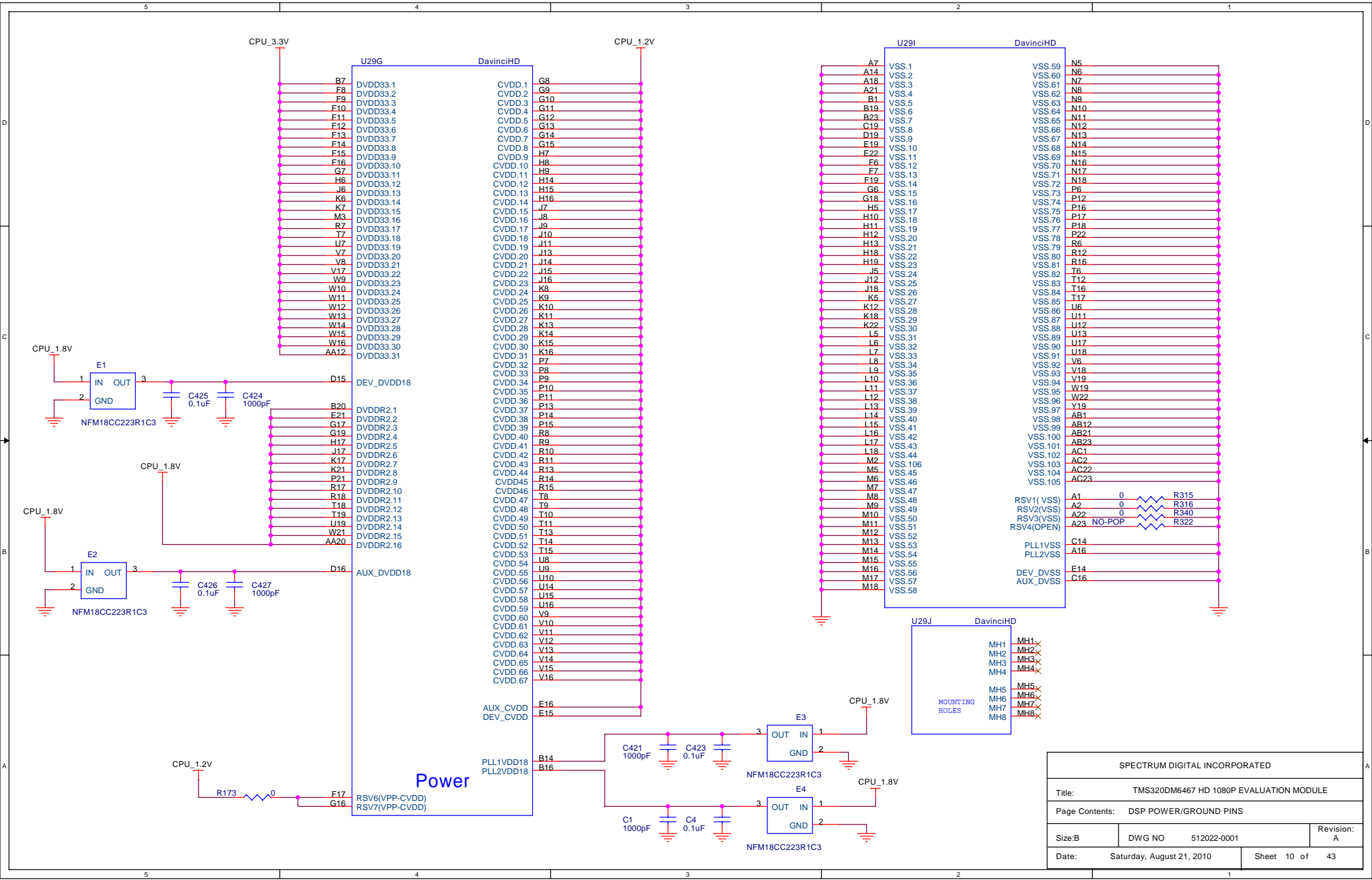
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DAVINCI-HD DDR2 INTERFACE			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 7 of	43



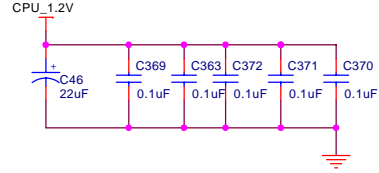
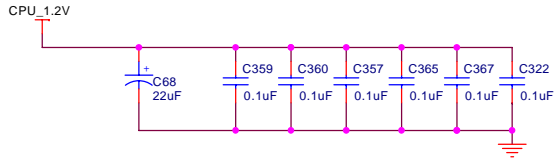
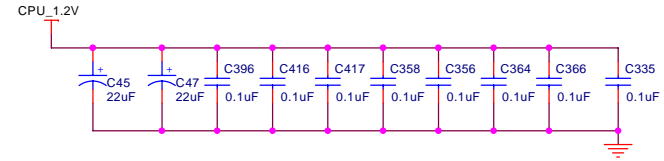
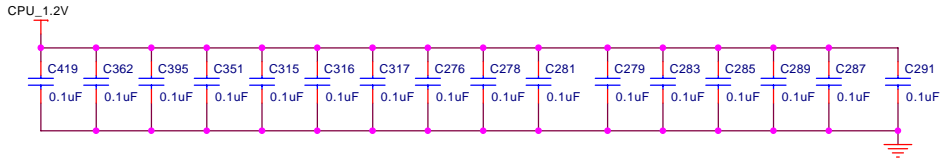
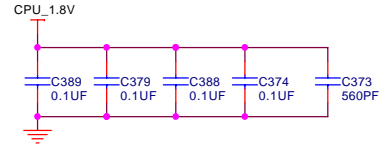
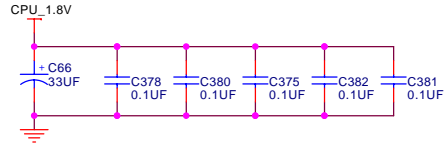
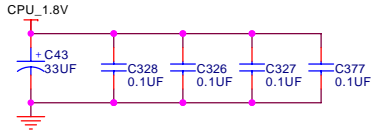
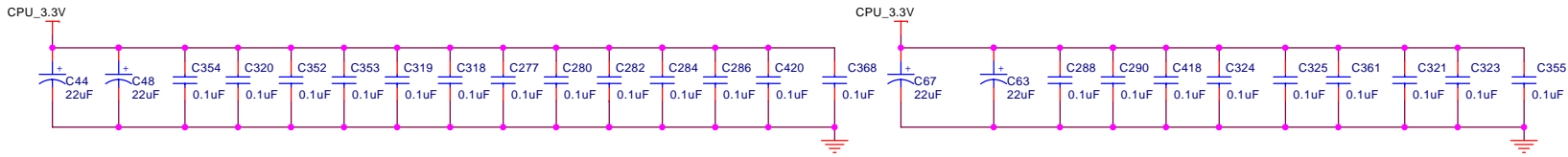
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DAVINCI HD VIDEO PORTS			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 8 of	43



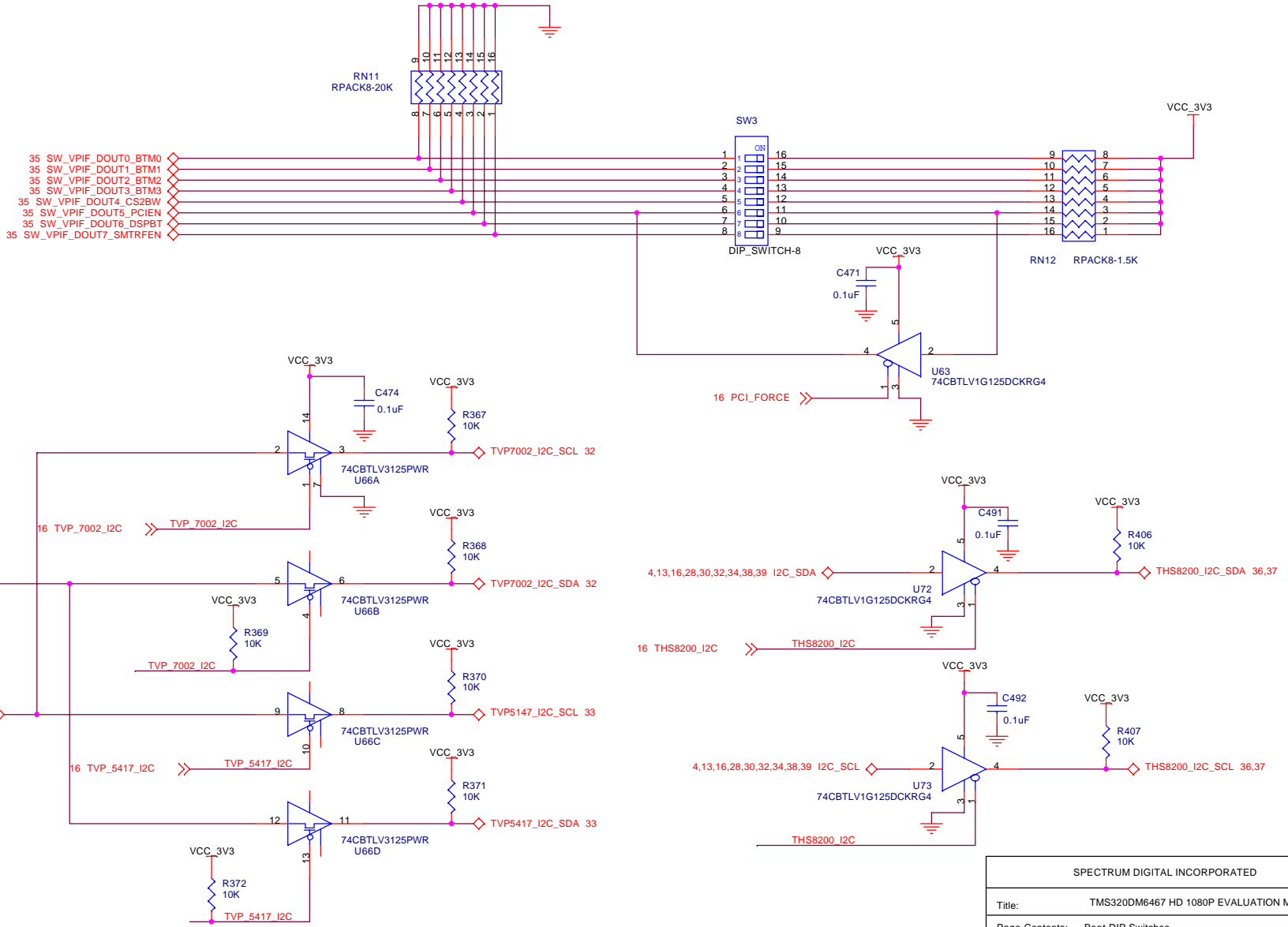
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DAVINCI-HD ENET INTERFACE			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 9 of 43	



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DSP POWER/GROUND PINS			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	10 of 43

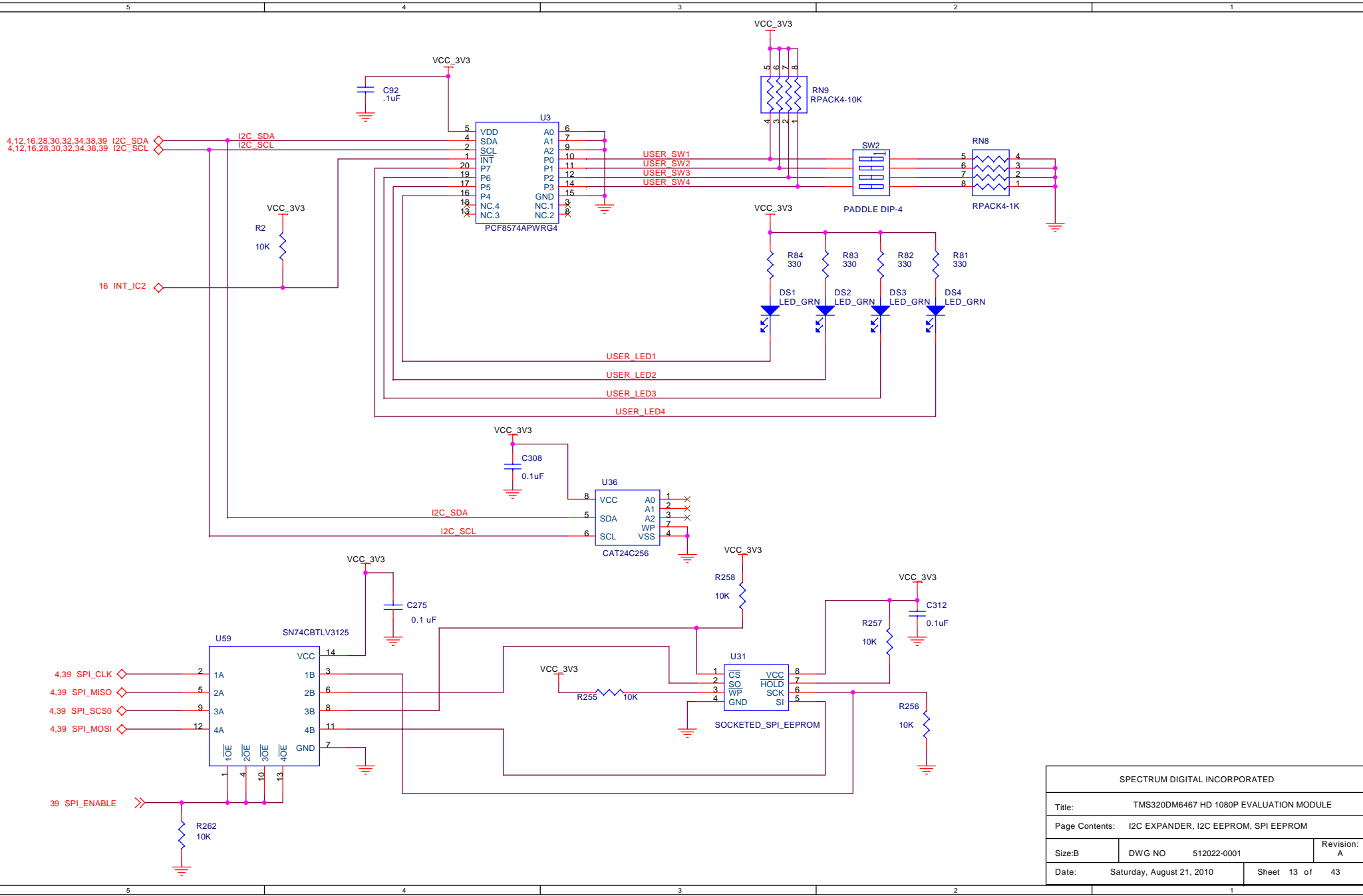


SPECTRUM DIGITAL INCORPORATED			
Title:		TMS320DM6467 HD 1080P EVALUATION MODULE	
Page Contents: DAVINCI HD DECOUPLING CAPS			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	11 of 43

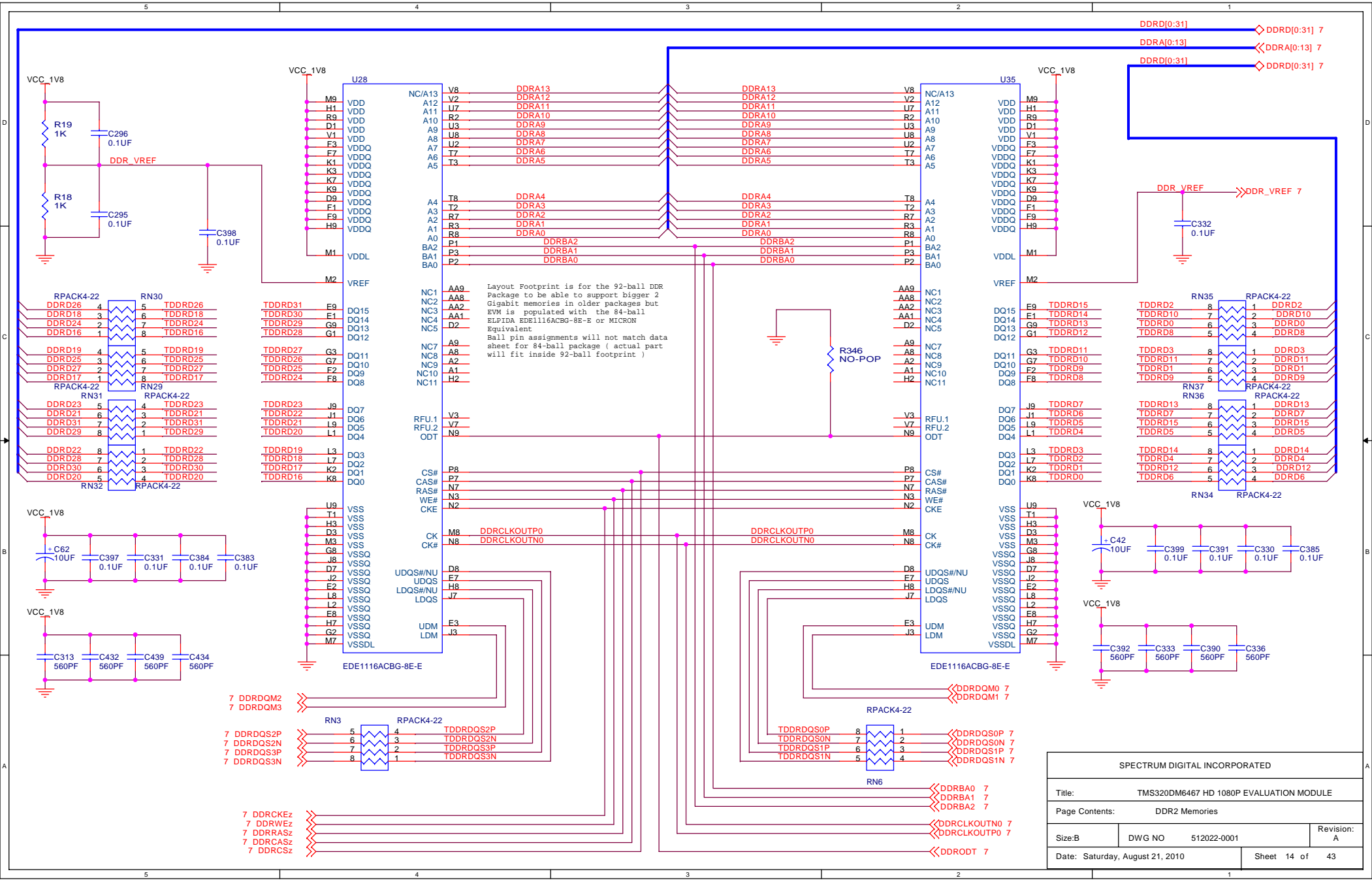


- 35 SW_VPIF_DOUT0_BTM0
- 35 SW_VPIF_DOUT1_BTM1
- 35 SW_VPIF_DOUT2_BTM2
- 35 SW_VPIF_DOUT3_BTM3
- 35 SW_VPIF_DOUT4_CS2BW
- 35 SW_VPIF_DOUT5_PCIEI
- 35 SW_VPIF_DOUT6_DSPBT
- 35 SW_VPIF_DOUT7_SMTRFEN

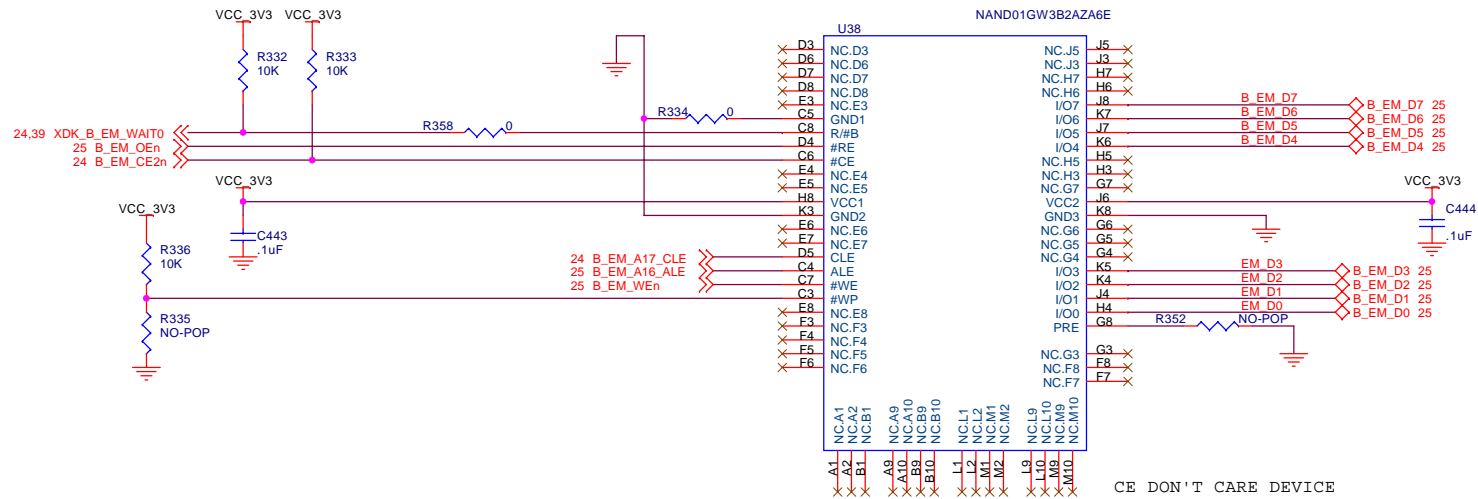
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: Boot DIP Switches			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	12 of 43



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: I2C EXPANDER, I2C EEPROM, SPI EEPROM			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 13 of	43

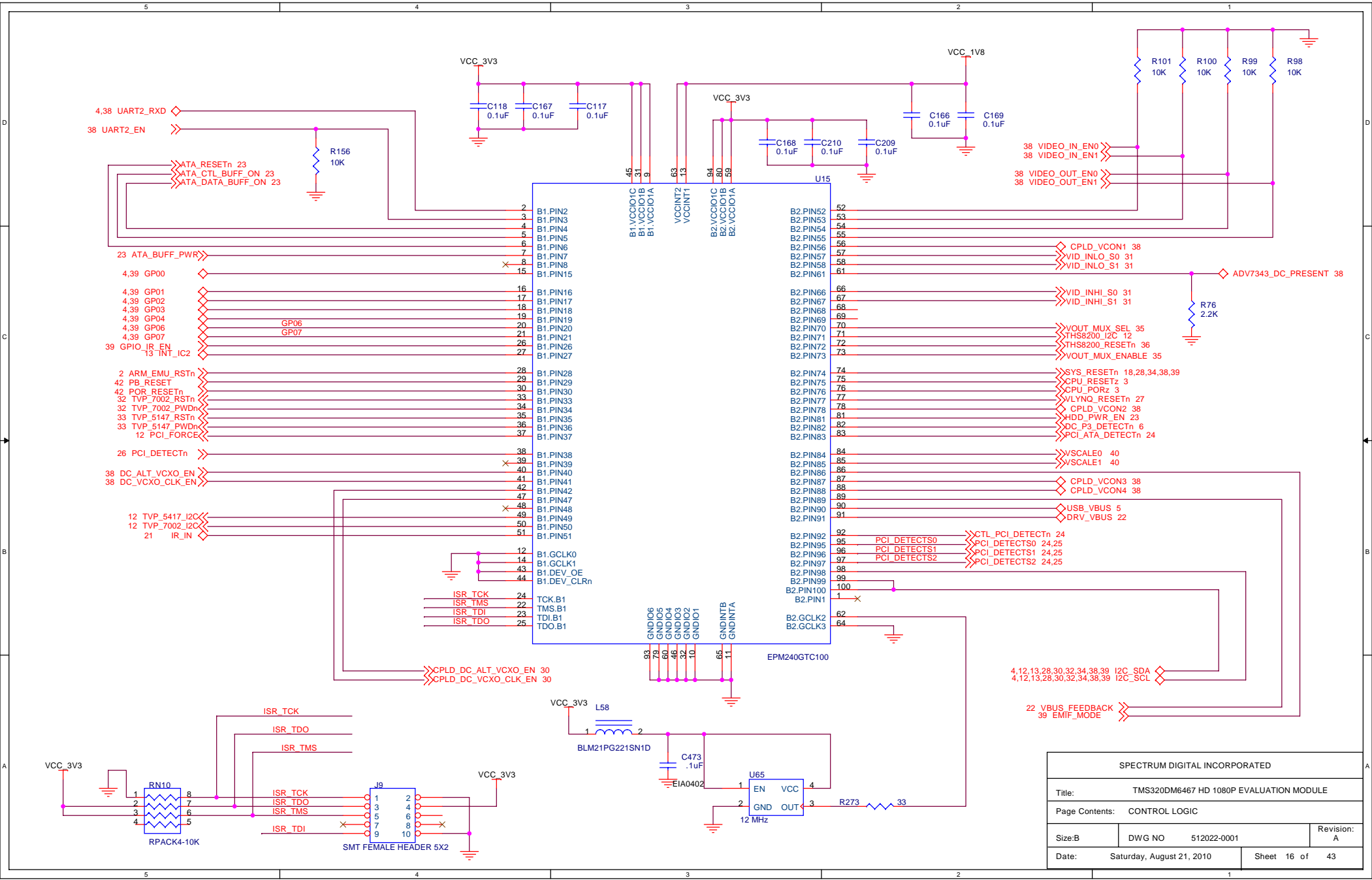


SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DDR2 Memories			
Size: B	DWG NO	512022-0001	Revision: A
Date: Saturday, August 21, 2010		Sheet 14 of 43	

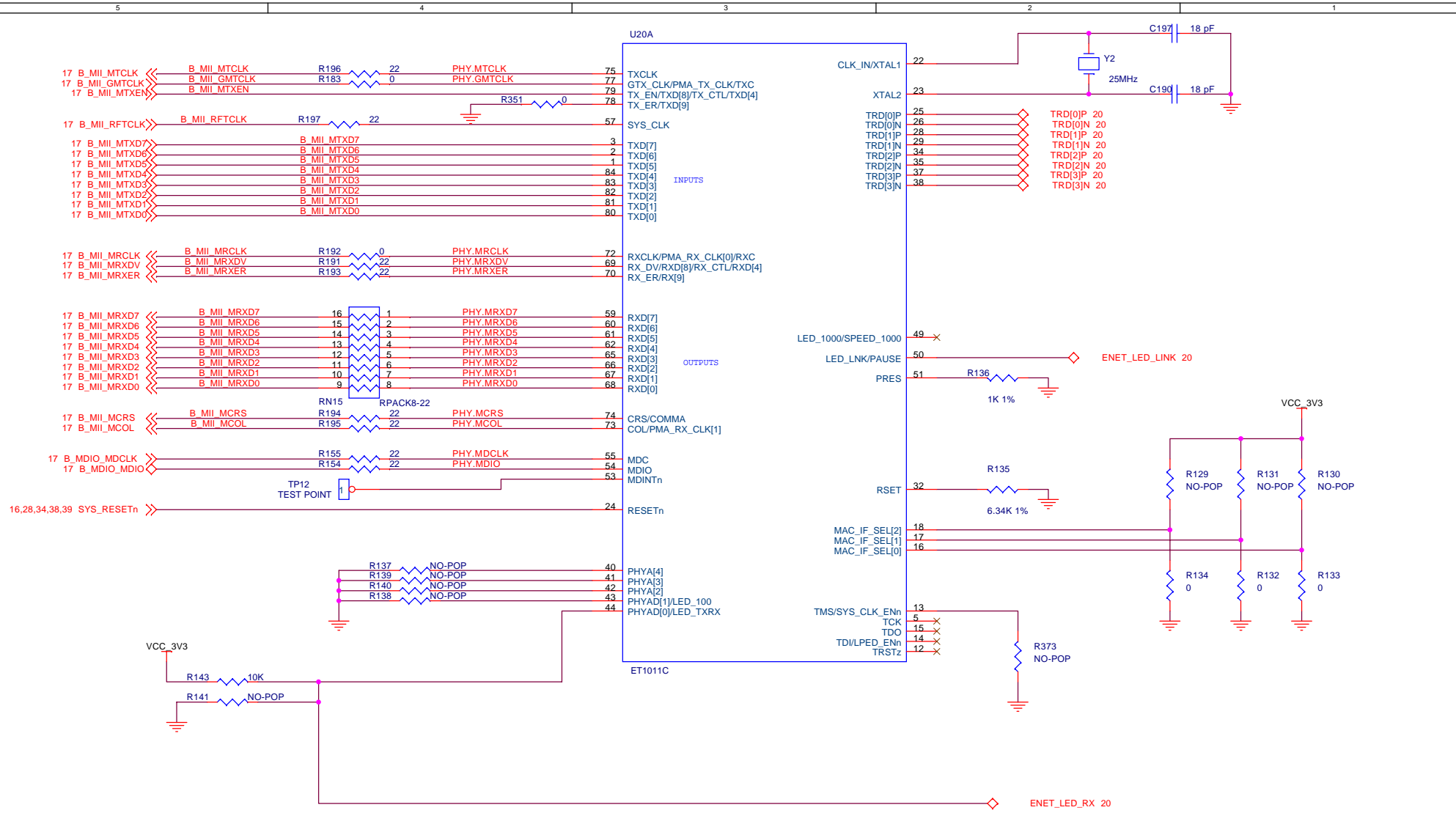


CE DON'T CARE DEVICE

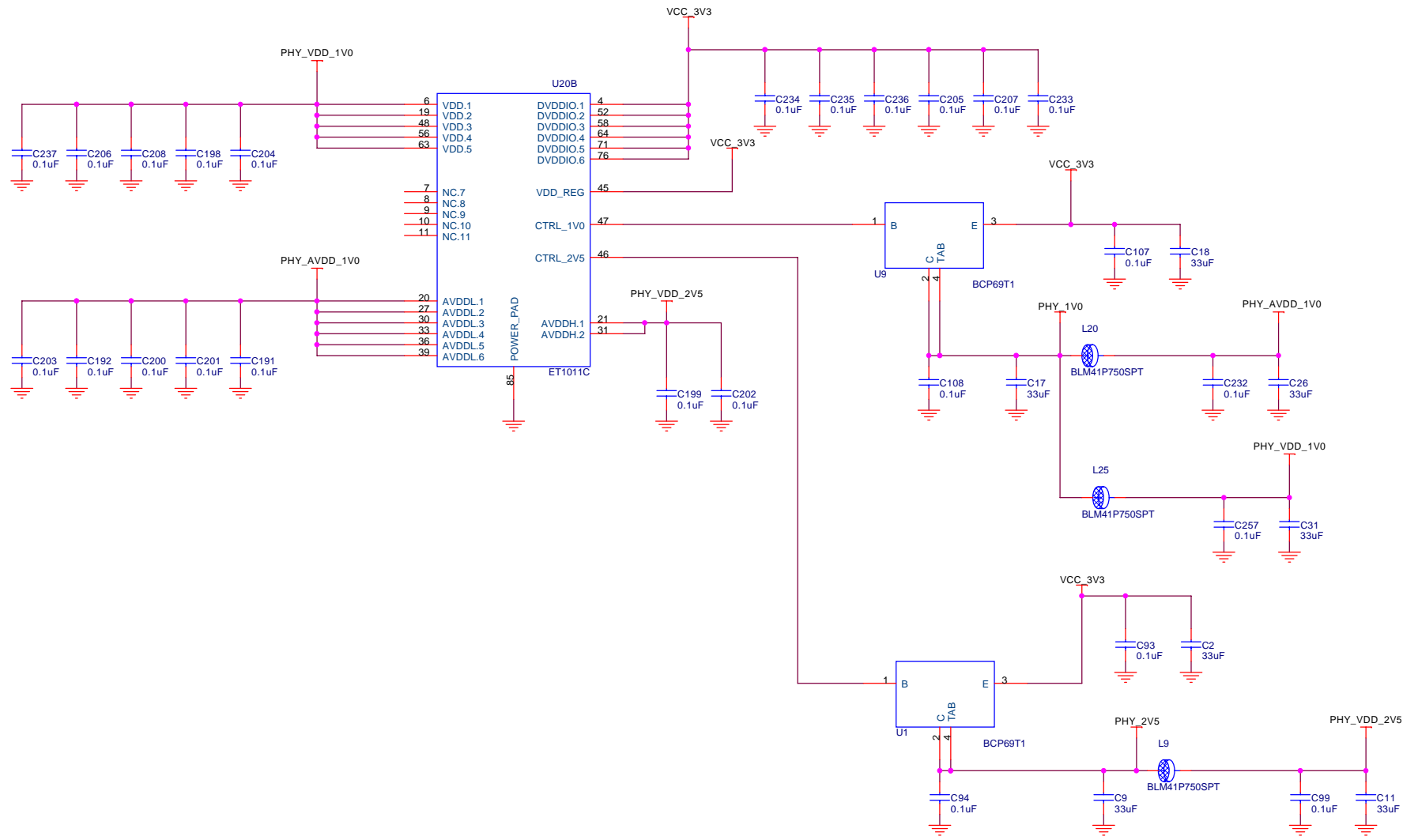
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: NAND FLASH			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010		Sheet 15 of 43



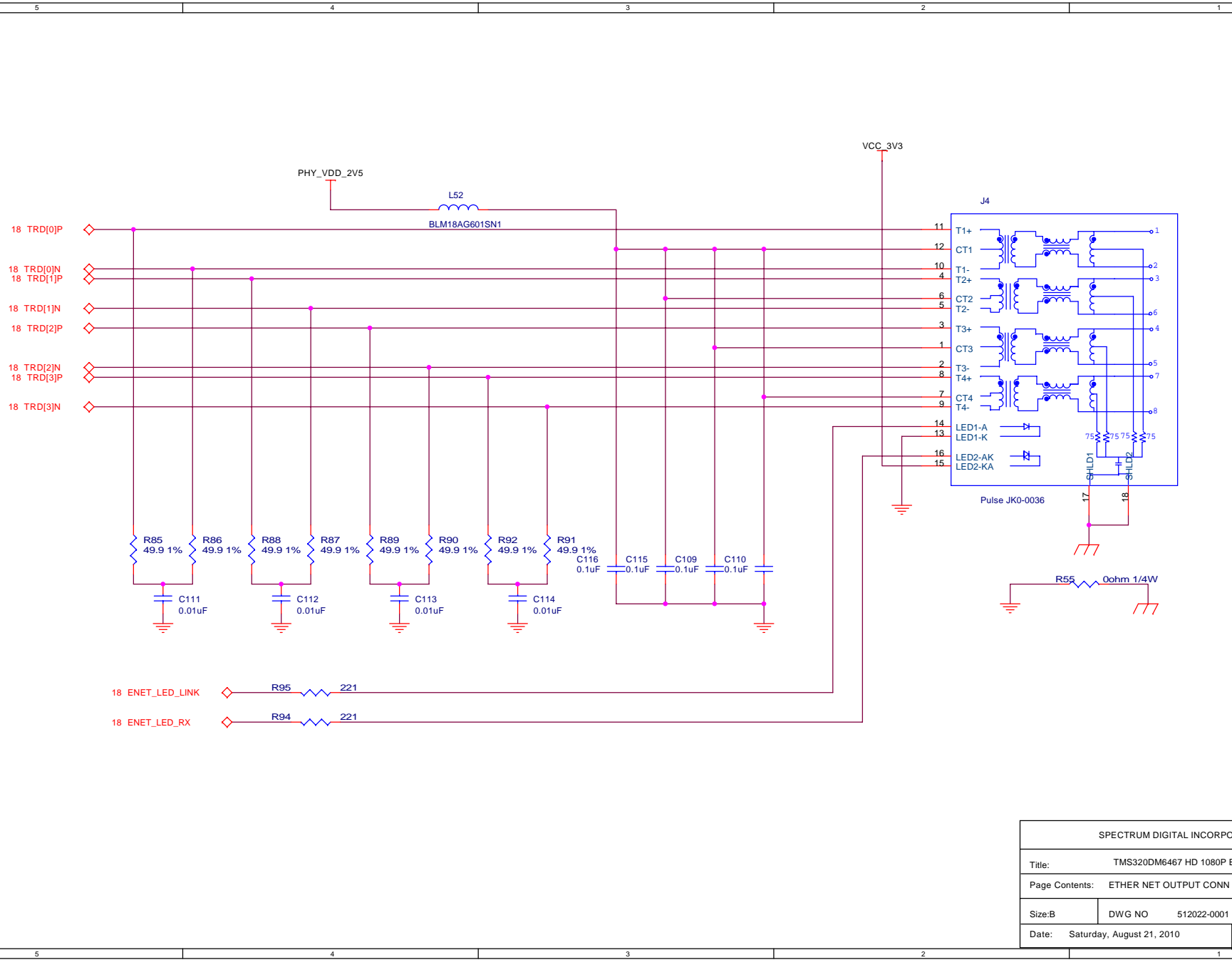
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: CONTROL LOGIC			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	16 of 43



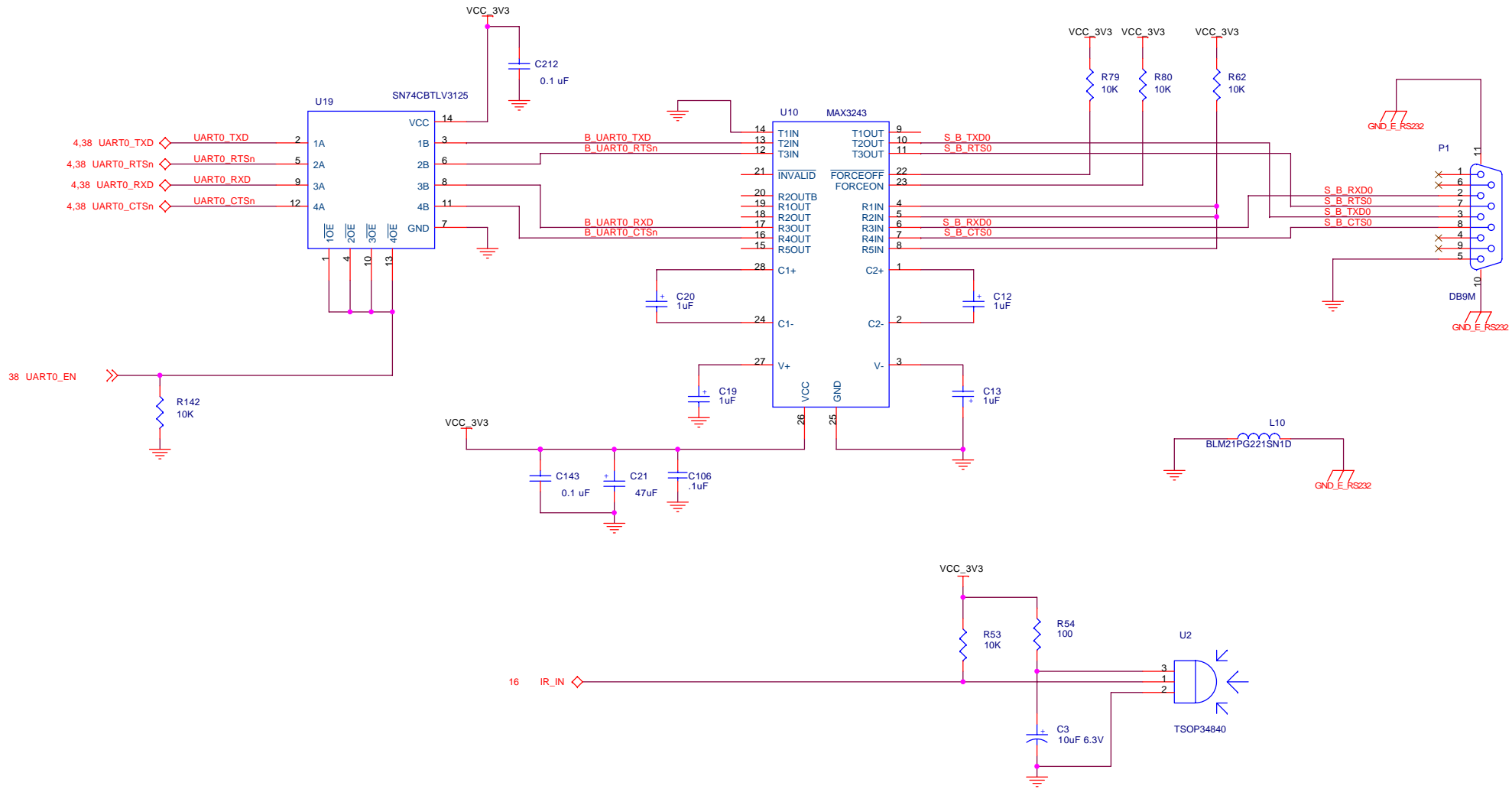
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: ETHERNET INTERFACE			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 18 of	43



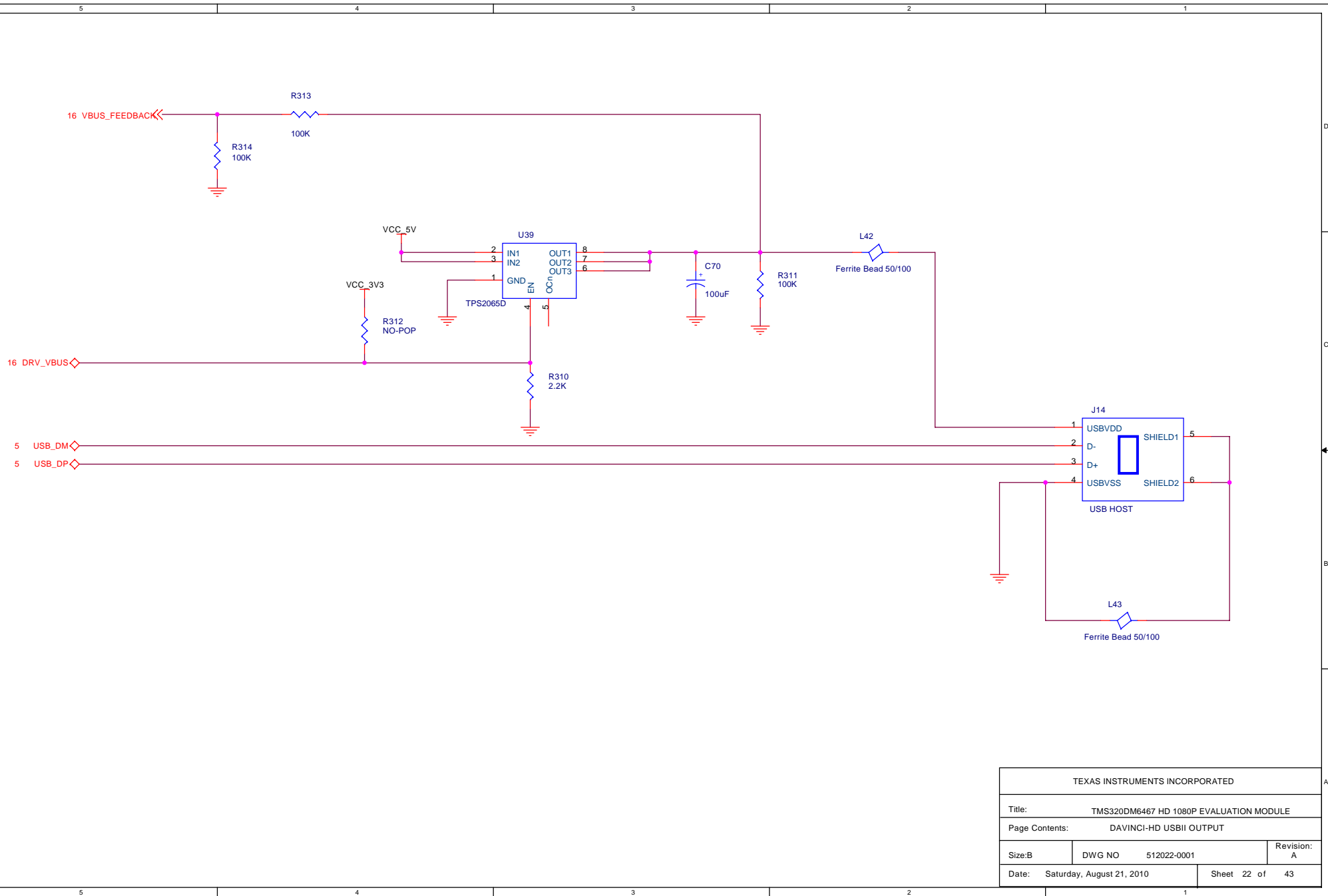
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: ETHERNET - POWER			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	19 of 43



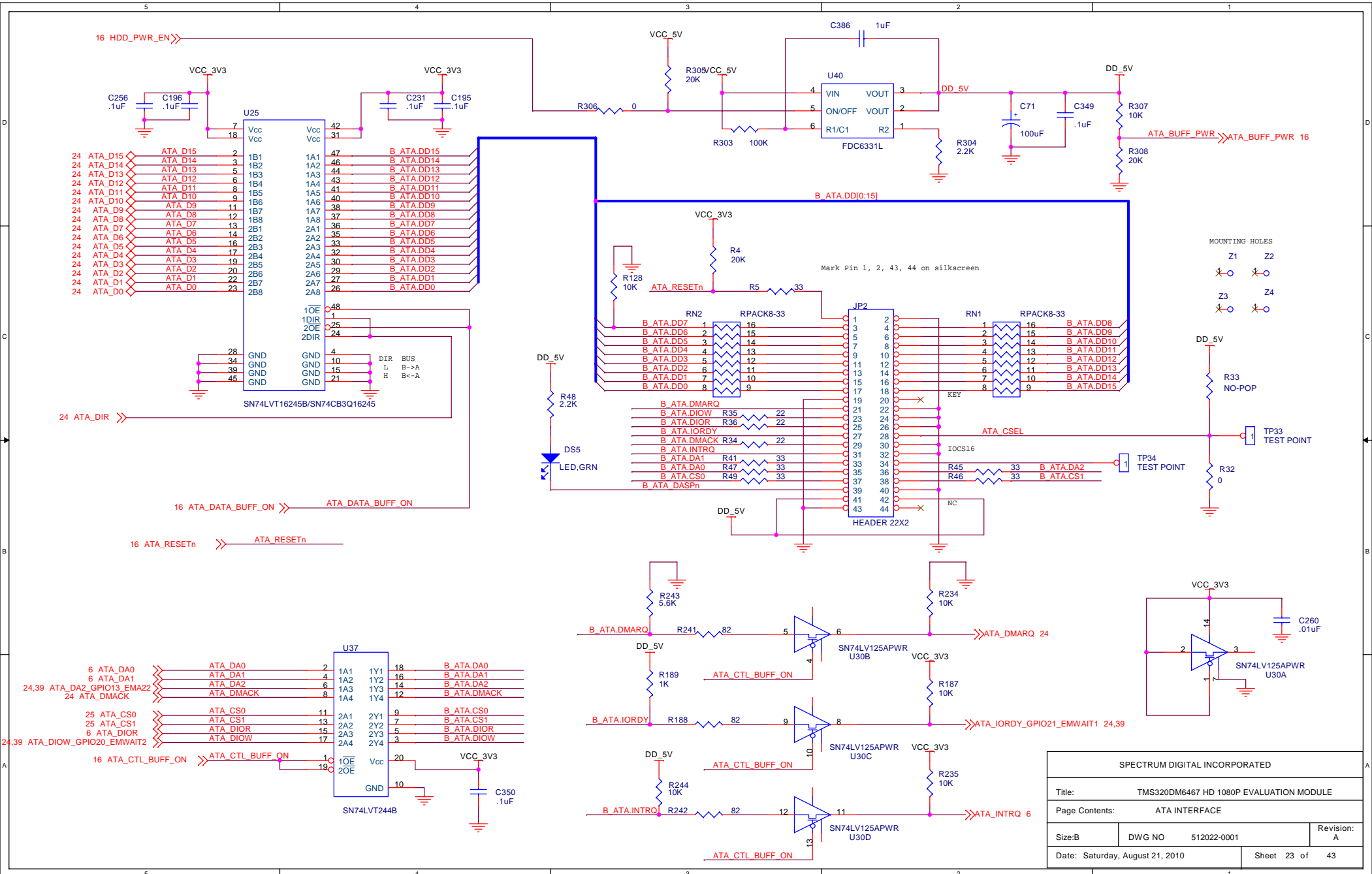
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: ETHER NET OUTPUT CONN			
Size: B	DWG NO	512022-0001	Revision: A
Date: Saturday, August 21, 2010		Sheet 20 of 43	



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: UART0 RS232 / UART2 IR RECEIVER			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	21 of 43

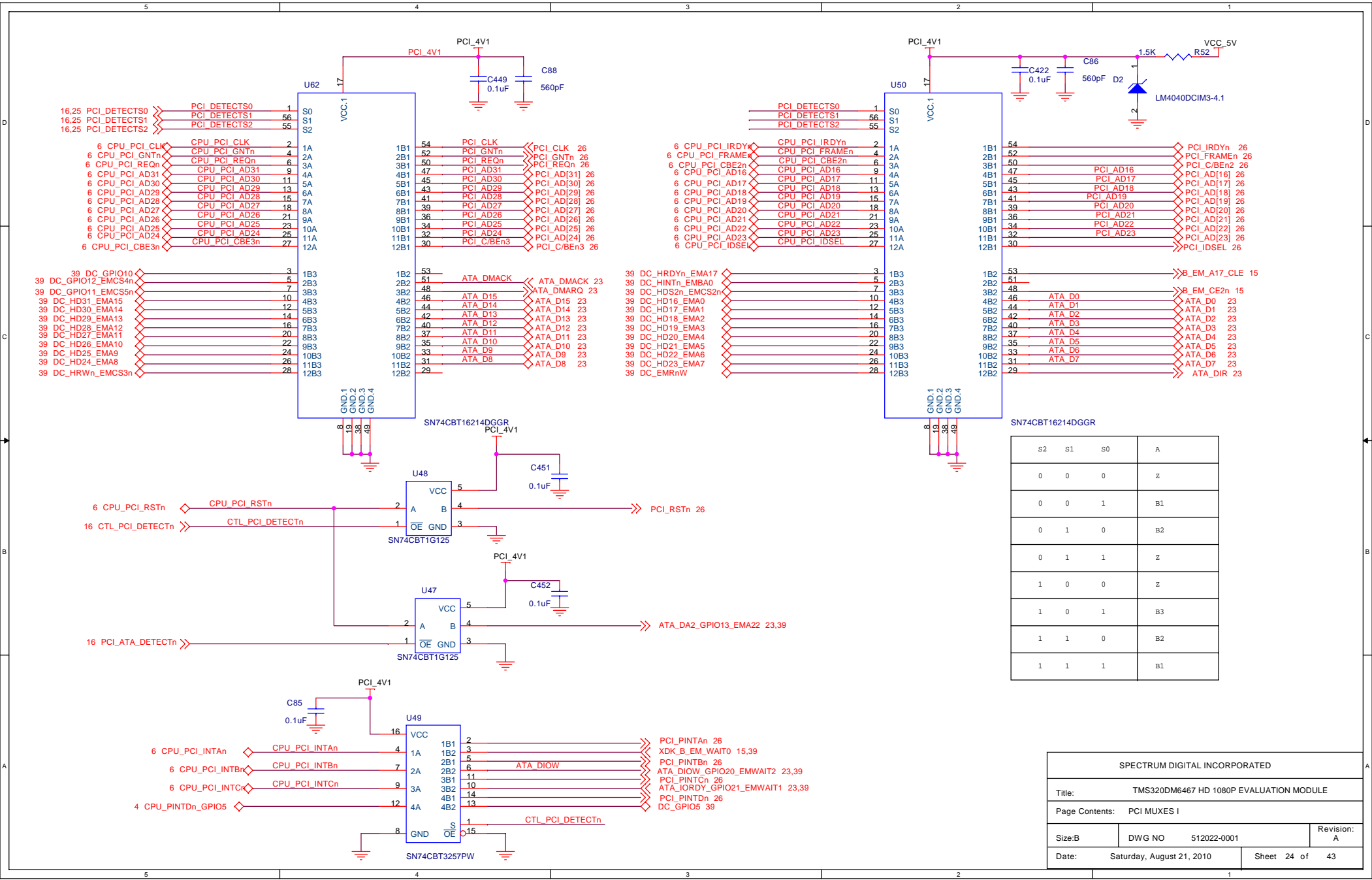


TEXAS INSTRUMENTS INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DAVINCI-HD USBII OUTPUT			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010		Sheet 22 of 43

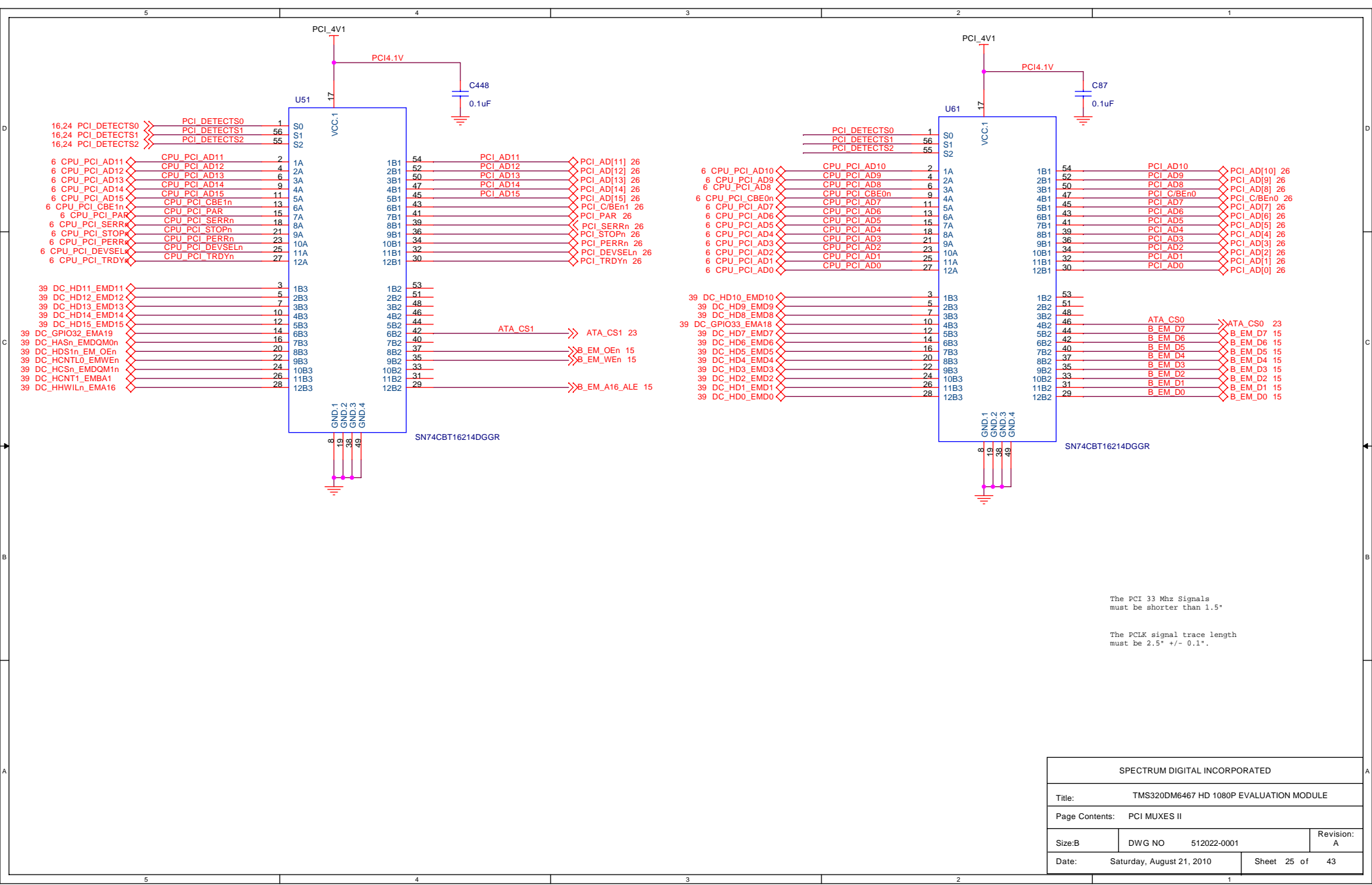


- MOUNTING HOLES
- Z1
 - Z2
 - Z3
 - Z4

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: ATA INTERFACE			
Size: B	DWG NO	512022-0001	Revision: A
Date: Saturday, August 21, 2010		Sheet 23 of 43	



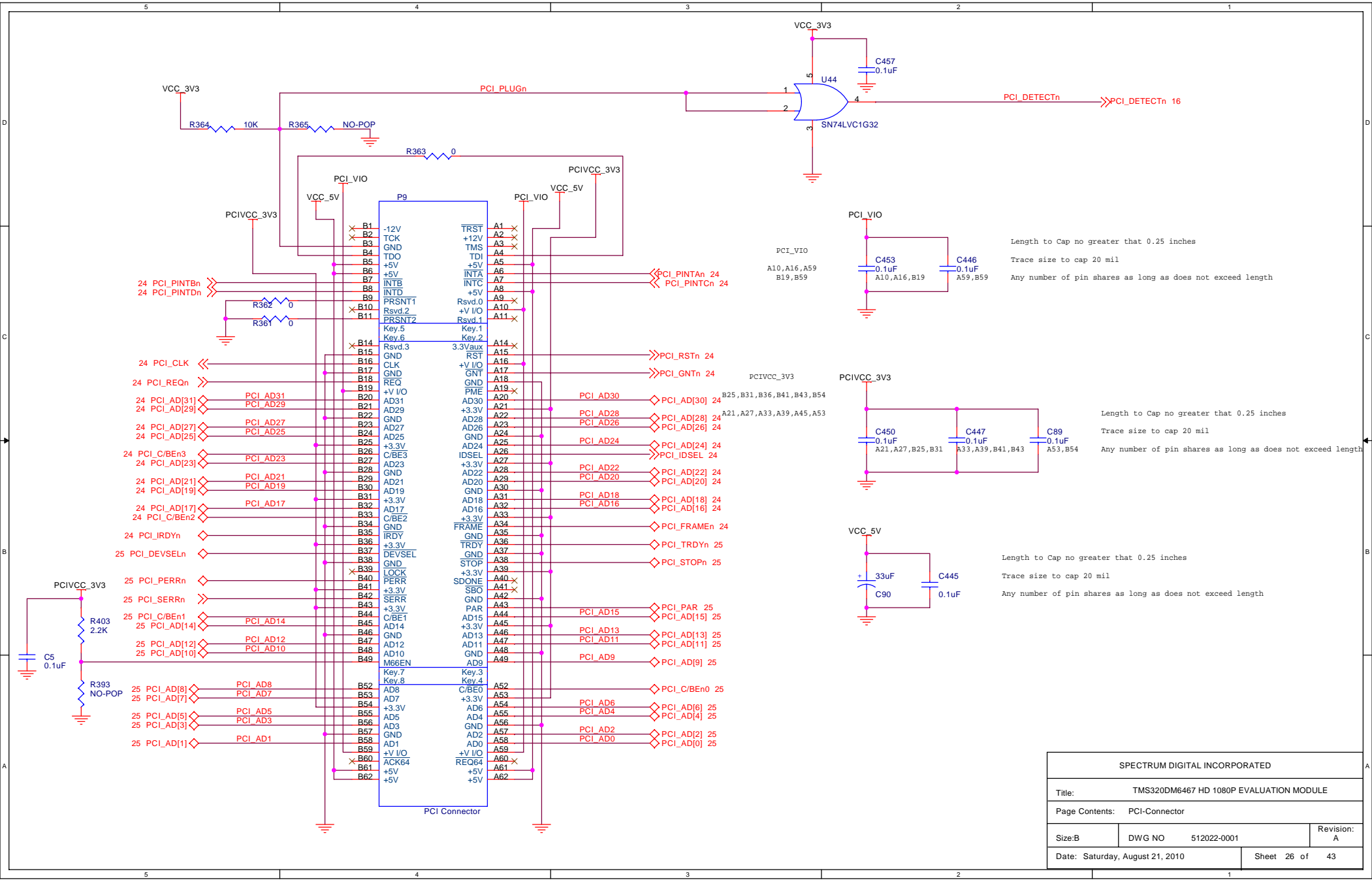
S2	S1	S0	A
0	0	0	Z
0	0	1	B1
0	1	0	B2
0	1	1	Z
1	0	0	Z
1	0	1	B3
1	1	0	B2
1	1	1	B1



The PCI 33 Mhz Signals must be shorter than 1.5"

The PCLK signal trace length must be 2.5" +/- 0.1".

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: PCI MUXES II			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 25 of	43

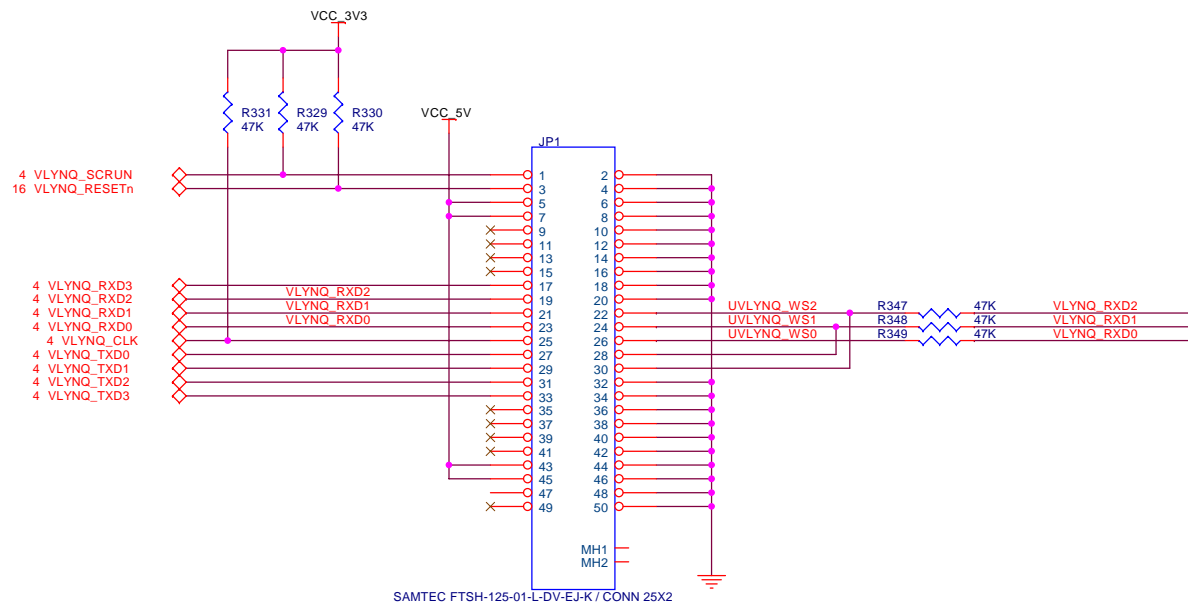


Length to Cap no greater that 0.25 inches
Trace size to cap 20 mil
Any number of pin shares as long as does not exceed length

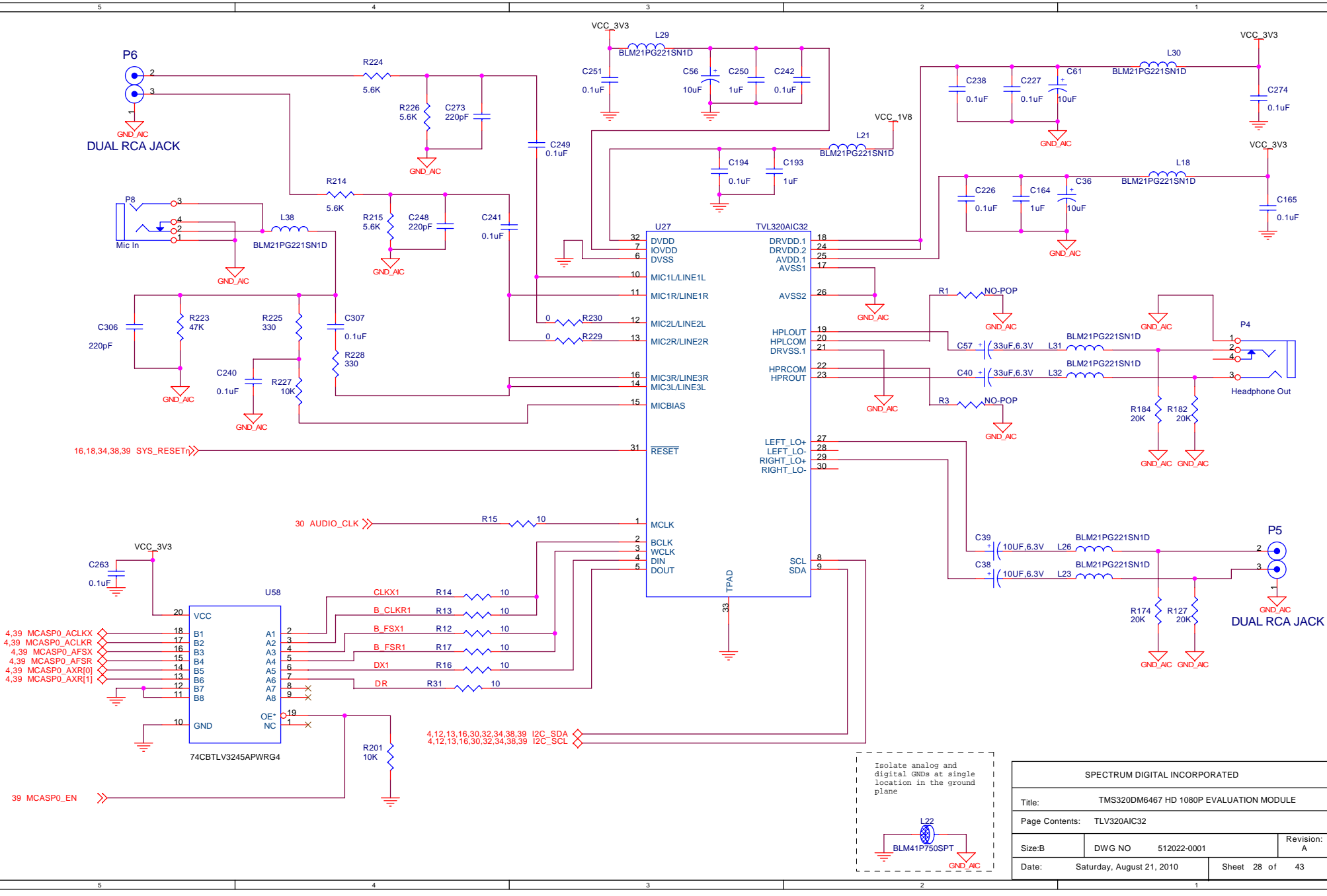
Length to Cap no greater that 0.25 inches
Trace size to cap 20 mil
Any number of pin shares as long as does not exceed length

Length to Cap no greater that 0.25 inches
Trace size to cap 20 mil
Any number of pin shares as long as does not exceed length

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: PCI-Connector			
Size: B	DWG NO	512022-0001	Revision: A
Date: Saturday, August 21, 2010		Sheet 26 of 43	

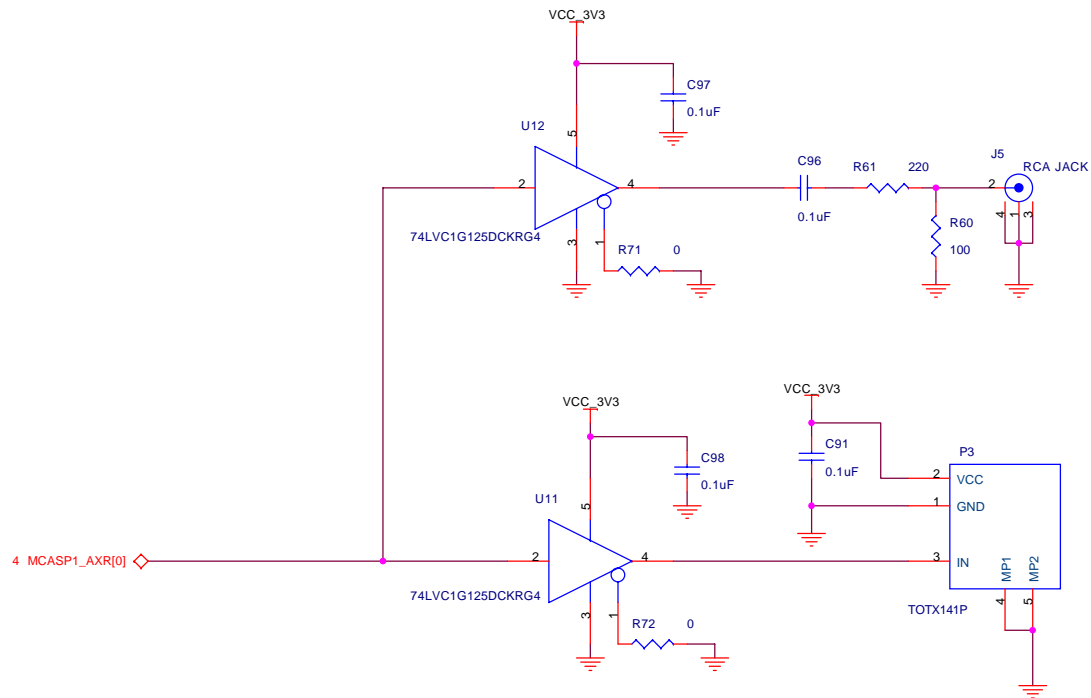


SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: VLYNQ CONNECTOR			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 27 of	43

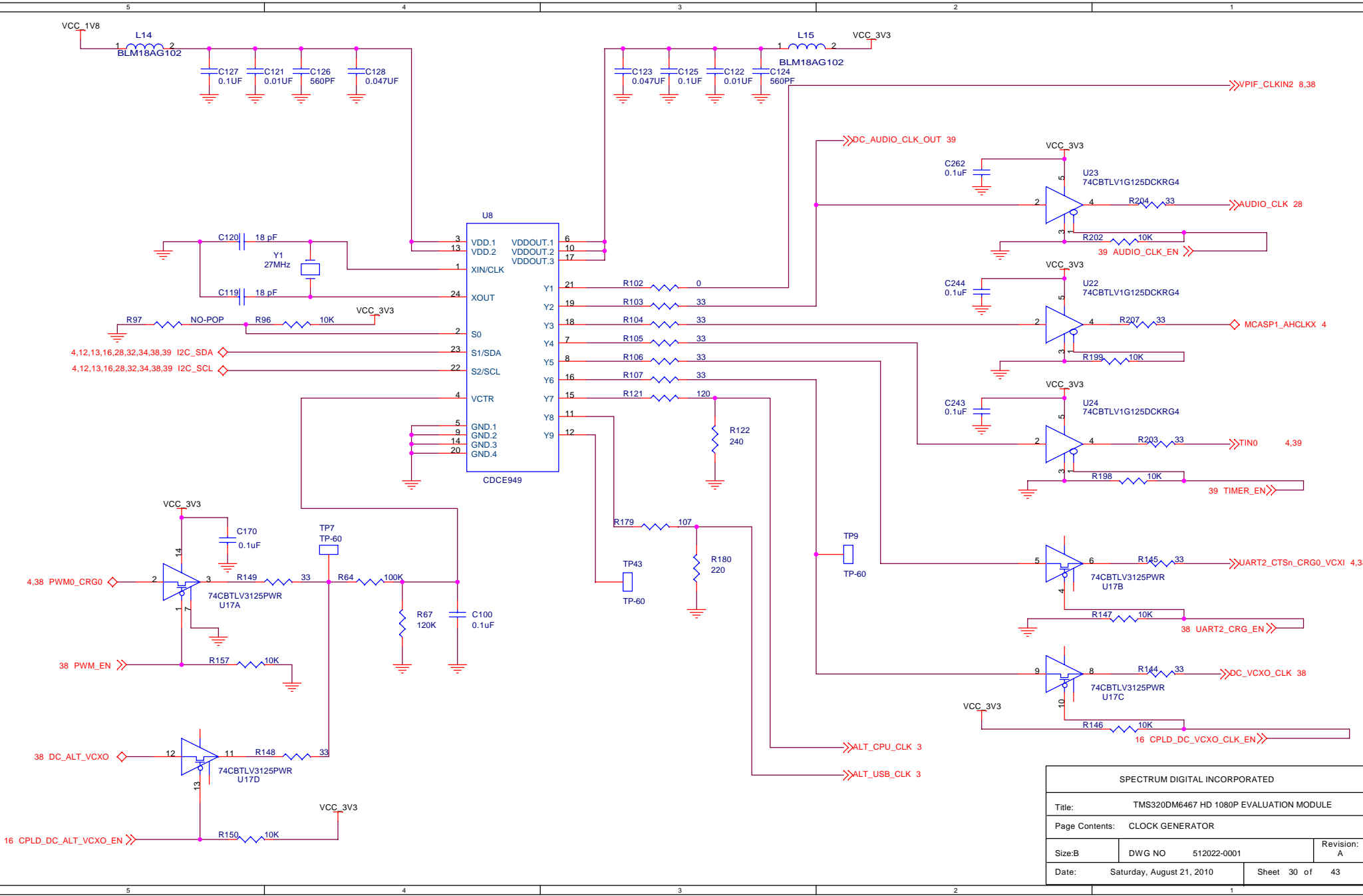


Isolate analog and digital GNDs at single location in the ground plane

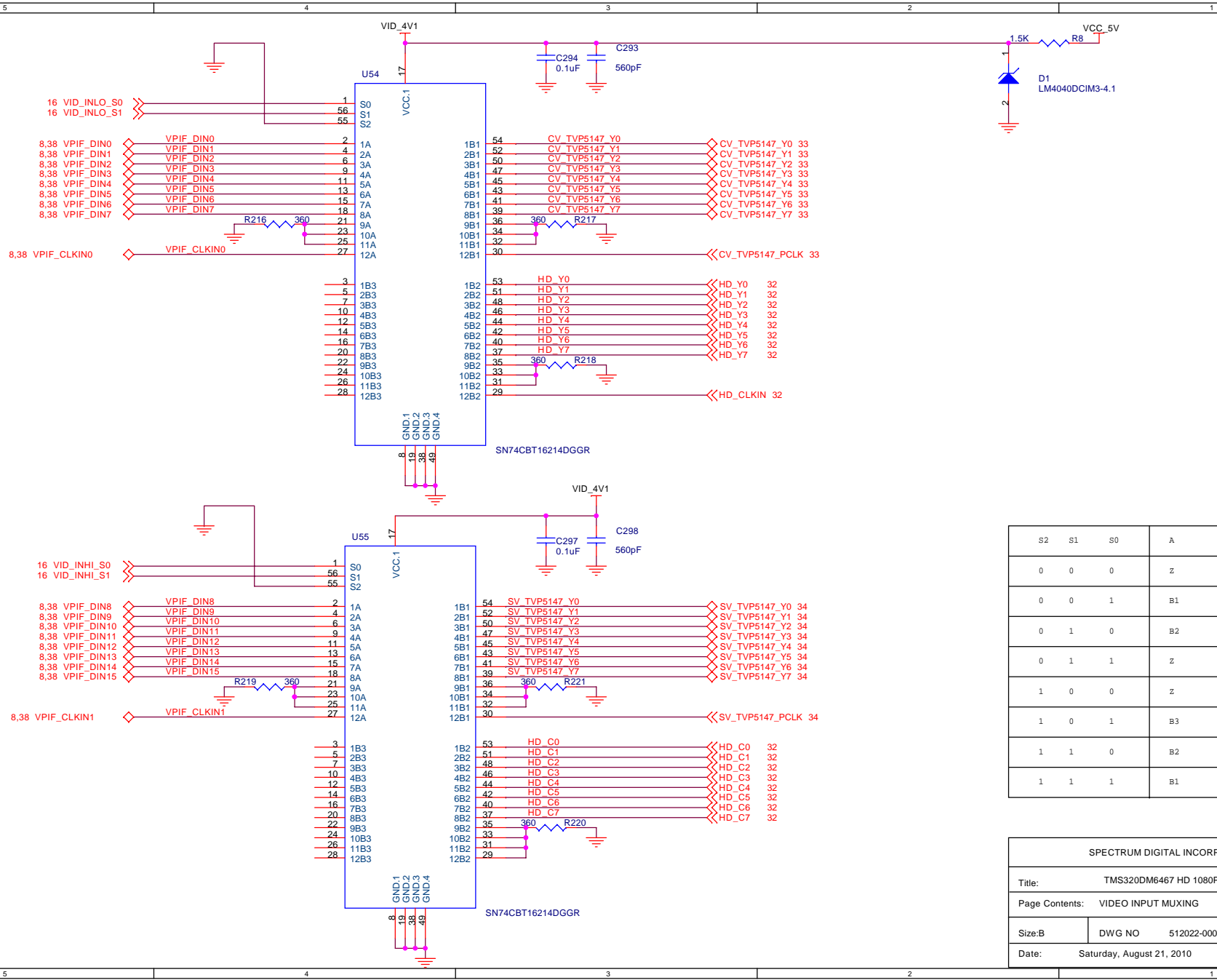
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: TLV320AIC32			
Size: B	DWG NO	512022-0001	Revision: A
Date: Saturday, August 21, 2010	Sheet 28 of 43		



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: SPDIF			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 29 of	43

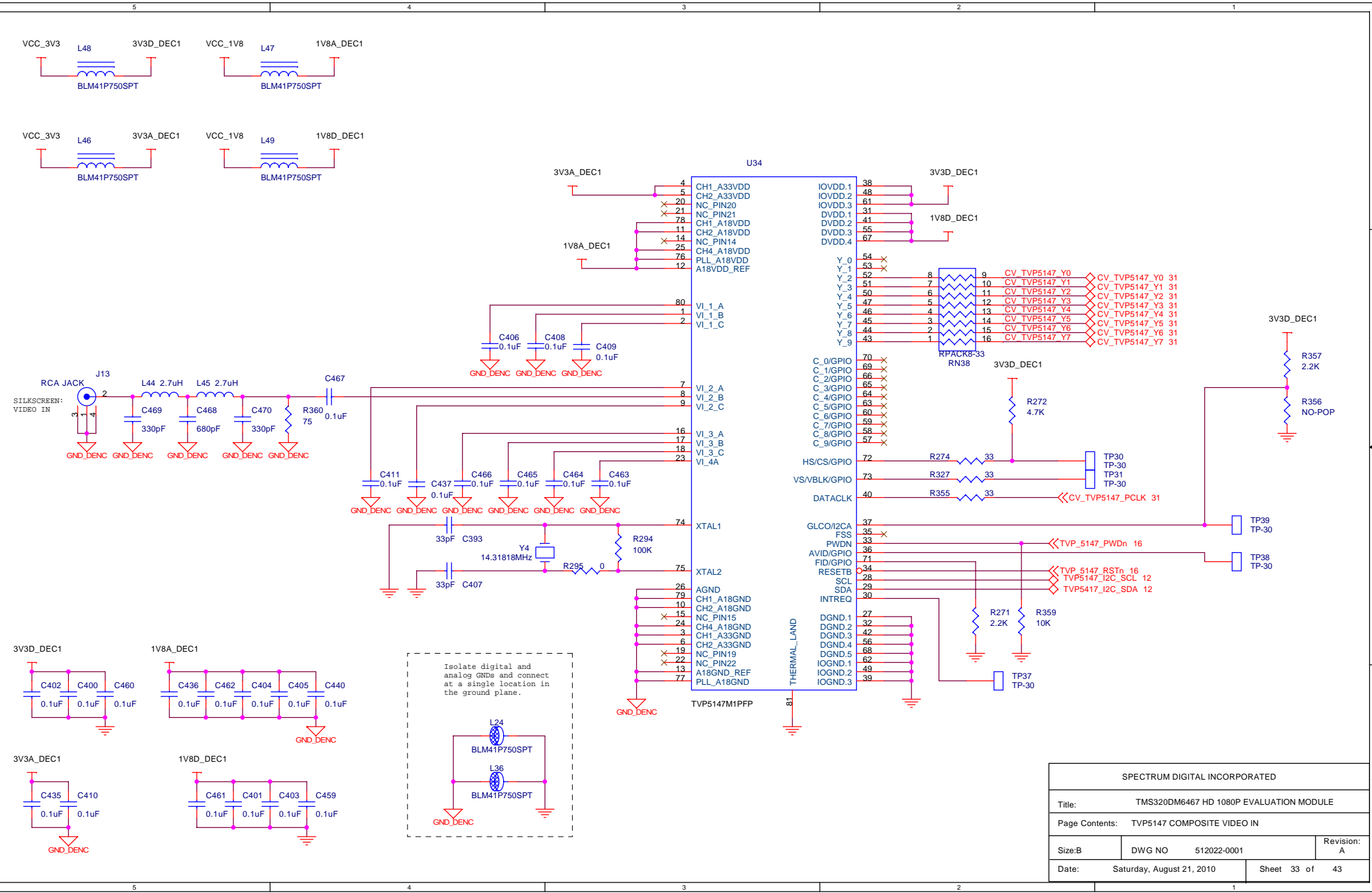


SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: CLOCK GENERATOR			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	30 of 43

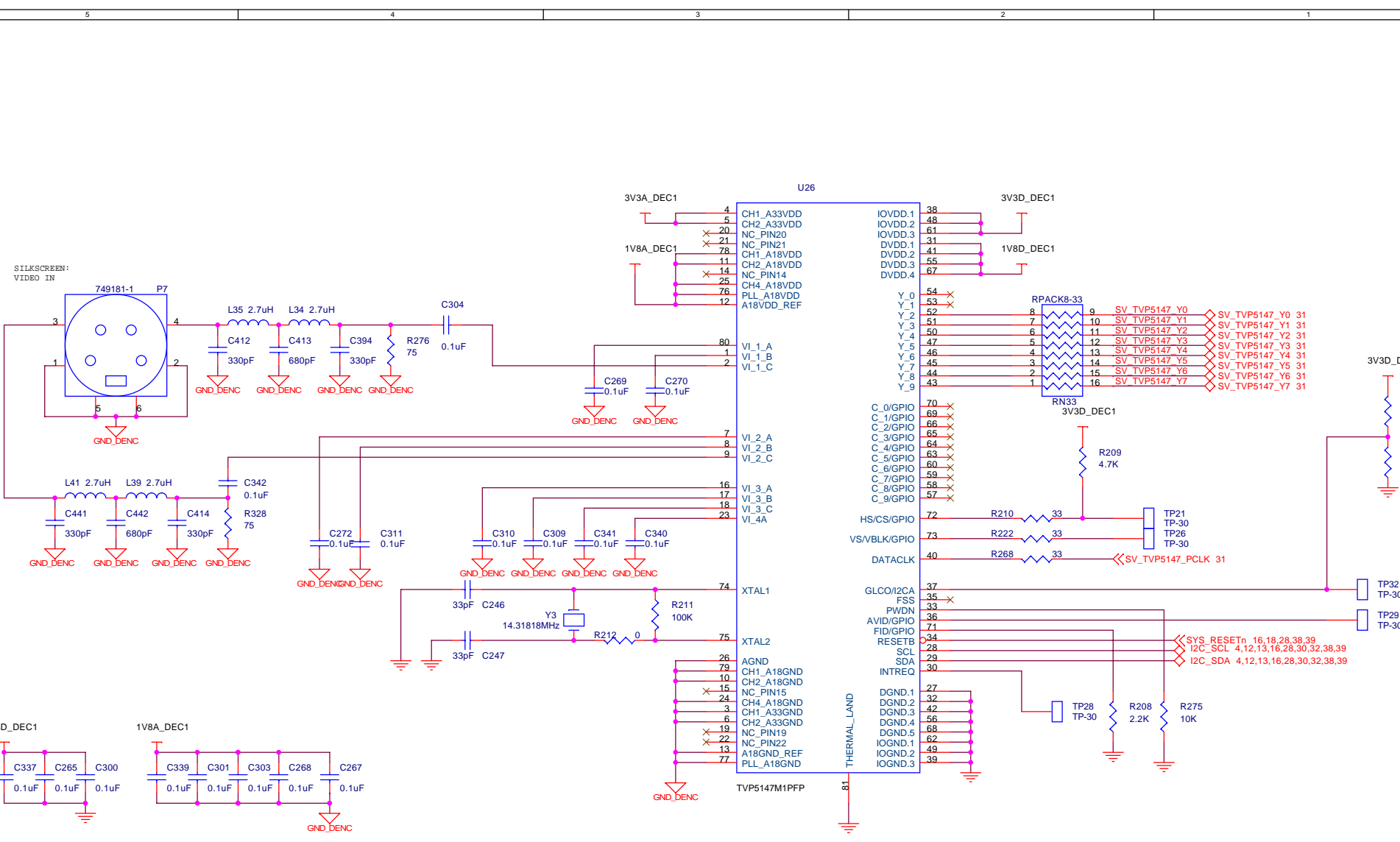


S2	S1	S0	A
0	0	0	Z
0	0	1	B1
0	1	0	B2
0	1	1	Z
1	0	0	Z
1	0	1	B3
1	1	0	B2
1	1	1	B1

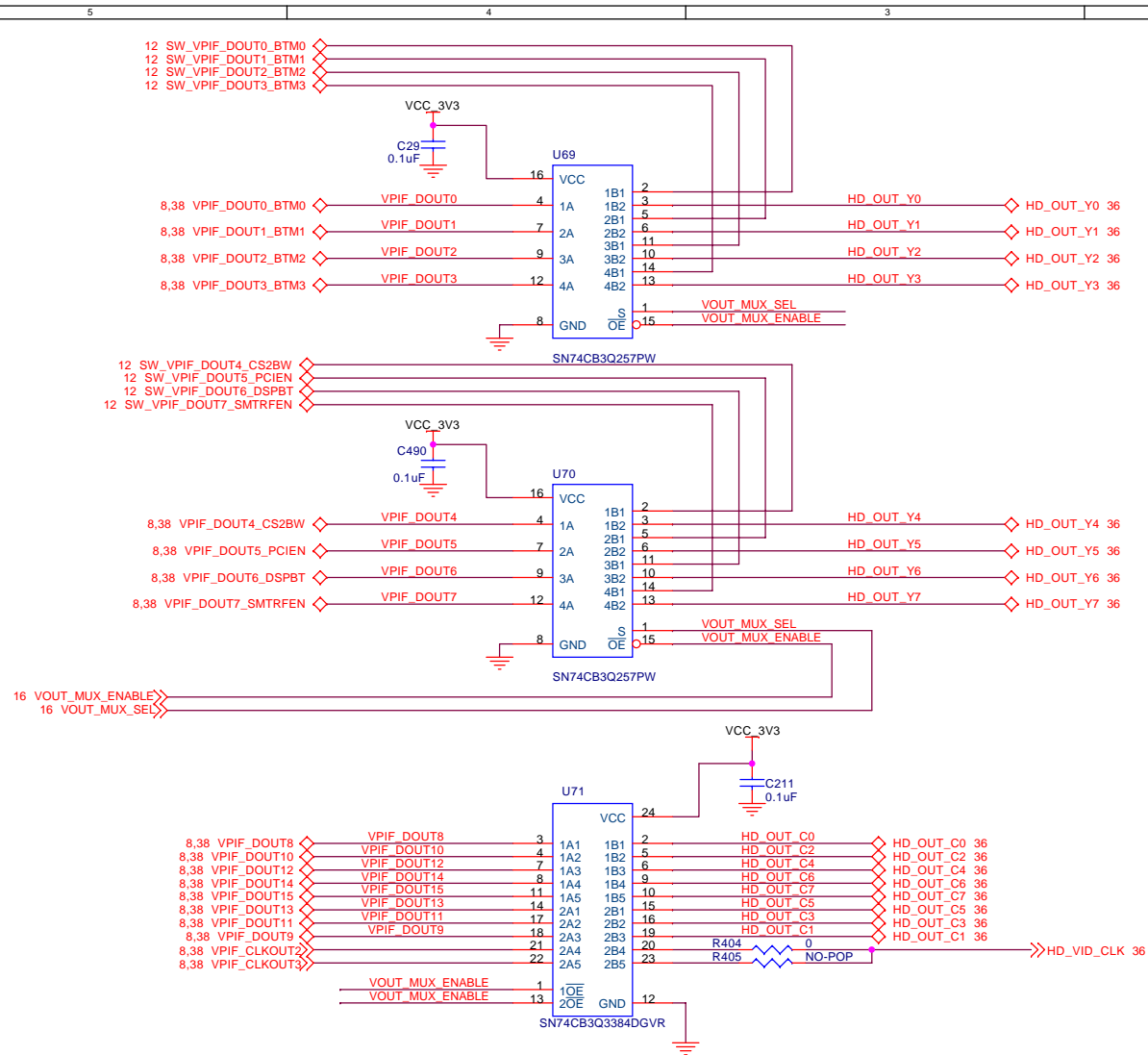
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: VIDEO INPUT MUXING			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 31 of 43	



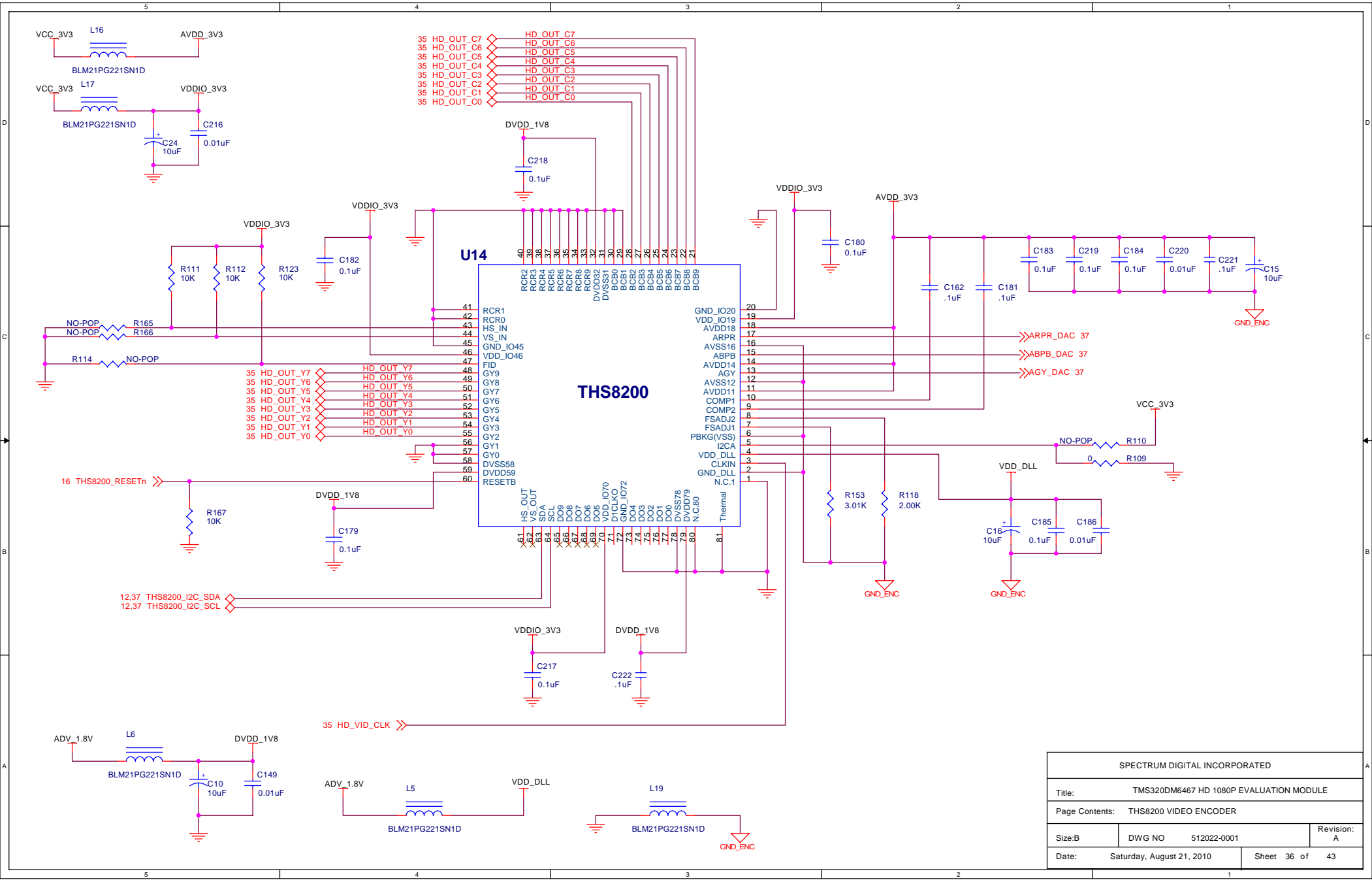
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: TVP5147 COMPOSITE VIDEO IN			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	33 of 43



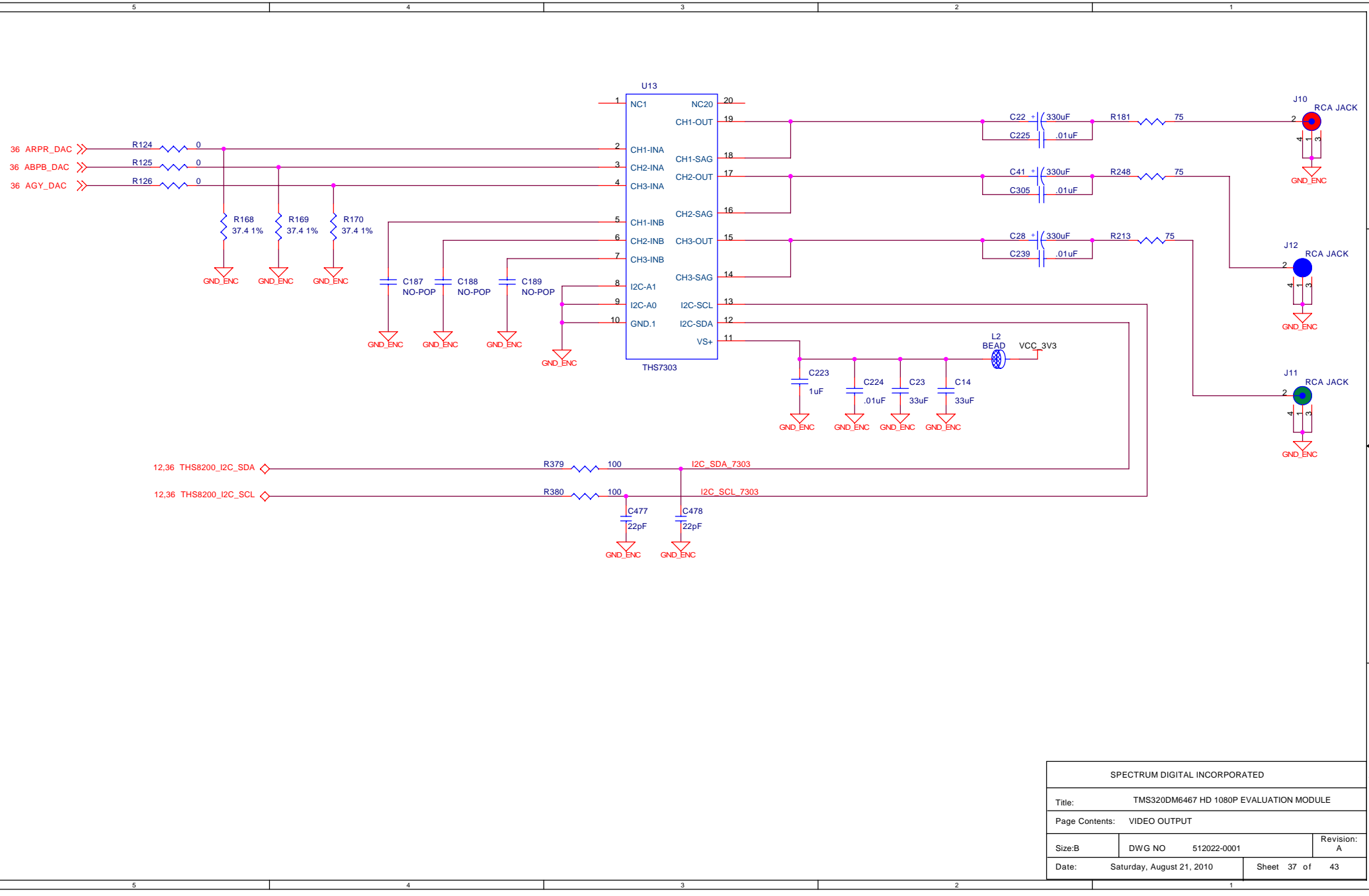
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: TVP5147 SVIDEO INPUT			
Size: B	DWG NO: 512022-0001	Revision: A	
Date: Saturday, August 21, 2010	Sheet 34 of 43		



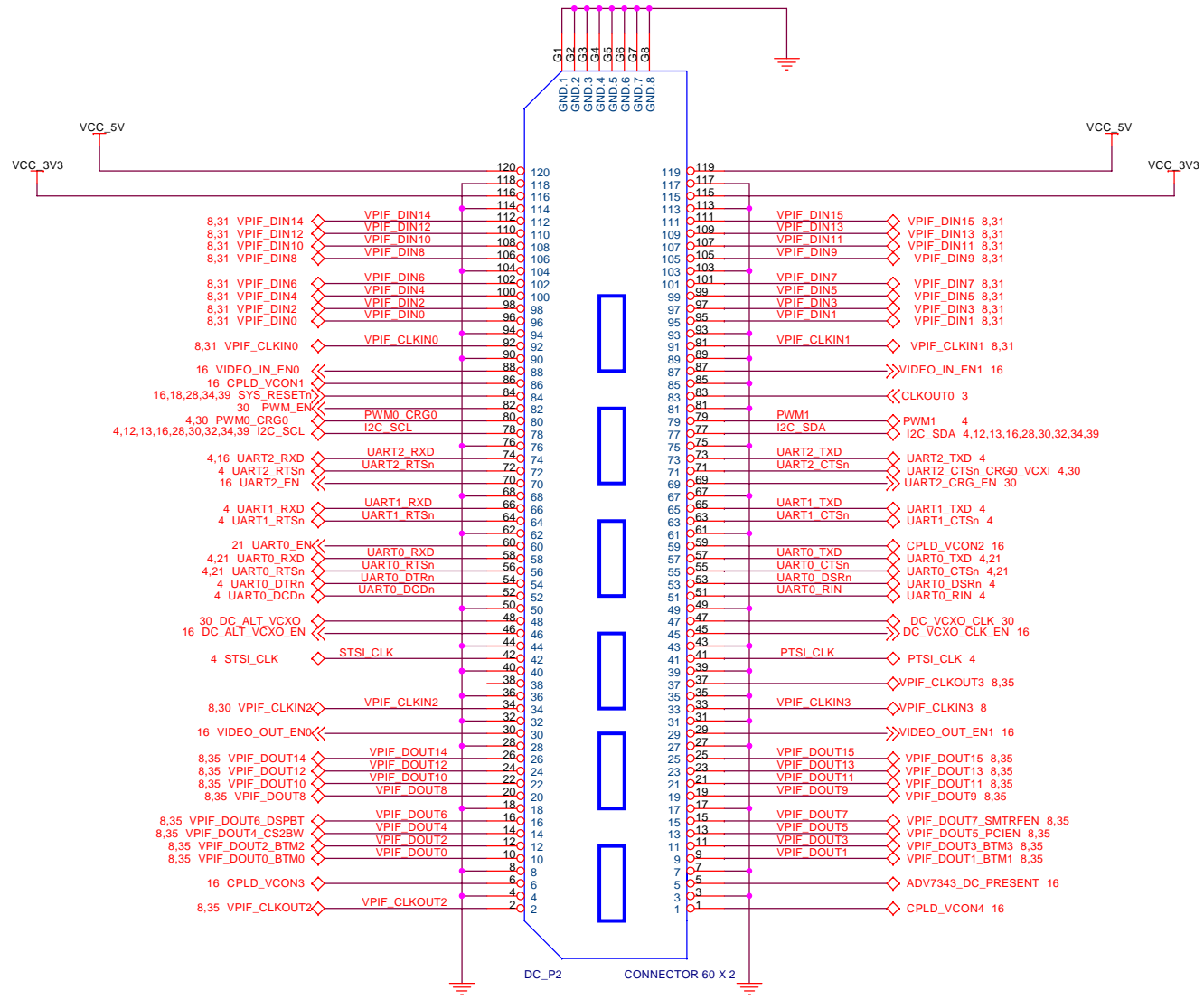
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: VIDEO OUTPUT MUXES			
Size: B	DWG NO	512022-0001	Revision: B
Date:	Saturday, August 21, 2010	Sheet 35 of	43



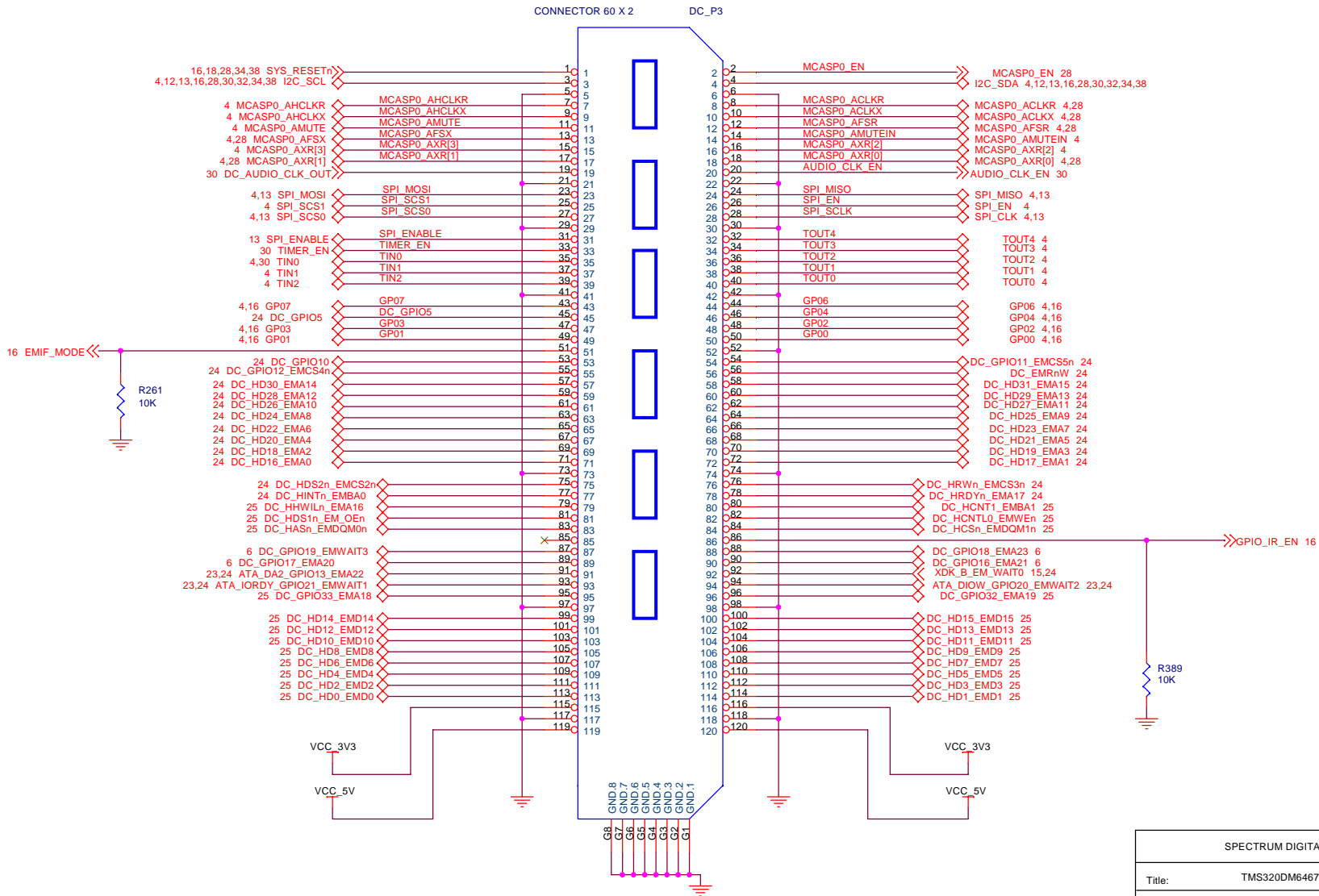
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: THS8200 VIDEO ENCODER			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	36 of 43



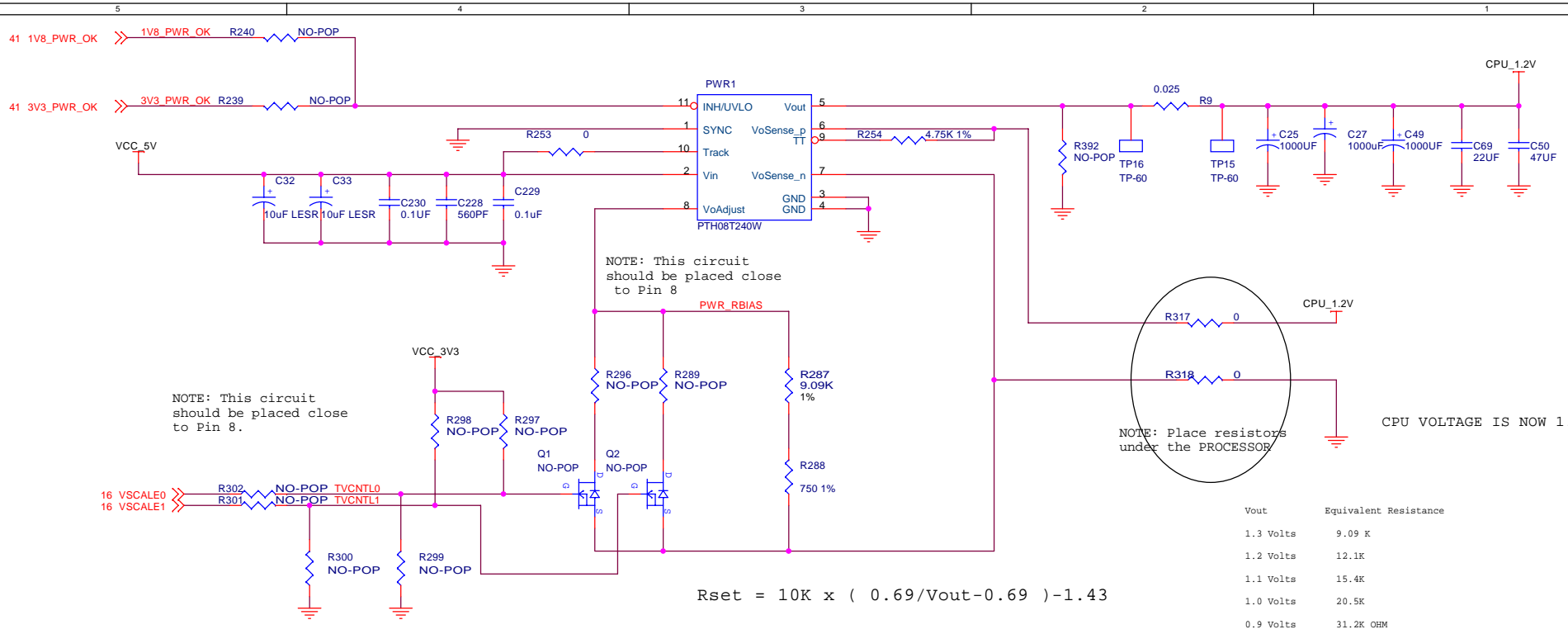
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: VIDEO OUTPUT			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 37 of	43



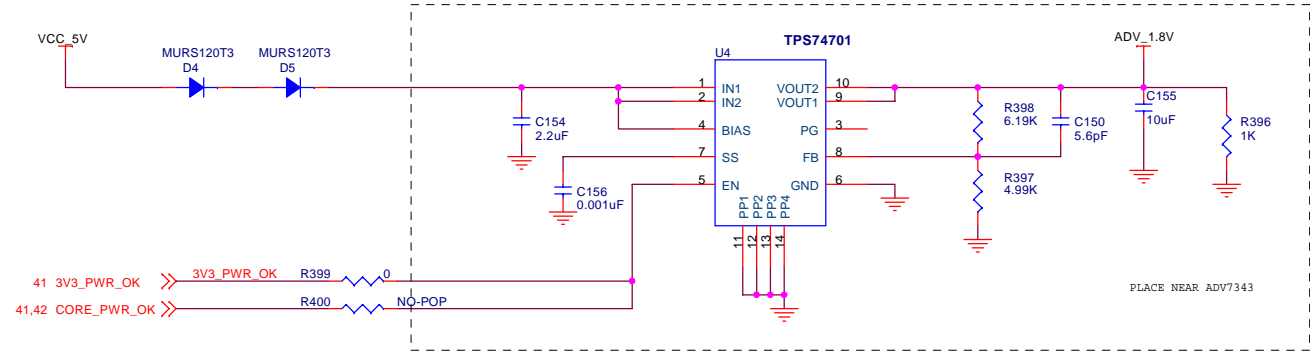
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: VIDEO EXPANSION CONNECTORS			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	38 of 43



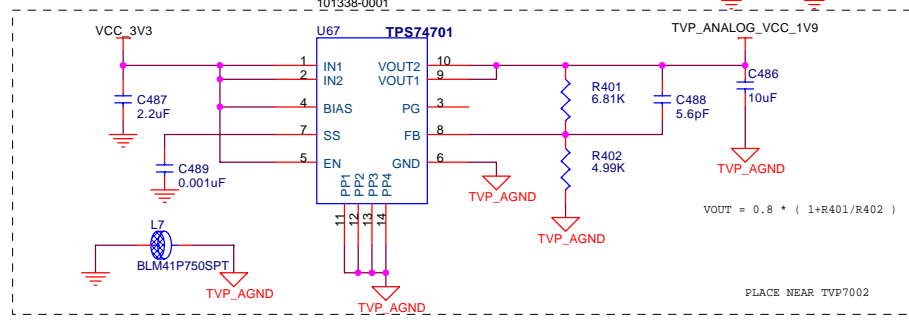
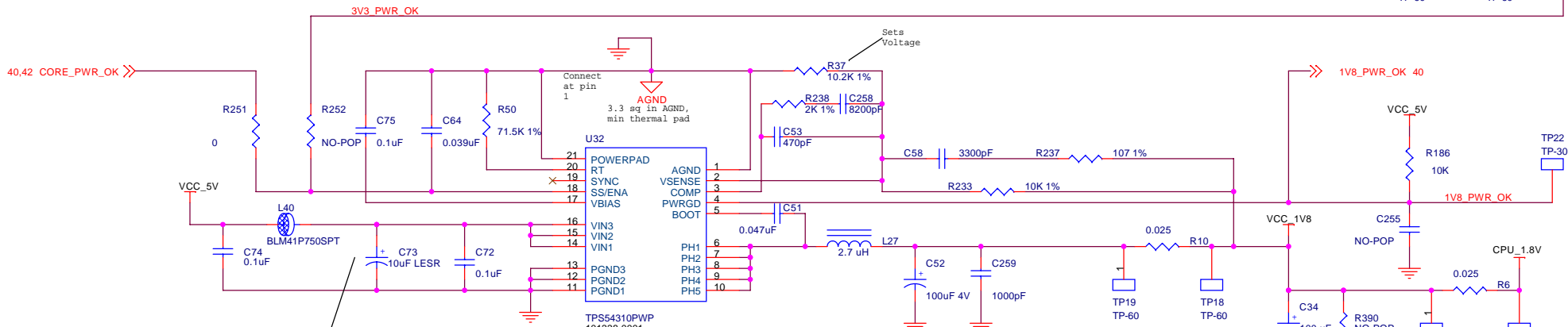
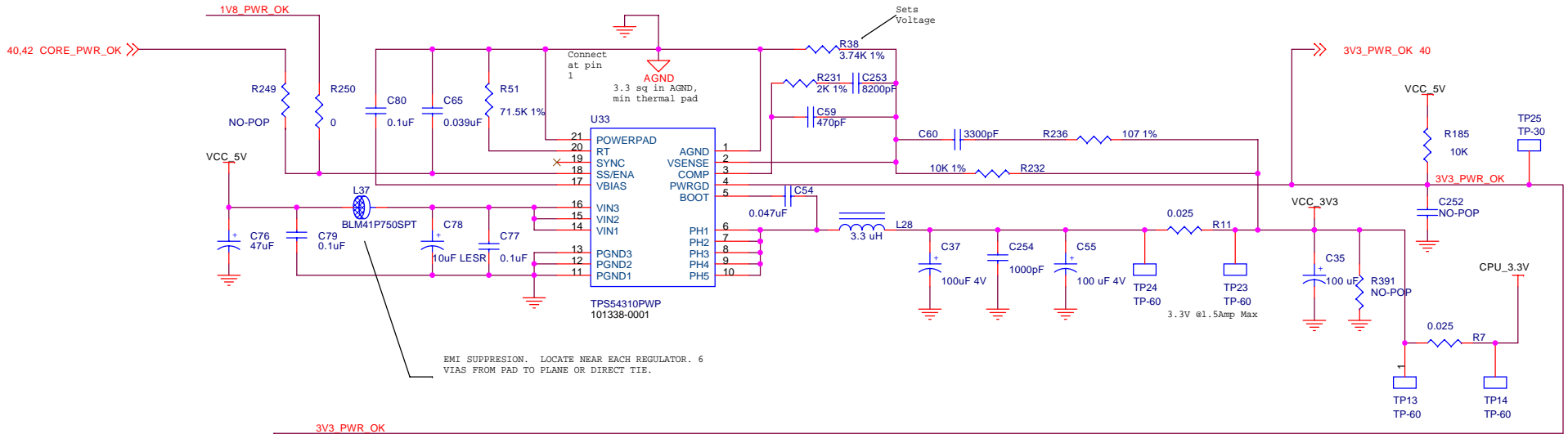
SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: I/O DAUGHTER CARD CONNECTOR			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010		Sheet 39 of 43



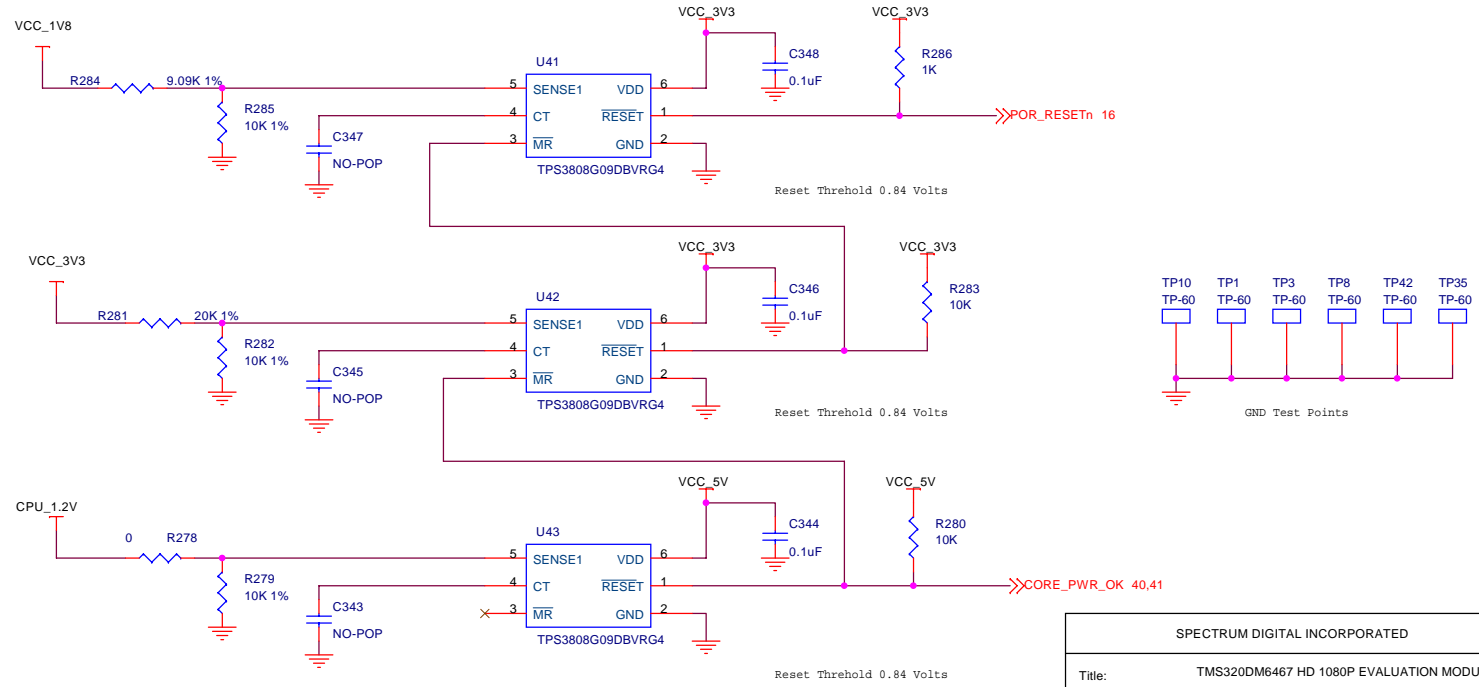
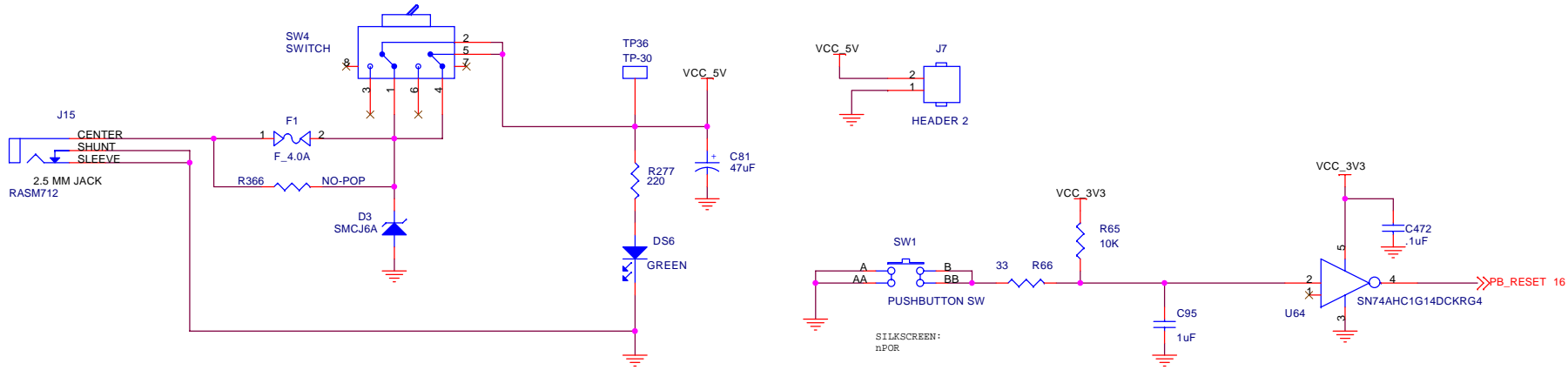
$$R_{set} = 10K \times (0.69/V_{out} - 0.69) - 1.43$$



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: DSP CORE POWER			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet	40 of 43



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: POWER 1.8V, 3.3V			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 41 of	43



SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: POWER INPUT			
Size: B	DWG NO	512022-0001	Revision: A
Date:	Saturday, August 21, 2010	Sheet 42 of	43

REVISION A - INITIAL RELEASE - JANUARY 15,2009

REVISION B - JUNE15,2009

FIXED PIN OUT ORDER ON VIDEO MULTIPLEXER

RE-ORDER COMPONENT OUT JACKS TO MATCH DIRECT CABLING

REVISION C - OCTOBER 15,2009

RE-ORDER INPUT CHANNELS ON THS7353 TO MATCH DM6467 (GREEN/COMPOSITE ON CHANNEL 2 OF THS7353)

SPECTRUM DIGITAL INCORPORATED			
Title: TMS320DM6467 HD 1080P EVALUATION MODULE			
Page Contents: REVISION HISTORY			
Size:B	DWG NO	512022-0001	Revision: C
Date:	Saturday, August 21, 2010	Sheet 43 of	43