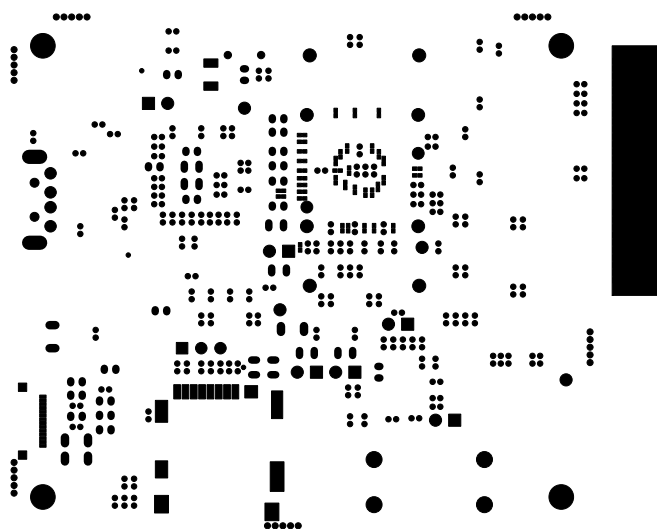
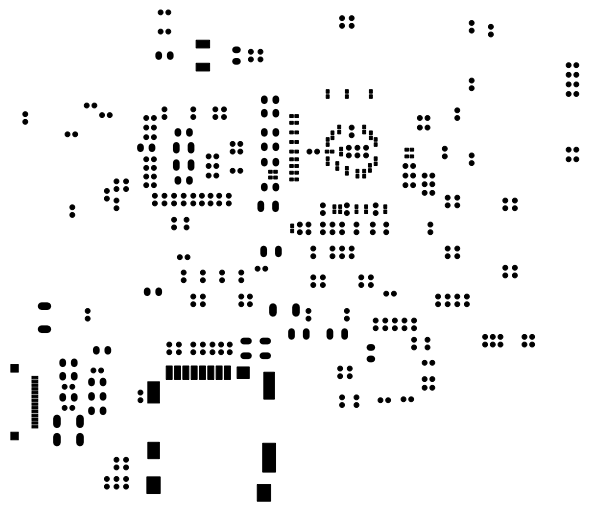


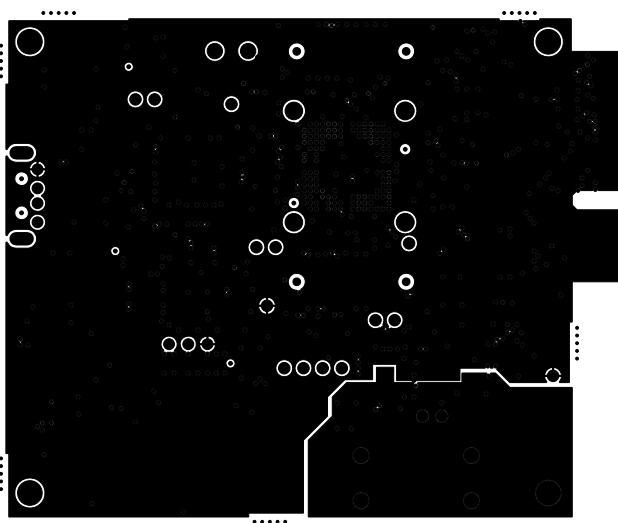
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	DATE : 7/22/11	



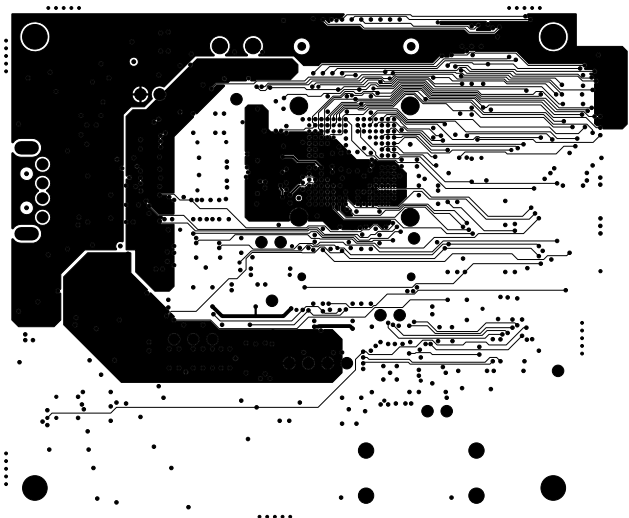
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	DATE : 7/22/11	



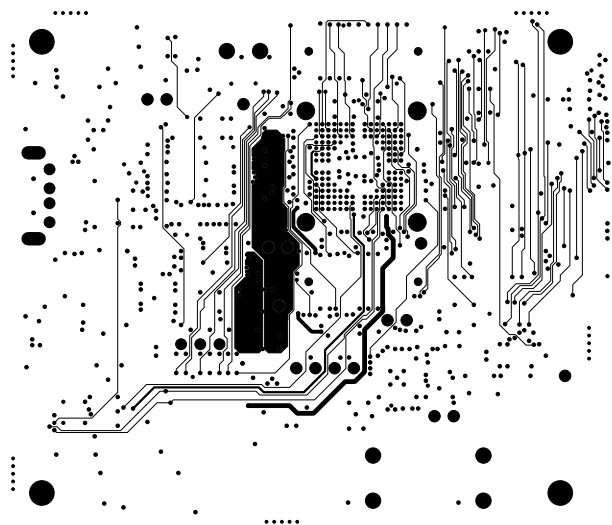
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	DATE : 7/22/11	



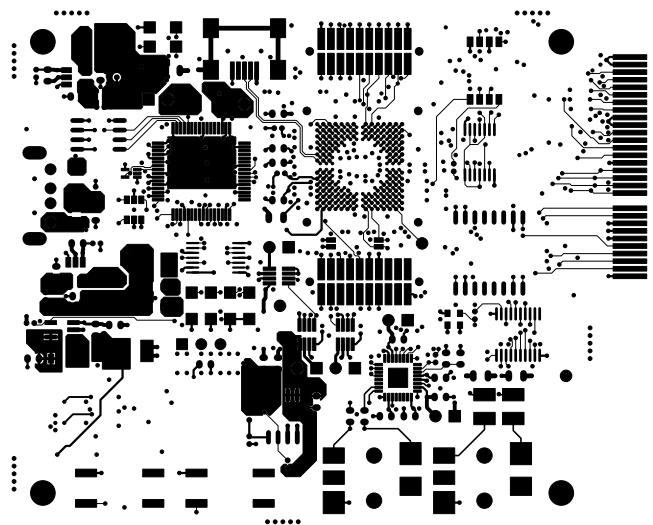
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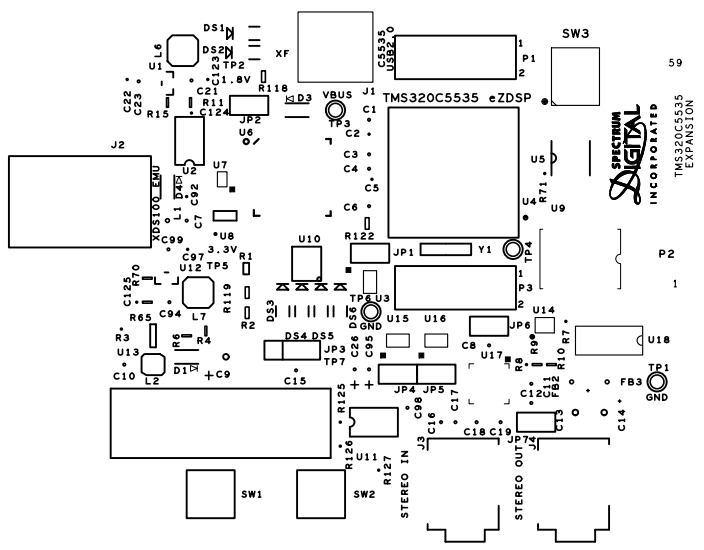
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	LAYER 3 ROUTING	514581 REV C
	DATE : 7/22/11	



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	DATE : 7/22/11	



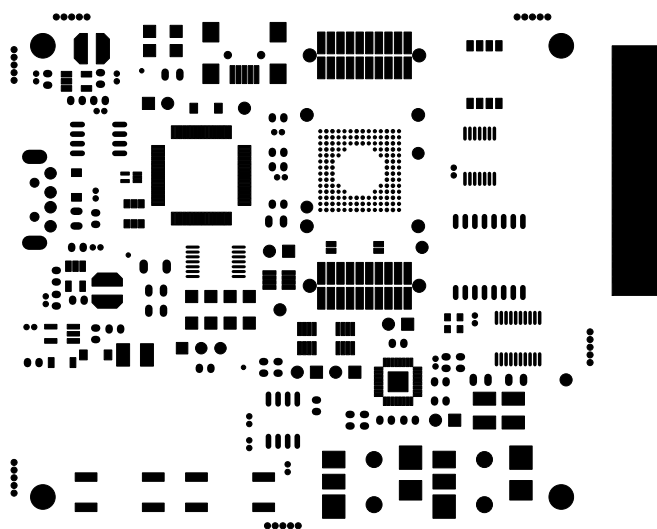
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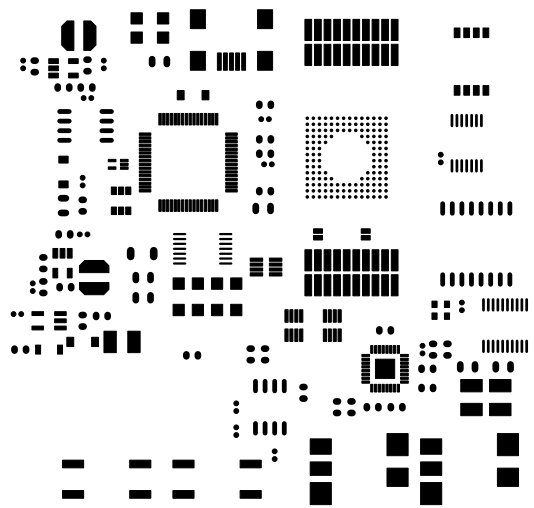

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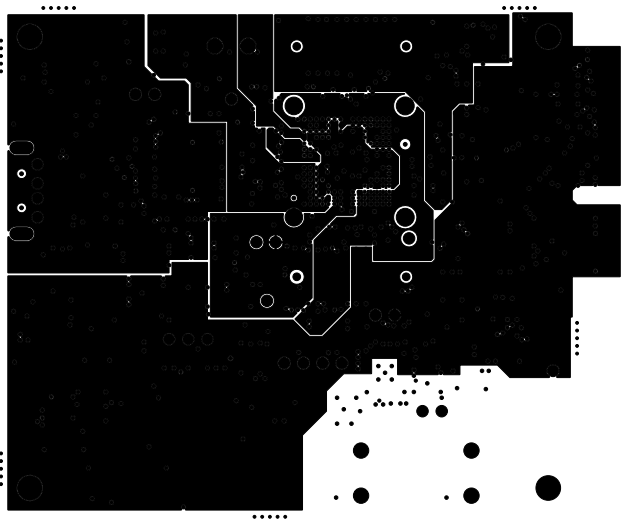
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	DATE : 7/22/11	



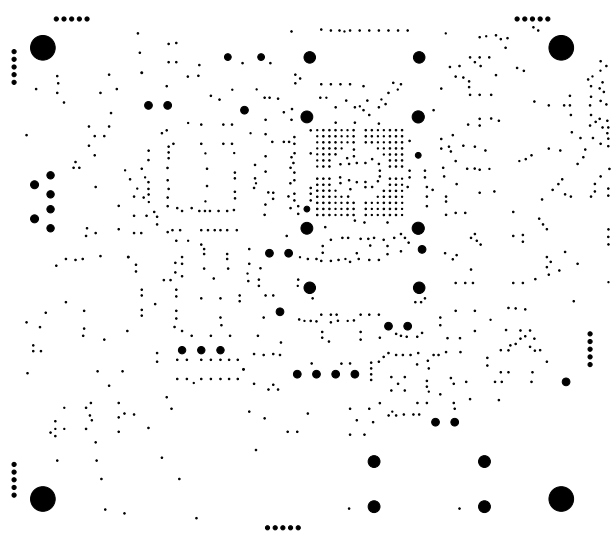
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	SMC	514581 REV C
	DATE : 7/22/11	



	SPECTRUM DIGITAL	
	SPC	514581 REV C
	DATE : 7/22/11	



	SPECTRUM DIGITAL	
	LAYER 5 POWER	514581 REV C
	DATE : 7/22/11	



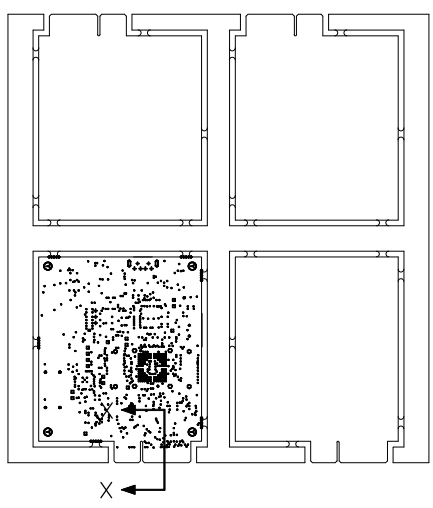
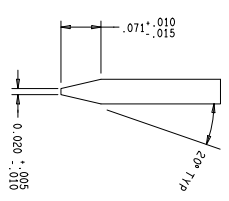


FIGURE	SIZE	ALL	DRILL	PLATE	NO. MILS	TOG.	NOTE
1	8.0	PLATED	789				
2	15.0	PLATED	3				
3	40.0	PLATED	16				
4	43.0	PLATED	2				
5	52.0	NON-PLATED	90				
6	52.0	NON-PLATED	2				
7	31.0	NON-PLATED	2				
8	43.0	NON-PLATED	4				
9	129.0	NON-PLATED	2				



VIEW X - X
SCALE: NONE

SPECTRUM DIGITAL
DRILL 514581 REV C
DATE: 7/22/11

- 5.1 MIL LINES ON LAYERS 1 AND 6 TO BE 90 OHMS DIFFERENTIAL.
- IMPEDANCE CONTROL TO 50 OHMS FOR 4 MIL TRACES.
- SURFACE FINISH SHALL BE IMMERSION GOLD (INCLUDING EDGE TABS).
- FINISHED BOARD SHALL BE FREE OF BURRS ON EDGES.
- PLATED THRU HOLE SIZE TOLERANCES SHALL BE +/- .003 INCH.
- UNPLATED THRU HOLE SIZE TOLERANCES SHALL BE +/- .002 INCH.
- SOLDERMASK ON BOTH SIDES OF THE BOARD SHALL BE S.M.O.B.C., LPT, COLOR GREEN.
- MINIMUM TRACE AND SPACE SHALL BE .004 OF AN INCH.
- COPPER PLATED SURFACE CONDUCTORS SHALL BE THE EQUIVALENT OF 1 OZ. COPPER AFTER PLATING. PLANE LAYERS SHALL BE 1 OZ. COPPER. AVERAGE COPPER ON THE WALL OF THE PLATED HOLE SHALL BE .001 INCH.
- TOTAL BOARD THICKNESS SHALL BE .062 +/- .007 INCHES.
- ALL MATERIAL SHALL BE NEMA GRADE FR-4 OR EQUIVALENT. (Tg>=170 DEG. C)

NOTES: UNLESS OTHERWISE SPECIFIED

DRILL CHART	
DATE	7/22/11
DESIGN ENGR	
PROJECT ENGR	
ENGR MGR	NEXT ASSEMBLY

CUSTOMER NAME	
APPROVED	
CHECKED	Tom K
DRAFTING	
ENGR	
PROJECT ENGR	
ENGR MGR	
DATE	7/22/11
DESIGN ENGR	
PROJECT ENGR	
ENGR MGR	

CUSTOMER NAME	
FABRICATION DRAWING	514581 REV C
SCALE	NONE
SHEET	1 OF 1

1 2 3 4 5 6 7 8 9 10 11